

# SMT Linear Integer Arithmetic proof checking in Lambdapi

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**Abstract.** Modern SMT solvers can generate proofs of unsatisfiability to ensure correctness independently of their implementation. A dependable approach to verify these proofs is to reconstruct them within a proof assistant. In previous work, the SMT checker Carcara was extended to reconstruct SMT proofs in Lambdapi — a proof assistant designed for interoperability, supporting the import and export of proofs for integration with other proof assistants such as Rocq, Lean, or HOL-Light. Nevertheless, that work was limited to non-arithmetic fragments. In this paper, we present an extension that enables the reconstruction, and thus independent verification of SMT proofs involving linear integer arithmetic.

**Keywords:** SMT · Alethe · LIA · Lambdapi · normal form · Proof by Reflection

## 1 Introduction

SMT solvers have become capable of producing proofs alongside their satisfiability results, enabling independent verification of correctness without relying on the solver’s implementation. This development addresses the growing need for trustworthy verification in safety-critical applications and SMT solver usage in proof assistant, where the solver cannot be assumed to be fully trusted. The Alethe [21, 2] is an established SMT proof format generated by the solvers cvc5 and veriT. In our previous work [9], we extended the Alethe proof checker and elaborator Carcara [1] to reconstruct Alethe SMT proofs within the Lambdapi proof assistant, thereby ensuring their validity. Lambdapi is an interactive proof assistant based on the  $\lambda II$ -calculus modulo rewriting, a logical framework that extends the  $\lambda$ -calculus with dependent types and user-defined rewrite rules. This foundation allows Lambdapi to serve as a framework for formalizing various logical systems. Designed with interoperability in mind, Lambdapi can import and export proofs, facilitating integration with other proof assistants such as Rocq, Lean, and HOL-Light.

However, our prior work was limited to proofs expressed within the logic of Uninterpreted Functions (UF) and Quantifier-Free (QF) fragments. In the present work, we extend this approach to support the reconstruction of proof steps involving linear integer arithmetic (LIA). When the propositional model is unsatisfiable in the theory of linear real/integer arithmetic, the solver creates a proof certificate. The conclusion is a tautological clause of linear inequalities

and equations and the justification of the step is a list of coefficients so that the linear combination is a trivially contradictory inequality after simplification (e.g.,  $0 \geq 1$ ). Most SMT solvers, including `cvc5` and `veriT`, use the simplex method [11] to handle linear arithmetic. To verify these certificates, we implemented an automatic decision procedure based on proof by reflection [8, 13].

In section 2, we provide an overview of `Lambdapi` and present the structure of Alethe proof certificates for linear arithmetic. Then, in section 3, we describe how we leverage Carcara’s elaboration process to reconstruct linear integer arithmetic steps, even when coefficient annotations are missing. In section 5 introduces our encoding of SMT linear arithmetic within `Lambdapi`, while in section 4 presents an automatic decision procedure, based on proof by reflection, to verify these arithmetic steps. An empirical evaluation of our approach is provided in section 6. We review related work in section 7, and we conclude in section 8.

## 2 Background

### 2.1 An Overview of `Lambdapi`

`Lambdapi` is an implementation of  $\lambda II$  modulo theory ( $\lambda II / \equiv$ ) [10], an extension of the Edinburgh Logical Framework  $\lambda II$  [14], a simply typed  $\lambda$ -calculus with dependent types.  $\lambda II / \equiv$  adds user-defined higher-order rewrite rules. Its syntax is given by

Universes	$u ::= \text{TYPE} \mid \text{KIND}$
Terms	$t, v, A, B, C ::= c \mid x \mid u \mid II\ x : A, B \mid \lambda x : A. t \mid t\ v$
Contexts	$\Gamma ::= \langle \rangle \mid \Gamma, x : A$
Signatures	$\Sigma ::= \langle \rangle \mid \Sigma, c : C \mid \Sigma, c := t : C \mid \Sigma, t \hookrightarrow v$

where  $c$  is a constant and  $x$  is a variable (ranging over disjoint sets),  $C$  is a closed term. *Universes* are constants used to verify if a type is well-formed – more details can be found in [14, §2.1].  $II\ x : A, B$  is the dependent product, and we write  $A \rightarrow B$  when  $B$  does not depend on  $x$ ,  $\lambda x : A. t$  is an abstraction, and  $t\ v$  is an application. A (local) context  $\Gamma$  is a finite sequence of variable declarations  $x : A$  introducing variables and their types. A signature  $\Sigma$  representing the global context is a finite sequence of *assumptions*  $c : C$ , indicating that constant  $c$  is of type  $C$ , *definitions*  $c := t : C$ , indicating that  $c$  has the value  $t$  and type  $C$ , and *rewrite rules*  $t \hookrightarrow v$  such that  $t = c\ v_1 \dots v_n$  where  $c$  is a constant.

The relation  $\hookrightarrow_{\beta\Sigma}$  is generated by  $\beta$ -reduction and by the rewrite rules of  $\Sigma$ . The relation  $\hookrightarrow_{\beta\Sigma}^*$  denotes the reflexive and transitive closure of  $\hookrightarrow_{\beta\Sigma}$ , and the relation  $\equiv_{\beta\Sigma}$  (called *conversion*) the reflexive, symmetric, and transitive closure of  $\hookrightarrow_{\beta\Sigma}$ . The relation  $\hookrightarrow_{\beta\Sigma}$  must be confluent, i.e., whenever  $t \hookrightarrow_{\beta\Sigma}^* v_1$  and  $t \hookrightarrow_{\beta\Sigma}^* v_2$ , there exists a term  $w$  such that  $v_1 \hookrightarrow_{\beta\Sigma}^* w$  and  $v_2 \hookrightarrow_{\beta\Sigma}^* w$ , and it must preserve typing, i.e., whenever  $\Gamma \vdash_{\Sigma} t : A$  and  $t \hookrightarrow_{\beta\Sigma} v$  then  $\Gamma \vdash_{\Sigma} v : A$  [5].

A `Lambdapi` typing judgment  $\Gamma \vdash_{\Sigma} t : A$  asserts that term  $t$  has type  $A$  in the context  $\Gamma$  and the signature  $\Sigma$ . The typing rules of  $\lambda II / \equiv$  are the one of

$\lambda\Pi$  [14, §2], except for the rule (Conv) where it use the version of fig. 1 that identifies types modulo  $\equiv_{\beta\Sigma}$  instead of just modulo  $\beta$ -reduction.

$$\frac{\Gamma, \vdash_{\Sigma} B : u \quad \Gamma \vdash_{\Sigma} t : A \quad A \equiv_{\beta\Sigma} B}{\Gamma \vdash_{\Sigma} t : B} \text{ (Conv)}$$

**Fig. 1.** (Conv) rule in  $\lambda\Pi/\equiv$

In our encoding presented [9] we employ Tarski-style universe [17, §Universes] where types are represented by elements of a base type and interpreted via the decoding function. We defined the constant `Prop` : `TYPE` for the type of proposition and the decoding function `Prfc` : `Prop` → `TYPE` that maps each proposition to `TYPE`. Since `Lambdapi` does not support quantify over a variable of type `TYPE`. More precisely, it is not possible to assign the type  $\Pi X : \text{TYPE}, (X \rightarrow \text{Prop}) \rightarrow \text{Prop}$  to the universal quantifier  $\forall$ . To circumvent this, we have the constant `Set` : `TYPE` for the types of object-terms, and a decoding function `E1` : `Set` → `TYPE` that embeds `Set` into `TYPE`. This permits us to define the quantifier as  $\forall : \Pi x : \text{Set}, (\text{E1 } x \rightarrow \text{Prop}) \rightarrow \text{Prop}$ . To quantify over propositions, we further define a constant `o` : `Set` and add the rewrite rule `E1 o`  $\hookrightarrow$  `Prop`.

## 2.2 Alethe proof

The Alethe proof trace format [2] for SMT solvers comprises two parts: the trace language based on SMT-LIB and a collection of proof rules. Traces witness proofs of unsatisfiability of a set of constraints. They are sequences  $a_1 \dots a_m t_1 \dots t_n$  where the  $a_i$  corresponds to the constraints of the original SMT problem being refuted, each  $t_i$  is a clause inferred from previous elements of the sequence, and  $t_n$  is  $\perp$  (the empty clause). In the following, we designate the SMT-LIB problem as the *input problem*.

```

1 (set-logic QF_LIA)
2 (declare-const x Int)
3 (declare-const y Int)
4 (assert (= 0 y))
5 (assert (= x 2))
6 (assert (or (< (+ x y) 1) (< 3 x)))
7 (get-proof)

```

**Listing 1.1.** Input problem

⚡

```

1 (assume a0 (or (< (+ x y) 1) (< 3 x)))
2 (assume a1 (= x 2))
3 (assume a2 (= 0 y))
4 (step t1 (c1 (< (+ x y) 1) (< 3 x)) :rule or :premises (a0))
5 (step t2 (c1 (not (< 3 x)) (not (= x 2))) :rule la_generic :args (1/1 1/1))
6 (step t3 (c1 (not (< 3 x))) :rule resolution :premises (a1 t2))

```

```

7 (step t4 (c1 (< (+ x y) 1)) :rule resolution :premises (t1 t3))
8 (step t5 (c1 (not (< (+ x y) 1)) (not (= x 2)) (not (= 0 y))) :rule
  la_generic :args (1/1 -1/1 1/1))
9 (step t6 (c1) :rule resolution :premises (t5 t4 a1 a2))

```

**Listing 1.2.** The following example is the proof for the unsatisfiability of  $(x + y < 1) \vee (3 < x), x = 2$  and  $0 = y$ .

We will use the input problem shown in the top part of listing 1.1 with its Alethe proof (found by `cvc5`) in the bottom part as a running example to provide an overview of Alethe concepts and to illustrate our reconstruction of linear arithmetic step in `Lambdapi`.

**Alethe Trace Format Overview** An Alethe proof trace inherits the declarations of its input problem. All symbols (sorts, functions, assertions, etc.) declared or defined in the input problem remain declared or defined, respectively. Furthermore, the syntax for terms, sorts, and annotations uses the syntactic rules defined in SMT-LIB [3, §3] and the SMT signature context defined in [3, §5.1 and §5.2]. In the following we will represent an Alethe step as

$$\begin{array}{c}
 \text{index} \uparrow \boxed{i} \cdot \quad \text{context} \uparrow \boxed{\Gamma} \triangleright \quad \text{clause} \downarrow \boxed{l_1 \dots l_n} \quad \left( \begin{array}{c} \text{rule} \uparrow \boxed{\mathcal{R}} \quad \text{premises} \uparrow \boxed{p_1 \dots p_m} \end{array} \right) \quad \text{arguments} \uparrow \boxed{[a_1 \dots a_r]} \quad (1)
 \end{array}$$

A step consists of an index  $i \in \mathbb{I}$  where  $\mathbb{I}$  is a countable infinite set of indices (e.g. `a0`, `t1`), and a clause of formulae  $l_1, \dots, l_n$  representing an  $n$ -ary disjunction. Steps that are not assumptions are justified by a proof rule  $\mathcal{R}$  that depends on a possibly empty set of premises  $\{p_1 \dots p_m\} \subseteq \mathbb{I}$  that only references earlier steps such that the proof forms a directed acyclic graph. A rule might also depend on a list of arguments  $[a_1 \dots a_r]$  where each argument  $a_i$  is either a term or a pair  $(x_i, t_i)$  where  $x_i$  is a variable and  $t_i$  is a term. The interpretation of the arguments is rule specific. The context  $\Gamma$  of a step is a list  $c_1 \dots c_l$  where each element  $c_j$  is either a variable or a variable-term tuple denoted  $x_j \mapsto t_j$ . Therefore, steps with a non-empty context contain variables  $x_j$  that appear in  $l_i$  and will be substituted by  $t_j$ . Proof rules  $\mathcal{R}$  include theory lemmas and `resolution`, which corresponds to hyper-resolution on ground first-order clauses.

add RARE rules  
in table 1

We now have the key components to explain the guiding proof in the bottom part of listing 1.2. The proofs starts with `assume` steps `a0`, `a1`, `a2` that restate the assertions from the *input problem* (listing 1.2). Step `t1` transforms disjunction into clause by using the Alethe rule `or`. Steps `t2` and `t5` are tautologies introduced by the main rule `la_generic` in Linear Real Arithmetic (LRA) logic and used also in LIA logic, where  $l_1, l_2, \dots, l_n$  represent linear inequalities. These logics use closed linear formulas over the `Real` signature and `Int` respectively. The `Real` terms in LRA logic are built over the Reals signature from SMT-LIB with free variables, but containing only linear atoms; that is atoms of the form  $d$ ,  $(* d x)$ , or  $(* x d)$  where  $x$  is a free variable and  $d$  is an integer or rational

Rule	Description
la_generic	Tautologous disjunction of linear inequalities.
lia_generic	Tautologous disjunction of linear integer inequalities.
la_disequality	$t_1 \approx t_2 \vee \neg(t_1 \geq t_2) \vee \neg(t_2 \geq t_1)$
la_totality	$t_1 \geq t_2 \vee t_2 \geq t_1$
la_mult_pos	$t_1 > 0 \wedge (t_2 \bowtie t_3) \rightarrow t_1 * t_2 \bowtie t_1 * t_3$ and $\bowtie \in \{<, >, \geq, \leq, =\}$
la_mult_neg	$t_1 < 0 \wedge (t_2 \bowtie t_3) \rightarrow t_1 * t_2 \bowtie_{inv} t_1 * t_3$
la_rw_eq	$(t \approx u) \approx (t \geq u \wedge u \geq t)$
comp_simplify	Simplification of arithmetic comparisons.

**Table 1.** Linear arithmetic rules in Alethe supported.

constant. Similarly, the **Int** terms in LIA logic are closed formulas built over the Ints signature with free variables, but whose terms are also all linear, such that there is no occurrences of the function symbols  $*$  (except variable multiplied by an **Int** constant),  $/$ , **div**, **mod**, and **abs**. A linear inequality is of term of the form

$$\sum_{i=0}^n c_i \times t_i + d_1 \bowtie \sum_{i=n+1}^m c_i \times t_i + d_2 \quad (1)$$

where  $\bowtie \in \{=, <, >, \leq, \geq\}$ , where  $m \geq n$ ,  $c_i, d_1, d_2$  are either **Int** or **Real** constants, and for each  $i$   $c_i$  and  $t_i$  have the same sort. Checking the clause validity of **t2** and **t5** in listing 1.2, amounts to checking the unsatisfiability of the system of linear equations (we provide more details in section 2.2) e.g.  $x < 3$  and  $x = 2$  in **t2**. A coefficient for each inequality are pass as arguments e.g.  $(\frac{1}{1}, \frac{1}{1})$  in **t2**. Steps **t3** (and **t4**) applies the **resolution** rule to the premises **a1**, **t2** (respectively **t1** **t3**). Finally, the step **t6** concludes the proof by generating the empty clause  $\perp$ , concretely denoted as (**c1**) in listing 1.2. Notice that the contexts  $\Gamma$  of each step are all empty in this proof.

**Linear arithmetic in Alethe** Proofs for linear arithmetic steps use a number of straightforward rules listed in table 1, such as **la\_totality**:  $(t_1 \leq t_2 \vee t_2 \geq t_1)$ .

Following our method to encode Alethe described in [9], the linear arithmetic tautology rules **la\_disequality**, **la\_totality** and **la\_mult\_\*** are encoded as lemmas in our embedding of Alethe in Lambdapi.

A different approach is taken for the primary rules **\*\_generic**, as they describe an algorithm. While **la\_generic** rule is primarily intended for LRA logic, it is also applied in LIA proofs when all variables in the (in)equalities are of integer sort. A step of the rule **la\_generic** represents a tautological clause of linear disequalities. It can be checked by showing that the conjunction of the negated disequalities is unsatisfiable. After the application of some strengthening rules, the resulting conjunction is unsatisfiable, even if **Int** variables are assumed to be **Real** variables. Although the rule may introduce rational coefficients, they often reduce to integers—as shown in listing 1.2, where the coefficients are  $(\frac{1}{1}, \frac{1}{1})$ . Cases where coefficients cannot be reduced to integers are rare in practice, how-

mention RARE  
rules

ever, we eliminate  $\cdot$ . Let  $\varphi_1, \dots, \varphi_n$  be linear inequalities and  $a_1, \dots, a_n$  rational numbers, then a **la\_generic** step has the general form

$$i. \triangleright \quad \varphi_1, \dots, \varphi_n \quad \mathbf{la\_generic} [a_1, \dots, a_n]$$

The constants  $a_i$  are of sort **Real**. To check the unsatisfiability of the negation of  $\varphi_1, \dots, \varphi_n$  one performs the following steps for each literal. For each  $i$ , let  $\varphi := \varphi_i$ ,  $a := a_i$  and we write  $s_1 \bowtie s_2$  to denotes the left and right side of an inequality of eq. (1).

1. If  $\varphi = \neg(s_1 < s_2)$  or  $s_1 \geq s_2$ , then let  $\varphi := \neg(-s_1 \geq -s_2)$ . If  $\varphi = \neg(s_1 \leq s_2)$  or  $s_1 > s_2$ , then let  $\varphi := \neg(-s_1 > -s_2)$ . If  $\varphi = s_1 < s_2$ , then let  $\varphi := \neg(s_1 \geq s_2)$ . If  $\varphi = s_1 \leq s_2$ , then let  $\varphi := \neg(s_1 > s_2)$ . This negates the literal. We want a canonical form that use only the operators  $>$ ,  $\geq$  and  $=$ .
2. Replace  $\varphi = \sum_{i=0}^n c_i \times t_i + d_1 \bowtie \sum_{i=n+1}^m c_i \times t_i + d_2$  by  $\sum_{i=0}^n c_i \times t_i - \sum_{i=n+1}^m c_i \times t_i \bowtie d_2 - d_1$ .
3. Now  $\varphi$  has the form  $s_1 \bowtie d$ . If all variables in  $s_1$  are integer sorted then replace  $\bowtie d$  by  $\bowtie \lceil d \rceil$ , otherwise replace by  $\bowtie \lfloor d \rfloor + 1$ .
4. If all variables of  $\varphi$  are **Int** and coefficient  $a_1 \dots a_n \in \mathbb{Q}$ , then  $a_i := a \times lcd(a_1 \dots a_n)$  where  $lcd$  is the least common denominator of  $[a_1 \dots a_n]$ .
5. If  $\bowtie$  is  $=$ , then replace  $\varphi$  by  $\sum_{i=0}^m a \times c_i \times t_i = a \times d$ , otherwise replace it by  $\sum_{i=0}^m |a| \times c_i \times t_i \bowtie |a| \times d$ .
6. Finally, the sum of the resulting literals is trivially contradictory. The sum

$$\sum_{k=1}^n \sum_{i=1}^m c_i^k * t_i^k \bowtie \sum_{k=1}^n d^k$$

where  $c_i^k$  and  $t_i^k$  are the constant and term from the literal  $\varphi_k$ , and  $d^k$  is the constant  $d$  of  $\varphi_k$ . The operator  $\bowtie$  is  $=$  if all operators are  $=$ ,  $>$  if all are either  $=$  or  $>$ , and  $\geq$  otherwise. Finally, the sum on the left-hand side is 0 and the right-hand side is  $> 0$  (or  $\geq 0$  if  $\bowtie$  is  $>$ ).

The step 1 has been added by our own, as the subsequent steps in the original algorithm are designed for  $>$  and  $\geq$  and do not clearly address how to handle  $<$  and  $\leq$ . Additionally, step 6 was added to ensure that our construction is independent of  $\mathbb{Q}$ .

*Example 1.* Consider the **la\_generic** step in the logic **QF\_UFLIA** with the uninterpreted function symbol (**f Int**):

```
1 (step t11 (c1 (not (<= f 0)) (<= (+ 1 (* 4 f)) 1))
2 :rule la_generic :args (1/1 1/4))
```

the algorithm run as follow in a natural deduction:

$$\begin{array}{ll}
\vdash \neg(-f > 0), \neg(4f > 0) & \text{(Step 2)} \\
\vdash \neg(-f > 0), \neg(4f \geq 1) & \text{(Step 3)} \\
\text{Replace } a = [\frac{1}{1}, \frac{1}{4}] \text{ by } a = [4, 1] & \text{(Step 4)} \\
\vdash \neg(|4| * -f > |4| * 0), \neg(|1| * 4f \geq |1| * 1) & \text{(Step 5)} \\
-4f + 4f \geq 1 \vdash \text{False} & \text{(Step 6)}
\end{array}$$

Which sums to the contradiction  $0 \geq 1$ .

The `lia_generic` is structurally similar to `la_generic`, but omits the coefficients. Since this rule can introduce a disjunction of arbitrary linear integer inequalities without any additional hints, proof checking is *NP-complete* [20].

Je met  $\vdash$  car je trouve que la dérivation ce comprend mieu de mon point de vue. Mais je peux l'enlever si trop de confusion est ajouté.

### 3 The approach to reconstruct `lia_generic` step

The `lia_generic` represent a challenge for reconstruction because the coefficients are not provided by the solver in the trace i.e. `[a1...ar]` is empty. We decided to leverage the elaboration process of `lia_generic` performed by Carcara, as doing otherwise would require implementing Fourier-Motzkin elimination for integers, as demonstrated in [19, 4] - and therefore reimplementing the work done by the solver.

Carcara considers `lia_generic` steps as holes in the proof, as "their checking is as hard as solving" [1, §3.2]. However, Carcara relies on an external tool that generates Alethe proofs to formulate the steps by solving corresponding problems in a proof-producing manner. The proof is then imported, and validated before replacing the original step. However, at present, Carcara only use `cvc5` for performing this task. In detail, the elaboration method, when encountering a `lia_generic` step `S` concluding the negated inequalities  $\neg l_1 \vee \dots \vee \neg l_n$ , generates an SMT-LIB problem asserting  $l_1 \wedge \dots \wedge l_n$  and invokes `cvc5` on it, expecting an Alethe proof  $\pi : (l_1 \wedge \dots \wedge l_n) \rightarrow \perp$  that do not use `lia_generic`. Carcara will check this subproof and then replace step `S` in the original proof by a proof of the form:

```
1 (step S (cl (not l1) ... (not ln)) :rule lia_generic)
```

4

```
1 (anchor :step S.t_m+1)
2 (assume S.h_1 l1)
3 ...
4 (assume S.h_n ln)
5 ...
6 (step t.t_m (cl false) :rule ...)
7 (step t.t_m+1 (cl (not l1) ... (not ln) false) :rule subproof)
8 (step t.t_m+2 (cl (not false)) :rule false)
```

```

9 | (step S (cl (not l1) ... (not ln)) :rule resolution :premises (S.t_m+1 S.t_m
+2))

```

Listing 1.3. Elaboration of `lia_generic`

In the next section, we first present an overview of our embedding of Alethe in Lambdapi, and then our automatic procedure to reconstruct `la_generic` step that appear in LIA problem.

## 4 Reconstruction of `la_generic` step for LIA logic

### 4.1 Encoding of Integers in Lambdapi

$\mathbb{P} : \text{TYPE}$	$\mathbb{Z} : \text{TYPE}$	$\text{Comp} : \text{TYPE}$	$\mathbb{B} : \text{TYPE}$
$H : \mathbb{P}$	$Z0 : \mathbb{Z}$	$\text{Eq} : \text{Comp}$	$\text{true} : \mathbb{B}$
$0 : \mathbb{P} \rightarrow \mathbb{P}$	$Z\text{Pos} : \mathbb{P} \rightarrow \mathbb{Z}$	$\text{Lt} : \text{Comp}$	$\text{false} : \mathbb{B}$
$1 : \mathbb{P} \rightarrow \mathbb{P}$	$Z\text{Neg} : \mathbb{P} \rightarrow \mathbb{Z}$	$\text{Gt} : \text{Comp}$	
$\text{pos} : \text{Set}$	$\text{int} : \text{Set}$	$\text{comp} : \text{Set}$	$\text{bool} : \text{Set}$
$\text{El pos} \hookrightarrow \mathbb{P}$	$\text{El int} \hookrightarrow \mathbb{Z}$	$\text{El comp} \hookrightarrow \text{Comp}$	$\text{El bool} \hookrightarrow \mathbb{B}$

Fig. 2. Overview of inductive types for integers and manipulating them

The definition we use of integers in Lambdapi in fig. 2 follows a common encoding found in many other theories, including the one adopted in the Rocq standard library [22]. First, the type  $\mathbb{P}$  is an inductive type representing strictly positive integers in binary form. Starting from 1 (represented by constructor  $H$ ), one can add a new least significant digit via the constructor  $0$  (digit 0) or constructor  $1$  (digit 1). The type  $\mathbb{Z}$  is an inductive type representing integers in binary form. An integer is either zero (with constructor  $Z0$ ) or a strictly positive number  $Z\text{pos}$  (coded as a  $\mathbb{P}$ ) or a strictly negative number  $Z\text{neg}$  (whose opposite is stored as a  $\mathbb{P}$  value). To enable quantification over integers, positive binary numbers, booleans, and comparison results, we introduce constants such as  $\text{int} : \text{Set}$  that represents codes for these types along with a rewrite rule that rewrite code to its corresponding type, for example  $\text{El int} \hookrightarrow \mathbb{Z}$ . The comparison inductive type  $\text{Comp}$  is used to specify comparison function. The infix function  $\doteq$  define a decidable order comparison between two terms of type  $\mathbb{Z}$ . Similarly for  $\mathbb{P}$  with the infix function  $\text{cmp} : \mathbb{P} \rightarrow \mathbb{P} \rightarrow \text{Comp}$ .

The arithmetic operator such as  $+$  are constants defined by rewriting rules. In the following sections, we will refers to the rewriting rules for integers as  $\rightarrow_{\mathbb{Z}}$  and positive binary numbers as  $\rightarrow_{\mathbb{P}}$ . The confluence of the rewriting rules for the arithmetic of  $\mathbb{Z}$  and  $\mathbb{P}$  has been proven using CSI [23]. A detailed proof of confluence can be found in appendix B.1. The inequality symbols for  $\mathbb{Z}$  are



$+$ : $\mathbb{Z} \rightarrow \mathbb{Z} \rightarrow \mathbb{Z}$ $Z0 + y \hookrightarrow y$ $x + Z0 \hookrightarrow x$ $(Zpos\ x) + (Zpos\ y) \hookrightarrow (Zpos\ (add\ x\ y))$ $(Zpos\ x) + (Zneg\ y) \hookrightarrow (sub\ x\ y)$ $(Zneg\ x) + (Zpos\ y) \hookrightarrow (sub\ y\ x)$ $(Zneg\ x) + (Zneg\ y) \hookrightarrow Zpos(add\ x\ y)$	$\doteq$ : $\mathbb{Z} \rightarrow \mathbb{Z} \rightarrow Comp$ $Z0 \doteq Z0 \hookrightarrow Eq$ $Z0 \doteq Zpos\ \_ \hookrightarrow Lt$ $Z0 \doteq Zneg\ \_ \hookrightarrow Gt$ $Zpos\ \_ \doteq Z0 \hookrightarrow Gt$ $Zpos\ p \doteq Zpos\ q \hookrightarrow cmp\ p\ q$ $Zpos\ \_ \doteq Zneg\ \_ \hookrightarrow Gt$ $Zneg\ \_ \doteq Z0 \hookrightarrow Lt$ $Zneg\ \_ \doteq Zpos\ \_ \hookrightarrow Lt$ $Zneg\ p \doteq Zneg\ q \hookrightarrow cmp\ q\ p$
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$isEq : Comp \rightarrow \mathbb{B}$ $isEq\ Eq \hookrightarrow true$ $isEq\ Lt \hookrightarrow false$ $isEq\ Gt \hookrightarrow false$	$isLt : Comp \rightarrow \mathbb{B}$ $isLt\ Eq \hookrightarrow false$ $isLt\ Lt \hookrightarrow true$ $isLt\ Gt \hookrightarrow false$	$isGt : Comp \rightarrow \mathbb{B}$ $isGt\ Eq \hookrightarrow false$ $isGt\ Lt \hookrightarrow false$ $isGt\ Gt \hookrightarrow true$
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$\leq$ : $\mathbb{Z} \rightarrow \mathbb{Z} \rightarrow Prop := \lambda x, \lambda y, \neg(istrue(isGt(x \doteq y)))$ $<$ : $\mathbb{Z} \rightarrow \mathbb{Z} \rightarrow Prop := \lambda x, \lambda y, (istrue(isLt(x \doteq y)))$ $\geq$ : $\mathbb{Z} \rightarrow \mathbb{Z} \rightarrow Prop := \lambda x, \lambda y, \neg(x < y)$ $>$ : $\mathbb{Z} \rightarrow \mathbb{Z} \rightarrow Prop := \lambda x, \lambda y, \neg(x \leq y)$	$istrue : \mathbb{B} \rightarrow Prop$ $istrue\ true \hookrightarrow \top$ $istrue\ false \hookrightarrow \perp$
---	--

**Fig. 3.** Decidable equality, + operator and inequalities relations definitions for  $\mathbb{Z}$

binary predicates defined by rewriting rules over the decidable equality  $\doteq$ . They reduce to  $\top$ ,  $\perp$  (or negated) by  $\equiv_{\beta\Sigma}$  with rules of  $\rightarrow_{\mathbb{Z}}$  and  $\rightarrow_{\mathbb{P}}$ . For example,  $1 < 2 \hookrightarrow \text{istrue}(\text{isLt}(1 \doteq 2)) \hookrightarrow \text{istrue}(\text{isLt}(\text{Lt})) \hookrightarrow \text{istrue}(\text{true}) \hookrightarrow \top$ .

## 4.2 Functions used in the translation

We now outline the encoding of arithmetic expressions from SMT-LIB [3, §5.2.1]. This extends the approach introduced in [9] to handle arithmetic constructs. To avoid notational conflicts with the Lambdapi signature  $\Sigma$ , we denote the set of SMT-LIB sorts as  $\Theta^S$ , the set of function symbols  $\Theta^F$ , and the set of variables  $\Theta^X$ . Our translation is based on the following functions:

- $\mathcal{D}$  translates declarations of sorts and functions in  $\Theta^S$  and  $\Theta^F$  into constants,
- $\mathcal{S}$  maps sorts to  $\Sigma$  types,
- $\mathcal{E}$  translates SMT expression to  $\lambda\Pi/\equiv$  terms,
- $\mathcal{C}$  translates a list of commands  $c_1 \dots c_n$  of the form  $i. \Gamma \triangleright \varphi (\mathcal{R} P)[A]$  to typing judgments  $\Gamma \vdash_{\Sigma} i := M : N$ .

**Definition 1 (Function  $\mathcal{D}$  translating SMT sort and function symbol declarations).** *For each sort symbol  $s$  with arity  $n$  in  $\Theta^S$  we create a constant  $s : \text{Set}^0 \rightarrow \dots \rightarrow^{n-1} \text{Set}$ . For each function symbol  $f \sigma^+$  in  $\Theta^F$  we create a constant  $f : \mathcal{S}(\sigma^+)$ .*

**Definition 2 (Function  $\mathcal{S}$  translating sorts of expression).** *The definition of  $\mathcal{S}(s)$  is as follows.*

- Case  $s = \text{Bool}$ , then  $\mathcal{S}(s) = \text{El } o$ ,
- Case  $s = \text{Int}$ , then  $\mathcal{S}(s) = \text{El } \text{int}$ ,
- Case  $s = \sigma_1 \sigma_2 \dots \sigma_n$  then  $\mathcal{S}(s) = \text{El}(\mathcal{S}(\sigma_1) \rightsquigarrow \dots \rightsquigarrow \mathcal{S}(\sigma_n))$ ,
- otherwise  $\mathcal{S}(s) = \text{El } \mathcal{D}(s)$ .

**Definition 3 (Function  $\mathcal{E}$  translating SMT expressions).** *The definition of  $\mathcal{E}(e)$  is as follows.*

- Case  $e = (+ \ t_1 \dots t_n)$ , then  $\mathcal{E}(e) = \mathcal{E}(t_1) + \dots + \mathcal{E}(t_n)$ .
- Case  $e = (* \ t_1 \dots t_n)$ , then  $\mathcal{E}(e) = \mathcal{E}(t_1) * \dots * \mathcal{E}(t_n)$ .
- Case  $e = (-t)$ , then  $\mathcal{E}(e) = \sim \mathcal{E}(t_1)$ .
- Case  $e = (- \ t_1 \dots t_n)$ , then  $\mathcal{E}(e) = \mathcal{E}(t_1) - \dots - \mathcal{E}(t_n)$ .
- Case  $e = (\approx \ t_1 \ t_2)$  then  $\mathcal{E}(e) = (\mathcal{E}(t_1) = \mathcal{E}(t_2))$ .
- Case  $e = (x : \sigma)$  with  $x \in \Theta^X$  a sorted variable, then  $\mathcal{E}(e) = x : \mathcal{S}(\sigma)$ .

We defined the function  $\text{Prf}^\bullet : \text{Clause} \rightarrow \text{TYPE}$ , mapping each clause  $c$  to the type  $\text{Prf}^\bullet c$  of its proofs. The type  $\text{Clause} : \text{TYPE}$  represent the type of clause encoded as list [9, §3] with the constructor  $\forall : \text{Prop} \rightarrow \text{Clause} \rightarrow \text{Clause}$  and the empty clause  $\blacksquare : \text{clause}$ . The function  $\mathcal{C}$  encodes each step by invoking the functions  $\mathcal{D}$ ,  $\mathcal{S}$  and  $\mathcal{E}$ , and provides a proof term corresponding to the Alethe rule applied at that step.

*Example 2.* The translation of the steps t2 and t5 in listing 1.2 with the input problem definitions give us:

```

1 symbol x: El int;
2 symbol y: El int;
3 ...
4 opaque symbol t2: Prf• (¬ (3 < x) ∨ ¬ (x = 2) ∨ ■) := begin ... end;
5 opaque symbol t5: Prf• (¬ (x + y < 1) ∨ (¬ (x = 2)) ∨ (¬ (0 = y)) ∨ ■) :=
6 begin ... end;

```

The proof terms generated by  $\mathcal{C}$  for steps t2 and t5 must faithfully represent the algorithm presented in section 2.2. While steps 1 through 7 of the algorithm correspond to explicit rewriting steps, the final step (step 8) — which involves summing all inequalities — represents a multi-step rewriting sequence. This sequence reduced the initial sum to a decidable comparison between constants (e.g.  $0 > 1 \hookrightarrow \perp$ ), which serves as the conclusion of the reduction.

The reflection technique introduced by [8] leverages the reduction system of the proof assistant to produce an efficient decidable automatic procedure for solving arithmetic goals over  $\mathbb{Q}, \mathbb{R}$  and  $\mathbb{Z}$ . We decided to follow this approach to implement our decision procedure for evaluating inequality. In the following section, we describe how we implemented this procedure for our case, and how we extended the definition of  $\mathcal{C}$ .

## 5 Reconstruction of linear arithmetic for LIA logic

Proof by reflection is a technique used to write certified automation procedure by reducing the validity of a logical statement to a symbolic computation. It relies on the following definitions: let  $P : Z \rightarrow \text{Prop}$  be a predicate over a data type  $Z$  and we have a function  $f : Z \rightarrow \text{bool}$  such that the following theorem holds:

$$\mathbf{f\_correct} : \forall z : Z, (f\ z = \text{true}) \rightarrow (P\ z)$$

If  $f\ z$  reduces to **true**, then the proof term  $\mathbf{f\_correct}\ z\ (\mathbf{refl\ bool\ true})$  with  $\mathbf{refl} : \Pi A : \text{Set}, \Pi x : \text{El}\ A, \text{Prf}^c(x = x)$ , constitutes a proof of predicate  $(P\ z)$ . In step 6 of the `la_generic`, the primary challenge lies in reasoning modulo associativity and commutativity when manipulating expressions over  $\mathbb{Z}$ . The key idea is to provide a normalization function that transforms a  $\mathbb{Z}$  expression into a canonical form, such that it can be reduce to a constant because variables will cancel each other, as is the case with  $f$  in example 1.

### 5.1 Representation

The procedure is based on an algebraic group structure, denoted as  $\mathbb{G}$  defined in fig. 4 which represents linear polynomials. The base type for the elements of this group is specified as  $\mathbb{G} : \text{TYPE}$ . The unary operator `cst` denotes a constant from  $\mathbb{Z}$ . A `var` constructor for "catch-all" case for subexpressions that we cannot

model. These subexpressions will correspond to actual variables in  $\mathbb{Z}$ . The constructor `mul` represents the multiplication of an element of  $\mathbb{G}$  by a constant. The constructor `opp` corresponds to the inverse operator within the group. Lastly, the constructor `add` represent the addition between two elements of fig. 4.

To support associative and commutative operations, `Lambdapi` provides the modifiers `associative commutative symbol`, ensuring that terms are systematically placed into a canonical form given a builtin ordering relation, as described in [7] and [6, §5]. Applying the `associative commutative` modifiers to the infix constructor  $\oplus$ , ensures that expressions involving sums of products are systematically canonicalized. Thus, equal variables are placed next to each other, facilitating simplification.

$\mathbb{G} : \text{TYPE}$	$\uparrow : \mathbb{Z} \rightarrow \mathbb{G}$	$\Downarrow : \mathbb{G} \rightarrow \mathbb{Z}$
$  \oplus : \mathbb{G} \rightarrow \mathbb{G} \rightarrow \mathbb{G}$	$\uparrow \text{Z0} \hookrightarrow (\text{cst } \text{Z0})$	$\Downarrow (\text{cst } c) \hookrightarrow c$
$  \text{var} : \mathbb{Z} \rightarrow \mathbb{Z} \rightarrow \mathbb{G}$	$\uparrow \text{ZPos } c \hookrightarrow (\text{cst } c)$	$\Downarrow \text{opp } x \hookrightarrow \sim (\Downarrow x)$
$  \text{mul} : \mathbb{Z} \rightarrow \mathbb{G} \rightarrow \mathbb{G}$	$\uparrow \text{ZNeg } c \hookrightarrow (\text{cst } c)$	$\Downarrow \text{mul } c \ x \hookrightarrow c \times (\Downarrow x)$
$  \text{opp} : \mathbb{G} \rightarrow \mathbb{G}$	$\uparrow (x + y) \hookrightarrow (\uparrow x) \oplus (\uparrow y)$	$\Downarrow x \oplus y \hookrightarrow (\Downarrow x) + (\Downarrow y)$
$  \text{cst} : \mathbb{Z} \rightarrow \mathbb{G}$	$\uparrow (\sim x) \hookrightarrow \text{opp } \uparrow x$	$\Downarrow (\text{var } c \ x) \hookrightarrow c \times x$
<code>grp</code> : <code>Set</code>	$\uparrow (\text{Zpos } c) * x \hookrightarrow \text{mul } c (\uparrow x)$	
<code>El</code> <code>grp</code> $\hookrightarrow \mathbb{G}$	$\uparrow (\text{ZNeg } c) * x \hookrightarrow \text{mul } c (\uparrow x)$	
	$\uparrow x * (\text{Zpos } c) \hookrightarrow \text{mul } c (\uparrow x)$	
	$\uparrow x * (\text{ZNeg } c) \hookrightarrow \text{mul } c (\uparrow x)$	
	$\uparrow x \hookrightarrow (\text{var } 1 \ x)$	

**Fig. 4.** Definition of  $\mathbb{G}$  Algebra and its reification ( $\uparrow$ ) and denotation ( $\Downarrow$ ) functions

## 5.2 Normalization

For `associative commutative` symbols, `Lambdapi` does not use matching modulo AC [18, 16] as this problem is NP-complete [15]. Instead, `Lambdapi` transforms sum expression of the form  $(\text{var } c_1 \ a_1) \oplus (\text{var } c_2 \ a_2) \oplus \dots \oplus (\text{var } c_n \ a_n)$  into a canonical form, ensuring that any pair of terms  $(\text{var } p \ x)$  and  $(\text{var } q \ x)$  involving the same variable  $x$  are placed adjacent to each other. In this way, we will use the rewriting rules *fig. 5* to reduce expressions in canonical form. Notably, the resulting normal forms do not contain the constructors `mul` and `opp`, as the associated rewrite rules eliminate them in favor of `var`, `add` and `cst`.

**Definition 4 (AC-canonical form).** *Let  $\leq$  be a total order on  $\mathbb{G}$ -terms defined as follows: Constants are ordered such that  $\text{cst}(c_1) \leq \text{cst}(c_2) < (\text{var } p \ x)$  for any constants  $c_1, c_2$  and any variable term  $(\text{var } p \ x)$ , with  $c_1 \leq c_2$ . For variable*

$$\begin{aligned}
&(\text{var } x \ c_1) \oplus (\text{var } x \ c_2) \hookrightarrow \text{var } x \ (c_1 + c_2) \\
&(\text{var } x \ c_1) \oplus ((\text{var } x \ c_2) \oplus y) \hookrightarrow (\text{var } x \ (c_1 + c_2)) \oplus y \\
&(\text{cst } c_1) \oplus (\text{cst } c_2) \hookrightarrow (\text{cst } c_1 + c_2) \\
&(\text{cst } c_1) \oplus ((\text{cst } c_2) \oplus y) \hookrightarrow (\text{cst } c_1 + c_2) \oplus y \\
&(\text{cst } 0) \oplus x \hookrightarrow x \\
&x \oplus (\text{cst } 0) \hookrightarrow x \\
&\text{opp } (\text{var } x \ c) \hookrightarrow (\text{var } x \ (-c)) \\
&\text{opp } (\text{cst } c) \hookrightarrow (\text{cst } (-c)) \\
&\text{opp opp } x \hookrightarrow x \\
&\text{opp } x \oplus y \hookrightarrow (\text{opp } x) \oplus (\text{opp } y) \\
&\text{mul } k \ (\text{var } x \ c) \hookrightarrow (\text{var } x \ (k * c)) \\
&\text{mul } k \ \text{opp } x \hookrightarrow \text{mul } (-k) \ x \\
&\text{mul } k \ (x \oplus y) \hookrightarrow (\text{mul } k \ x) \oplus (\text{mul } k \ y) \\
&\text{mul } k \ (\text{cst } c) \hookrightarrow (\text{cst } (k * c)) \\
&\text{mul } c_1 \ (\text{mul } c_2 \ x) \hookrightarrow \text{mul } (c_1 * c_2) \ x
\end{aligned}$$

Fig. 5. Rewrite system on canonical forms

terms,  $(\text{var } p \ x) \leq (\text{var } q \ y)$  if either  $x < y$ , or  $x = y$  and  $p \leq q$ . Let  $\rightarrow^{AC}$  be the relation mapping every term  $t$  to its unique AC-canonical form denoted  $[t]$ .

Two terms are AC-equivalent iff their AC-canonical forms are equal.

**Definition 5 (Rewriting modulo AC-canonicalization).** Let  $\rightarrow_{\Sigma}^{AC} = \hookrightarrow_{\Sigma} \rightarrow^{AC}$ , where  $\Sigma$  contains the rewrite rules of figs. 3 and 5.

An  $\rightarrow_{\Sigma}^{AC}$  step is a standard  $\hookrightarrow_{\Sigma}$  step with syntactic matching followed by ACcanonicalization. We now prove that the relation  $\rightarrow_{\Sigma}^{AC}$  terminates and is confluent.

**Lemma 1.** *The matching modulo AC relation  $\rightarrow_{\Sigma/AC} = \simeq_{AC} \hookrightarrow_{\Sigma} \simeq_{AC}$ , which contains  $\rightarrow_{\Sigma}^{AC}$ , terminates.*

*Proof.* AProVE [12] automatically proves the termination of  $\rightarrow_{\Sigma/AC}$ .

**Lemma 2.**  $\rightarrow_{\Sigma}^{AC}$  is locally confluent on AC-canonical.

*Proof.* We show that every critical pair is joinable using  $\rightarrow_{\Sigma}^{AC}$  and confluence of  $\rightarrow_{\mathbb{Z}}$  and  $\rightarrow_{\mathbb{P}}$ .

To compare two  $\mathbb{Z}$ -terms,  $t_1$  and  $t_2$  wrt  $\rightarrow_{\Sigma}^{AC}$ , the procedure involves reifying them into their corresponding  $\mathbb{G}$ -terms, denoted  $[g_1]$  and  $[g_2]$ , using the reification function  $\uparrow(\_)$ , and put in normal forms. Following the reduction rules specified

in fig. 5, we can then compare their corresponding  $\mathbb{Z}$ -terms by applying the denotation function  $\Downarrow (\_)$ . To validate this procedure, it is necessary to establish the correctness of the following diagram:

$$\begin{array}{ccccc}
 \Downarrow (\Uparrow (t_1)) =_{\mathbb{Z}} \Downarrow (\Uparrow (t_2)) & & \begin{array}{ccc} \mathbb{G} & \xrightarrow{[\_]} & \mathbb{G} \\ \Downarrow (\Uparrow (\_)) \uparrow & \lrcorner & \downarrow \Downarrow (\_) \\ \mathbb{Z} & \dots \iff \dots & \mathbb{Z} \end{array} & & \Downarrow ([g_1]) =_{\mathbb{Z}} \Downarrow ([g_2]) \\
 t_1 =_{\mathbb{Z}} t_2 & & & & \Downarrow g_1 =_{\mathbb{Z}} \Downarrow g_2
 \end{array}$$

by the correctness theorem:

**Theorem 1 (Normalization correctness).** *For all  $\mathbb{G}$ -terms  $t$ , we have  $(\Downarrow [t]) = (\Downarrow t)$  where  $[t]$  is the AC-canonical form of  $t$  with respect to  $\longrightarrow_{\Sigma}^{AC}$ .*

We make use of lemma 3 to embed  $\mathbb{Z}$ -terms into  $\mathbb{G}$  in order to normalize and subsequent comparison.

**Lemma 3 (Conversion).** *For all  $x : \mathbb{Z}$ , we have  $x = (\Downarrow (\Uparrow x))$ .*

*Example 3 (Lambdapi proof term for step t5).*

```

1 symbol x: El int;
2 symbol y: El int;
3
4 opaque symbol t5: Prf• (¬ (x + y < 1) ∨ (¬ (x = 2)) ∨ (¬ (0 = y)) ∨ ■) :=
5 begin
6 end;
```

## 6 Evaluation

## 7 Related work

## 8 conclusion

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## A Alethe

### A.1 The Syntax

```

    <proof> = <proof_command>*
    <proof_command> = (assume <symbol> <proof_term>)
                    | (step <symbol> <clause> :rule <symbol>
                        <premises_annotation>?
                        <context_annotation>? <attribute>*)
                    | (anchor :step <symbol>
                        <args_annotation>? <attribute>*)
                    | (define-fun <function_def>)
    <clause> = (cl <proof_term>*)
    <proof_term> = <term> extended with
                  (choice ( <sorted_var> ) <proof_term> )
    <premises_annotation> = :premises ( <symbol>+ )
    <args_annotation> = :args ( <step_arg>+ )
    <step_arg> = <symbol> | ( <symbol> <proof_term> )
    <context_annotation> = :args ( <context_assignment>+ )
    <context_assignment> = ( <sorted_var> )
                        | ( := <symbol> <proof_term> )

```

Fig. 6. Alethe grammar

## B Lambdapi Formalizations for Integer and Binary Number Operations

$\text{add} : \mathbb{P} \rightarrow \mathbb{P} \rightarrow \mathbb{P}$	$\text{add\_c} : \mathbb{P} \rightarrow \mathbb{P} \rightarrow \mathbb{P}$
$\text{add } (I \ x) \ (I \ q) \hookrightarrow 0 \ (\text{add\_c } x \ q)$	$\text{add\_c } (I \ x) \ (I \ q) \hookrightarrow I \ (\text{add\_c } x \ q)$
$\text{add } (I \ x) \ (0 \ q) \hookrightarrow I \ (\text{add } x \ q)$	$\text{add\_c } (I \ x) \ (0 \ q) \hookrightarrow 0 \ (\text{add\_c } x \ q)$
$\text{add } (0 \ x) \ (I \ q) \hookrightarrow I \ (\text{add } x \ q)$	$\text{add\_c } (0 \ x) \ (I \ q) \hookrightarrow 0 \ (\text{add\_c } x \ q)$
$\text{add } (0 \ x) \ (0 \ q) \hookrightarrow 0 \ (\text{add } x \ q)$	$\text{add\_c } (0 \ x) \ (0 \ q) \hookrightarrow I \ (\text{add } x \ q)$
$\text{add } x \ H \hookrightarrow \text{succ } x$	$\text{add\_c } x \ H \hookrightarrow \text{add } x \ (0 \ H)$
$\text{add } H \ y \hookrightarrow \text{succ } y$	$\text{add\_c } H \ y \hookrightarrow \text{add } (0 \ H) \ y$

```

sub :  $\mathbb{P} \rightarrow \mathbb{P} \rightarrow \mathbb{Z}$ 
sub (I p) (I q)  $\hookrightarrow$  double (sub p q)
sub (I p) (0 q)  $\hookrightarrow$  succ_double (sub p q)
sub (I p) H  $\hookrightarrow$  Zpos (0 p)
sub (0 p) (I q)  $\hookrightarrow$  pred_double (sub p q)
sub (0 p) (0 q)  $\hookrightarrow$  double (sub p q)
sub (0 p) H  $\hookrightarrow$  Zpos (pos_pred_double p)
sub H (I q)  $\hookrightarrow$  Zneg (0 q)
sub H (0 q)  $\hookrightarrow$  Zneg (pos_pred_double q)
sub H H  $\hookrightarrow$  Z0

```

```

compare_acc :  $\mathbb{P} \rightarrow \text{Comp} \rightarrow \mathbb{P} \rightarrow \text{Comp}$ 
compare_acc (I x) c (I q)  $\hookrightarrow$  compare_acc x c q
compare_acc (I x) _ (0 q)  $\hookrightarrow$  compare_acc x Gt q
compare_acc (I _) _ H  $\hookrightarrow$  Gt
compare_acc (0 x) _ (I q)  $\hookrightarrow$  compare_acc x Lt q
compare_acc (0 x) c (0 q)  $\hookrightarrow$  compare_acc x c q
compare_acc (0 _) _ H  $\hookrightarrow$  Gt
compare_acc H _ (I _)  $\hookrightarrow$  Lt
compare_acc H _ (0 _)  $\hookrightarrow$  Lt
compare_acc H c H  $\hookrightarrow$  c

```

```

compare x y := compare_acc x Eq y

```

### B.1 Confluence of the rewriting rules of integers and positive binary number

The rules presented below represent the relations  $\rightarrow_{\mathbb{Z}}$  and  $\rightarrow_{\mathbb{P}}$  encoded in the TRS<sup>1</sup> format accepted by the [23] tool. These rules can be used to rerun the tool in order to verify the confluence property.

```

1 (VAR
2   a: Z
3   b: Z
4   x : P

```

<sup>1</sup> <http://www.lri.fr/marche/tpdb/format.html>

```

5   q : P
6   y : P
7 )
8 (RULES
9   ~(Z0) -> Z0
10  ~(Zpos(p)) -> Zneg(p)
11  ~(Zneg(p)) -> Zpos(p)
12  ~(~(a)) -> a
13
14  double(Z0) -> Z0
15  double(Zpos(p)) -> Zpos(0(p))
16  double(Zneg(p)) -> Zneg(0(p))
17
18  succ_double(Z0) -> Zpos(H)
19  succ_double(Zpos(p)) -> Zpos(I(p))
20  succ_double(Zneg(p)) -> Zneg(pos_pred_double(p))
21
22  pred_double(Z0) -> Zneg(H)
23  pred_double(Zpos(p)) -> Zpos(pos_pred_double(p))
24  pred_double(Zneg(p)) -> Zneg(I(p))
25
26  sub(I(p), I(q)) -> double(sub(p, q))
27  sub(I(p), 0(q)) -> succ_double(sub(p, q))
28  sub(I(p), H) -> Zpos(0(p))
29  sub(0(p), I(q)) -> pred_double(sub(p, q))
30  sub(0(p), 0(q)) -> double(sub(p, q))
31  sub(0(p), H) -> Zpos(pos_pred_double(p))
32  sub(H, I(q)) -> Zneg(0(q))
33  sub(H, 0(q)) -> Zneg(pos_pred_double(q))
34  sub(H, H) -> Z0
35
36  +(Z0,a) -> a
37  +(a,Z0) -> a
38  +(Zpos(x), Zpos(y)) -> Zpos(add(x, y))
39  +(Zpos(x), Zneg(y)) -> sub(x, y)
40  +(Zneg(x), Zpos(y)) -> sub(y, x)
41  +(Zneg(x), Zneg(y)) -> Zneg(add(x, y))
42
43  mult(Z0, a) -> Z0
44  mult(a, Z0) -> Z0
45  mult(Zpos(x), Zpos(y)) -> Zpos(mul(x, y))
46  mult(Zpos(x), Zneg(y)) -> Zneg(mul(x, y))
47  mult(Zneg(x), Zpos(y)) -> Zneg(mul(x, y))
48  mult(Zneg(x), Zneg(y)) -> Zpos(mul(x, y))
49
50
51  succ(I(x)) -> 0(succ(x))
52  succ(0(x)) -> I(x)
53  succ(H) -> 0(H)
54  add(I(x), I(q)) -> 0(addcarry(x, q))
55  add(I(x), 0(q)) -> I(add(x, q))
56  add(0(x), I(q)) -> I(add(x, q))
57  add(0(x), 0(q)) -> 0(add(x, q))
58  add(x, H) -> succ(x)
59  add(H, y) -> succ(y)
60
61  addcarry(I(x), I(q)) -> I(addcarry(x, q))
62  addcarry(I(x), 0(q)) -> 0(addcarry(x, q))
63  addcarry(0(x), I(q)) -> 0(addcarry(x, q))
64  addcarry(0(x), 0(q)) -> I(add(x, q))
65  addcarry(x, H) -> add(x, 0(H))
66  addcarry(H, y) -> add(0(H), y)
67
68  pos_pred_double(I(x)) -> I(0(x))
69  pos_pred_double(0(x)) -> I(pos_pred_double(x))
70  pos_pred_double(H) -> H

```

```

71 |
72 |   mul(I(x), y) -> add(x, 0(mul(x,y)))
73 |   mul(0(x), y) -> 0(mul(x, y))
74 |   mul(H, y) -> y
75 | )

```

**Listing 1.4.** Rewriting rule of  $\mathbb{Z}$  and  $\mathbb{P}$  in the TRS format**Lemma 4 (Confluence).**

*Proof. CSI automatically proves the confluence of  $\rightarrow_{\mathbb{Z}}$  and  $\rightarrow_{\mathbb{P}}$  by giving the polynomial interpretation:*

$$[\mathit{succ}(x)] = 4 * x \qquad [\mathit{add}(x, y)] = 4 * x + 4 * y + 2 \qquad [H] = 4$$