

Tarea 6:

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Parte c

i) Arg. en pipeline:

Ciclo	Fetch	Decode	Execute
1	A		
2	B	A	
3	C	B	A
4	D	C	B
5	E	D	C
6	F	E	D
7	G	F	E
8	H	G	F
9	R		
10	S	R	
11		S	R
12			S

ii) Arg. superescalador:

Ciclo	Fetch	Decode	Execute
1	AB		
2	CD	AB	
3	EF	CD	AB
4		D	C
5	GH	EF	D
6		GH	EF
7	RS		
8		RS	
9		S	R
10			S

