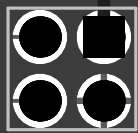


OFF ON
B00T

RESET

ACCEL

GYRO

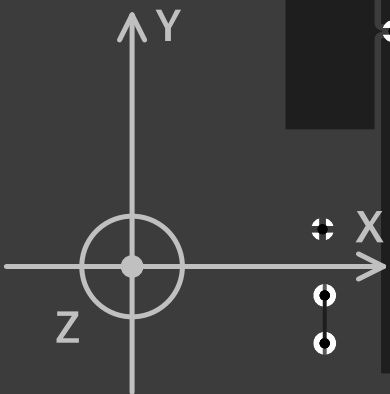


SWD

MAG



ALTIUM
DESIGNER

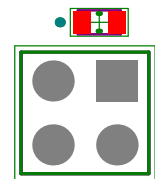
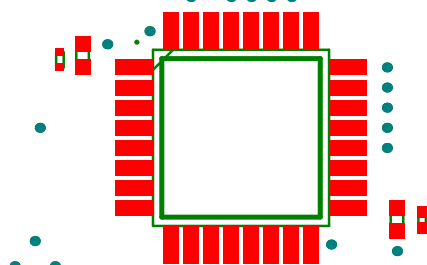
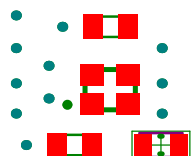
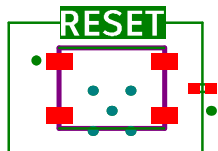
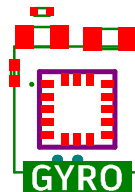
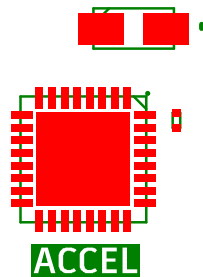
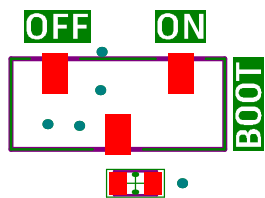


VALKYRIE
DYNAMICS MEASURING
UNIT

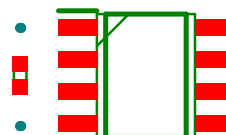
NFS

NOVA FORMULA STUDENT

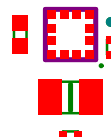
STM GYRO ACCEL GNSS MAG



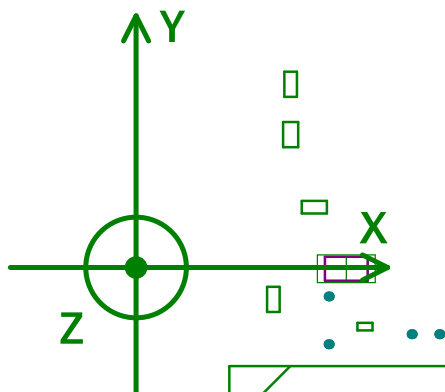
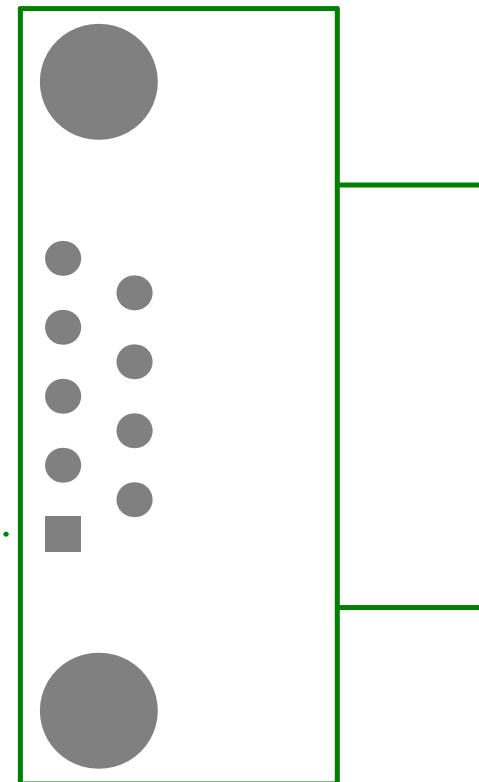
SWD



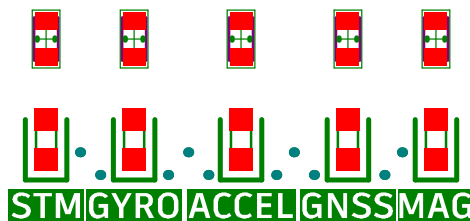
MAG



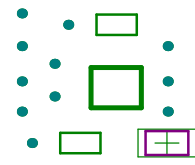
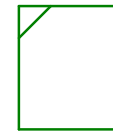
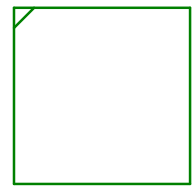
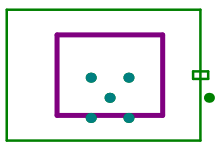
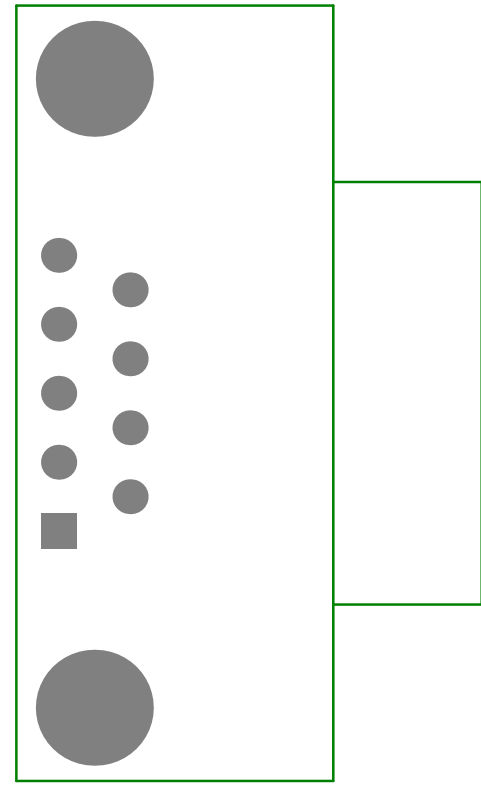
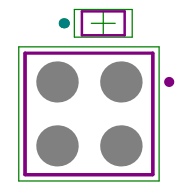
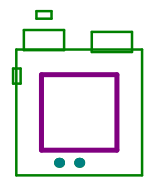
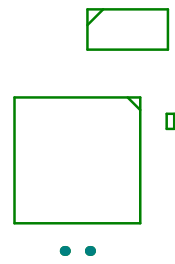
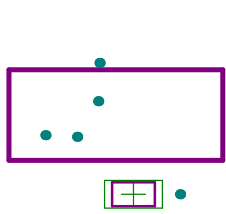
ALTIUM
DESIGNER



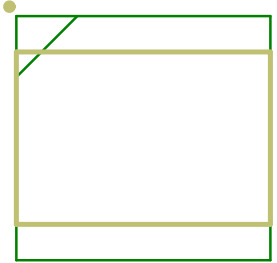
VALKYRIE
DYNAMICS MEASURING
UNIT

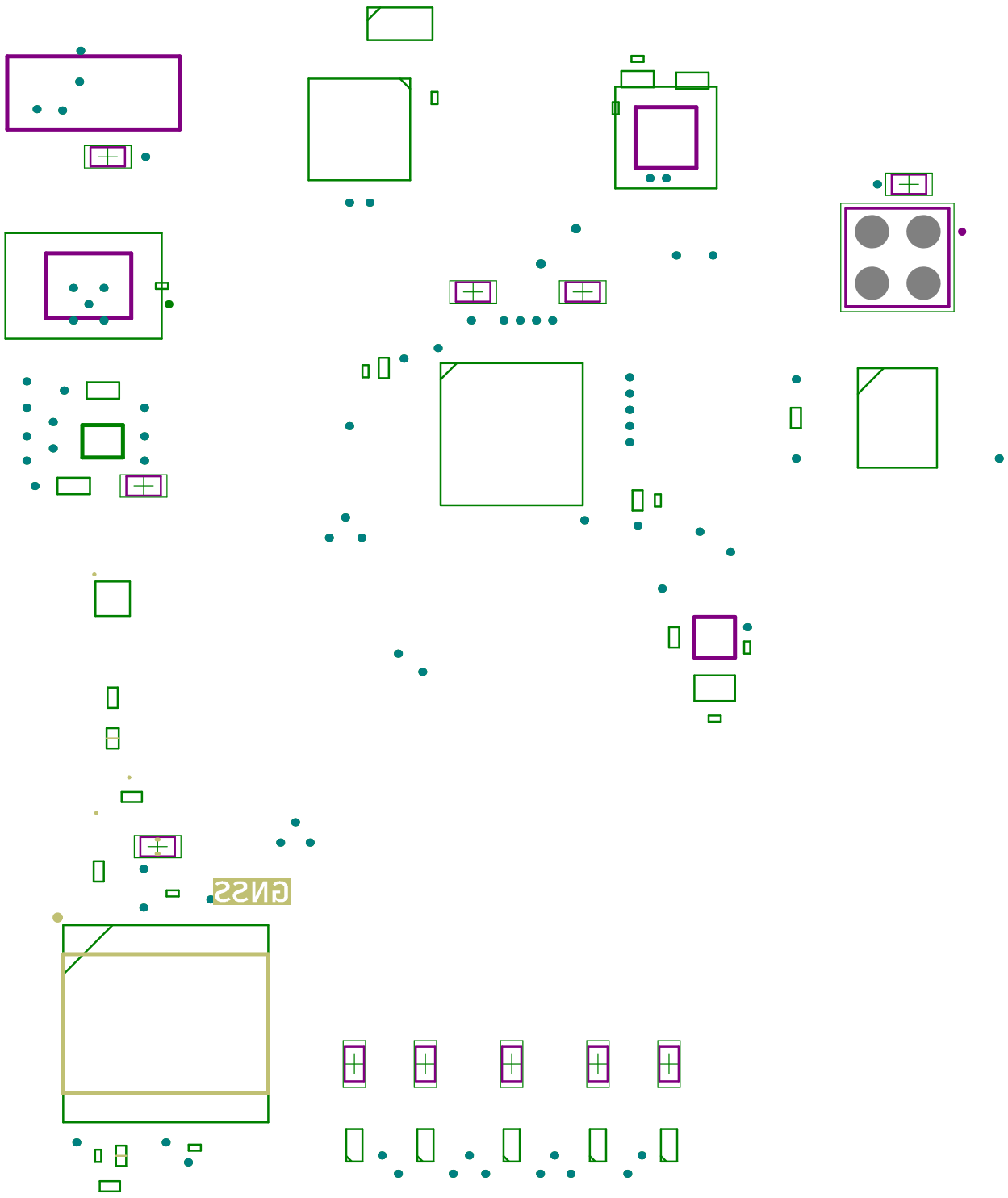


NFS
NOVA FORMULA STUDENT

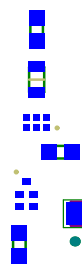
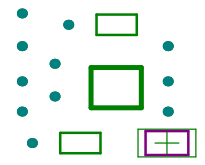
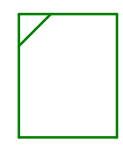
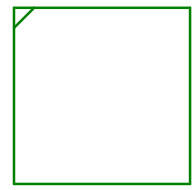
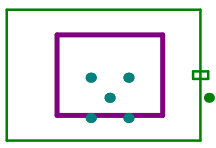
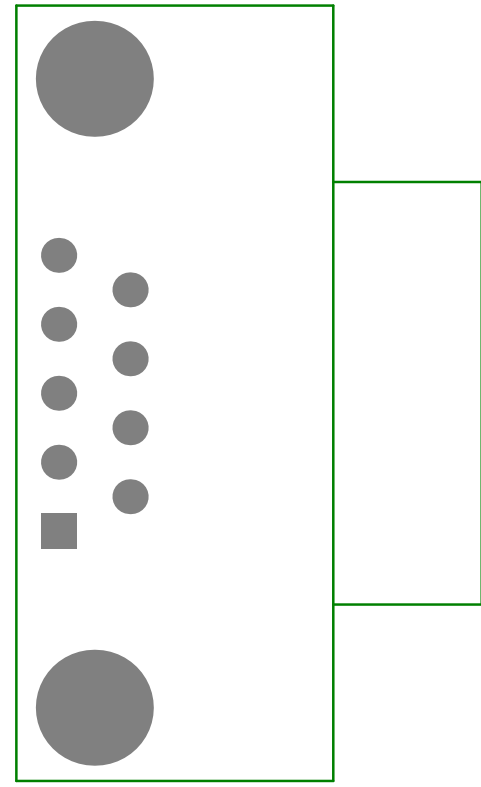
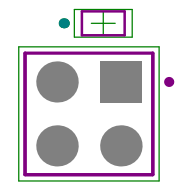
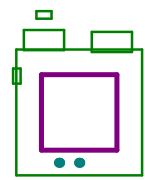
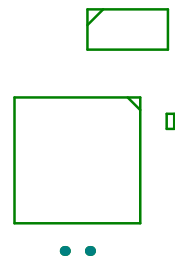
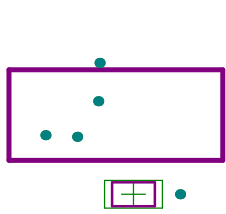


gnis

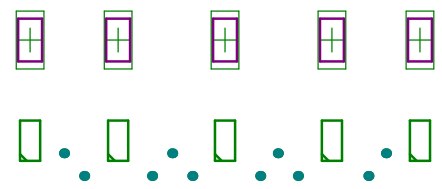
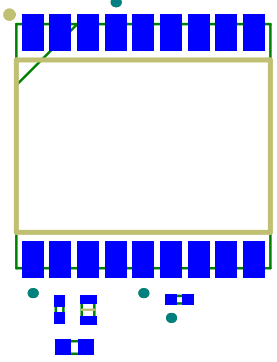




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gnis



Design Rules Verification Report

Filename : C:\Users\35196\Desktop\FS\Valkyrie\Valkyrie\VALK.PcbDo

Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.254mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.1mm) (Max=1mm) (Preferred=0.156mm) (All	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Hole Size Constraint (Min=0.025mm) (Max=6.3mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.15mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.1mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All	0
Total	0