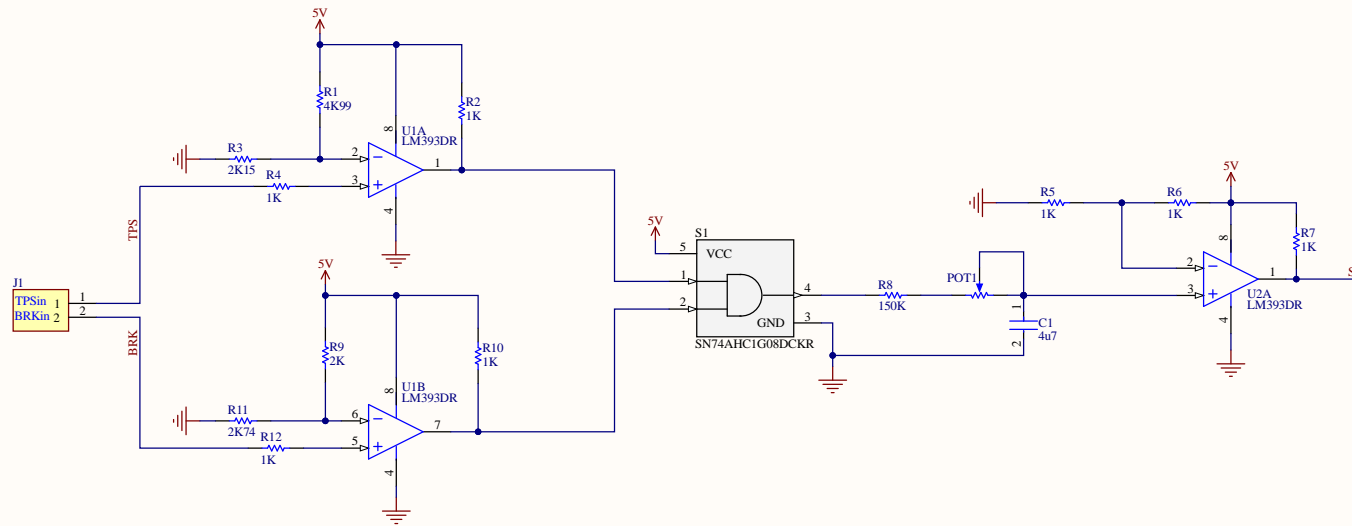
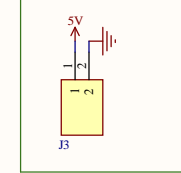


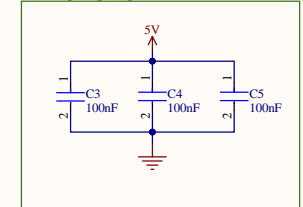
Input Aquisition + 500msec delay Circuit



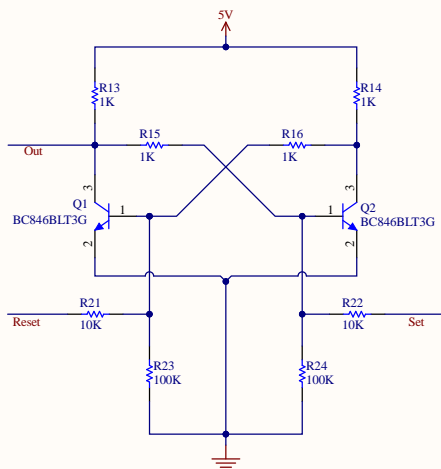
VCC & GND Connector



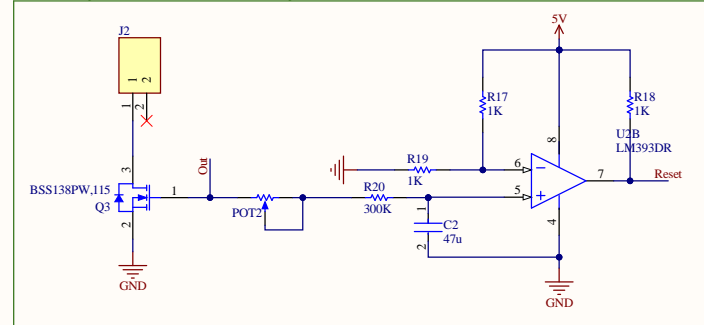
Decoupling Capacitors



Set-Reset Latch Circuit



10sec delay Circuit in Series with Relay Control Circuit



Title BSPD Circuitry	Drawn By Pedro Fernandes
Component Name BSPD	Revised By Pedro Fernandes
Project Name FS Fenix	Sheet 1 of 1