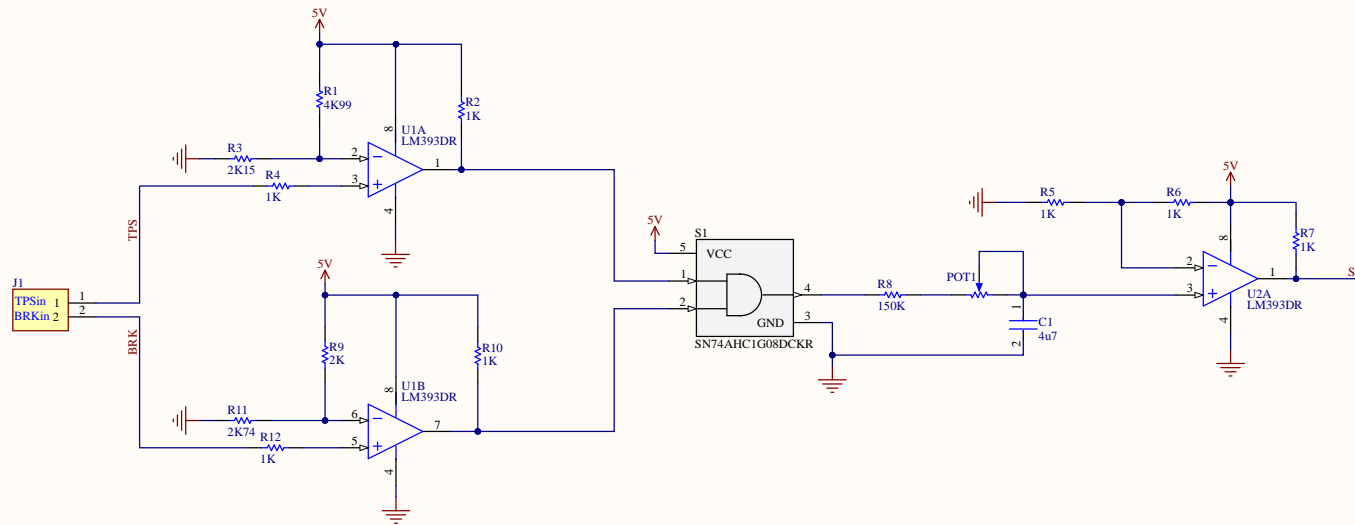
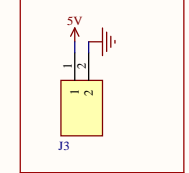


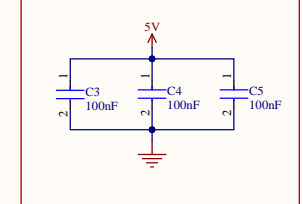
Input Aquisition + 500msec delay Circuit



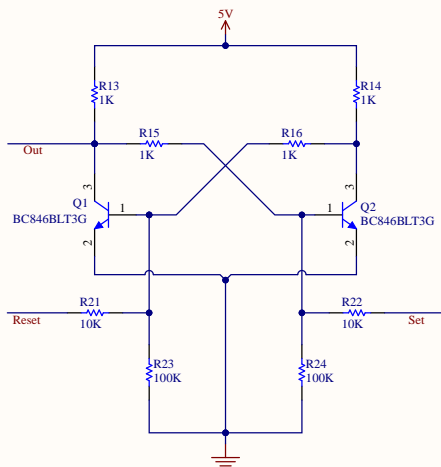
VCC & GND Connector



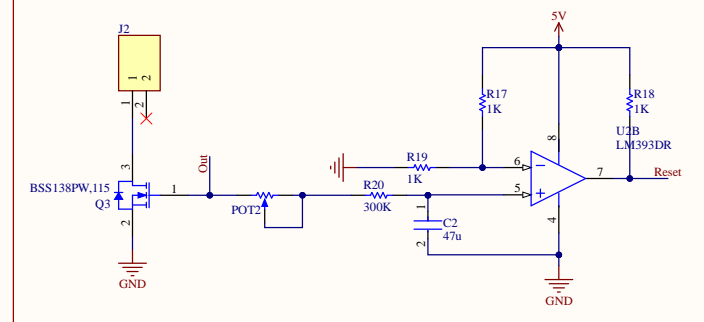
Decoupling Capacitors



Set-Reset Latch Circuit



10sec delay Circuit in Series with Relay Control Circuit



Title FSFCT Fenix - BSPD Circuitry		
Size B	Number 01	Revision Pedro Fernandes
Date: 8/21/2023	Sheet of 01/01	
File: C:\Users\...\BSPDSchematic.SchDoc	Drawn By: Pedro Fernandes	

