Part–A

Q. 1. Why $AD_0 - AD_7$ lines are multiplexed ? 2

Q. 2. What do you mean by de-multiplexing of buses ? 2

Q. 3. Write short note on assembly language programming. 2

Q. 4. How data transfer takes place in 8085 ? 2

Q. 5. Write short note on Interfacing of I/O devices. 2

Q. 6. Write short note on SFRs of 8051. 2

Q. 7. What do you mean by stack and stack pointer in 8051. 2

Q. 8. What is the use of program counter and I/O ports in 8051. 2

Q. 9. Write short note on programming timer interrupts. 2

Q. 10. What is the use of ALE signal ? 2

Part–B

Q. 1. Draw the block diagram of microprocessor 8085 and explain in brief. 4

Q. 2. Write stack and machine control groups of instruction set of 8085. 4

Q. 3. Explain macro RTL and micro RTL flow chart of instruction. 4

Q. 4. With the help of block diagram explain the organization and working of programmable interrupt controller 8259A. 4

Q. 5. What is the need of DMA controller in microprocessor applications. 4

Q. 6. Describe timing diagrams and execution cycles of 8051 microcontroller. 4

Q. 7. Explain the instructions RIM and SIM. 4

Part–C

Q. 1. Write a program to sort given 10 numbers from memory location 2000 in the ascending order. 10

Q. 2. Describe memory interfacing in 8085. 10

Q. 3. With the help of block diagram explain programmable interval timer (8253/8254). 10

Q. 4. Describe programming timer interrupts and external hardware interrupts. 10

Q. 5. Explain timing diagrams and execution cycles of 8051 Microcontroller. 10

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