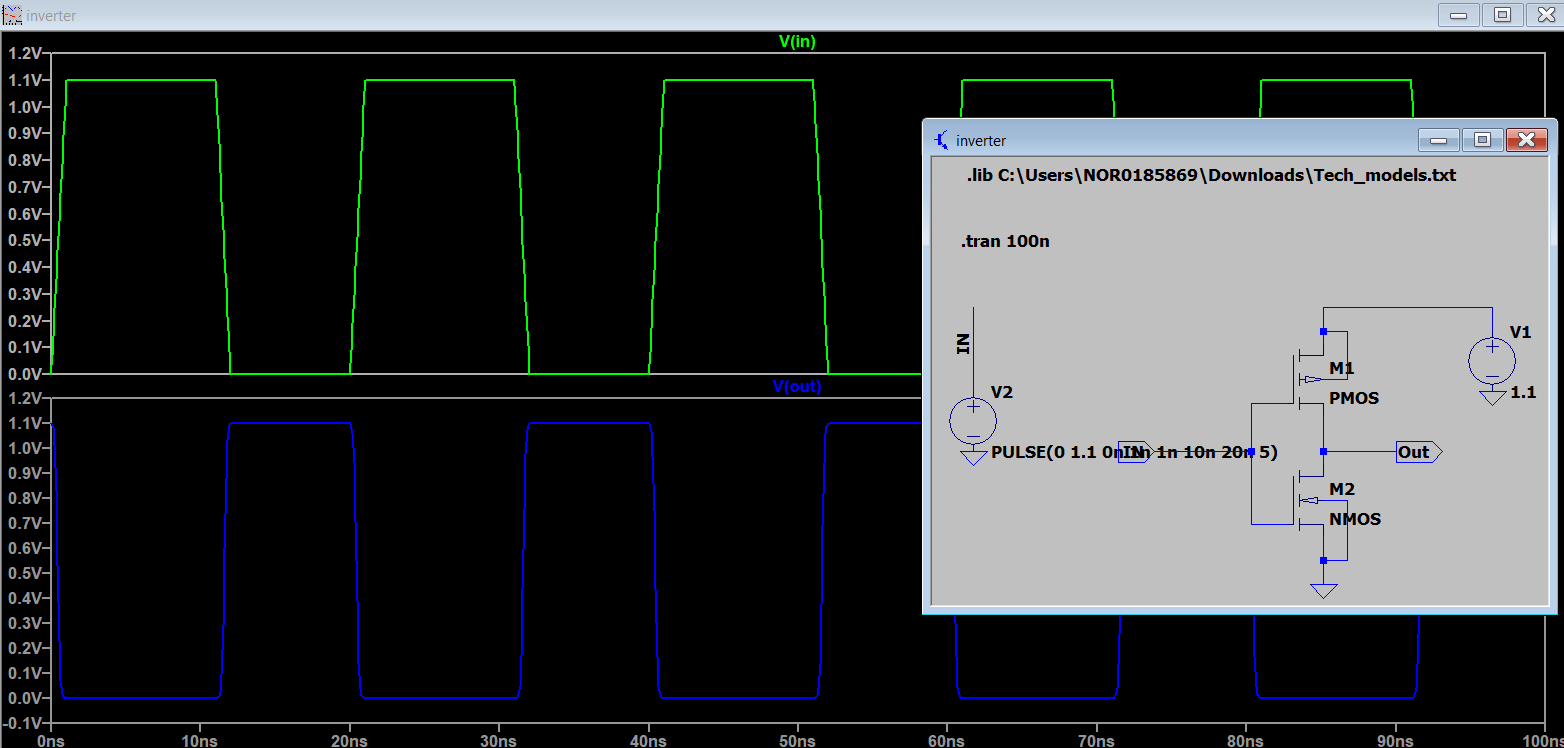
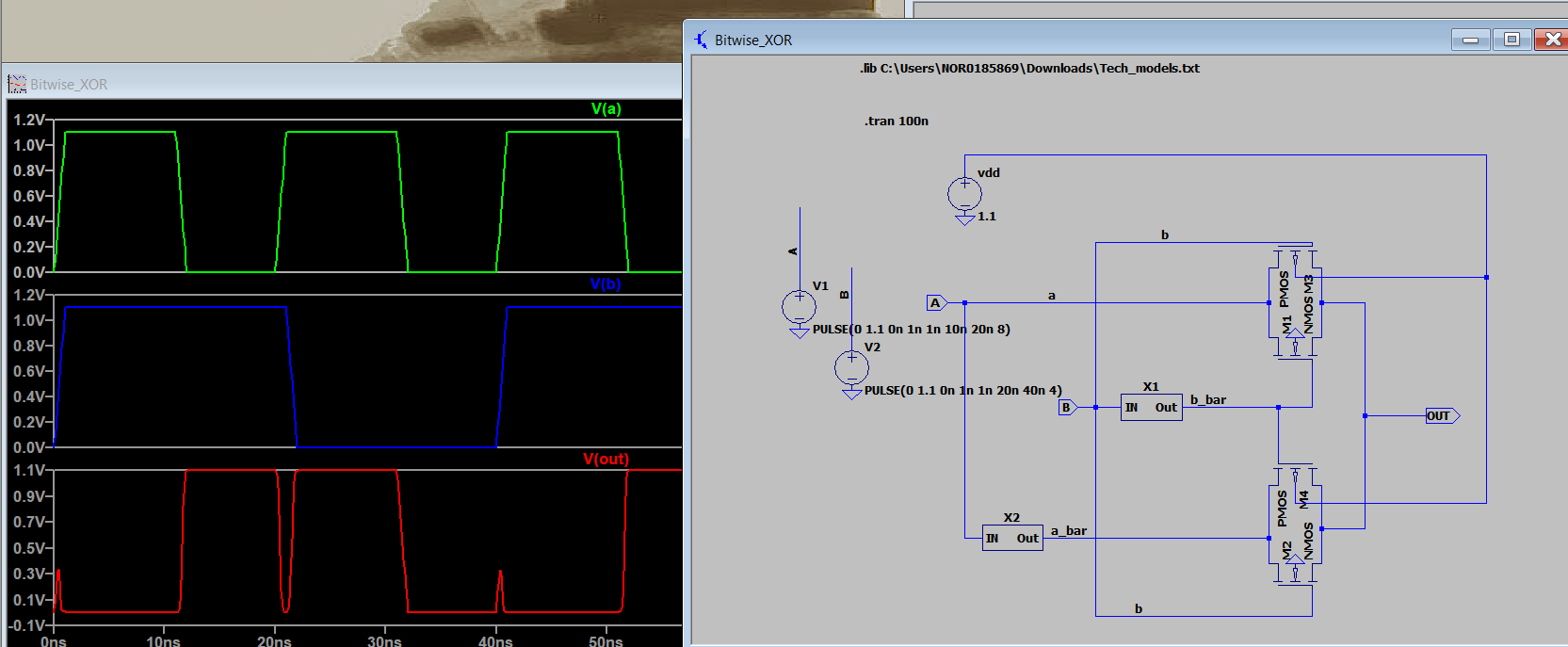
Part1: schematic

1-Inverter+Test

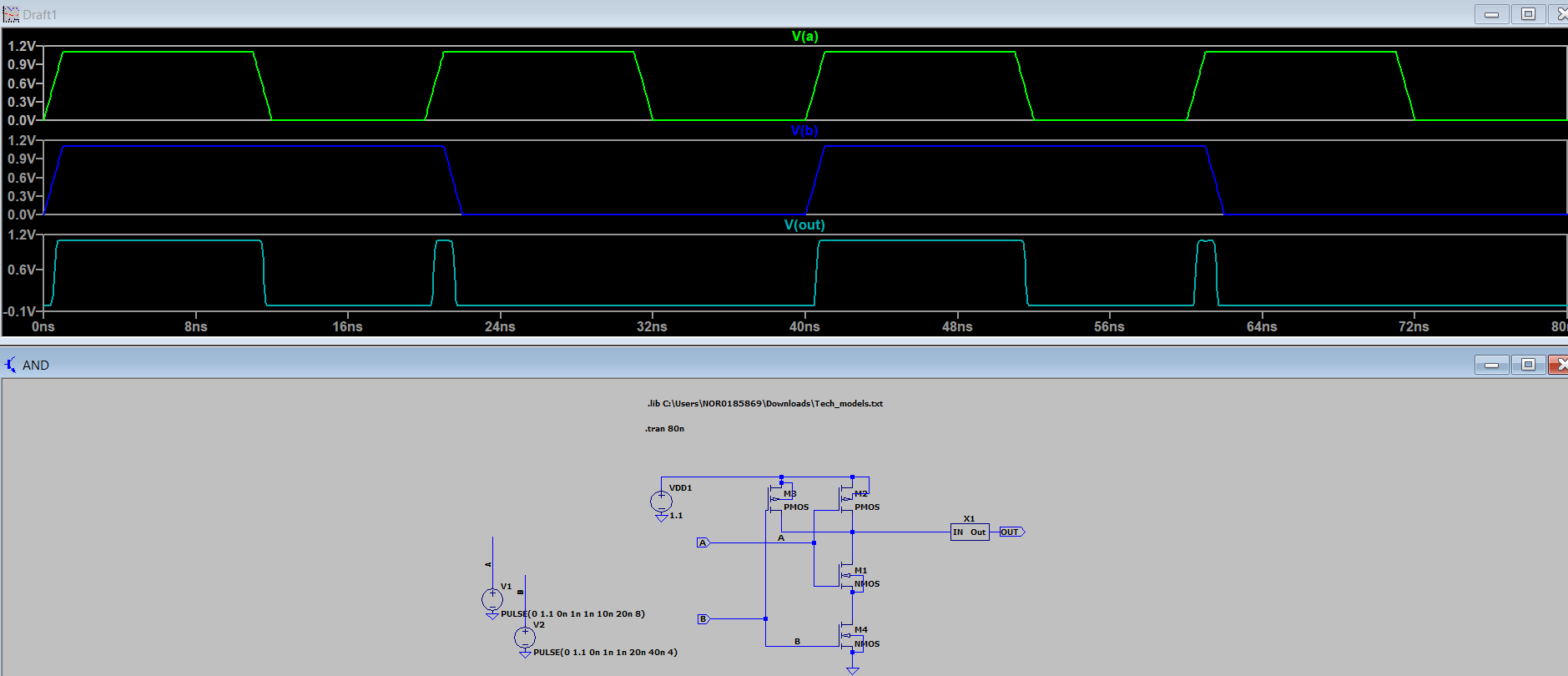


2-Bitwise\_XOR

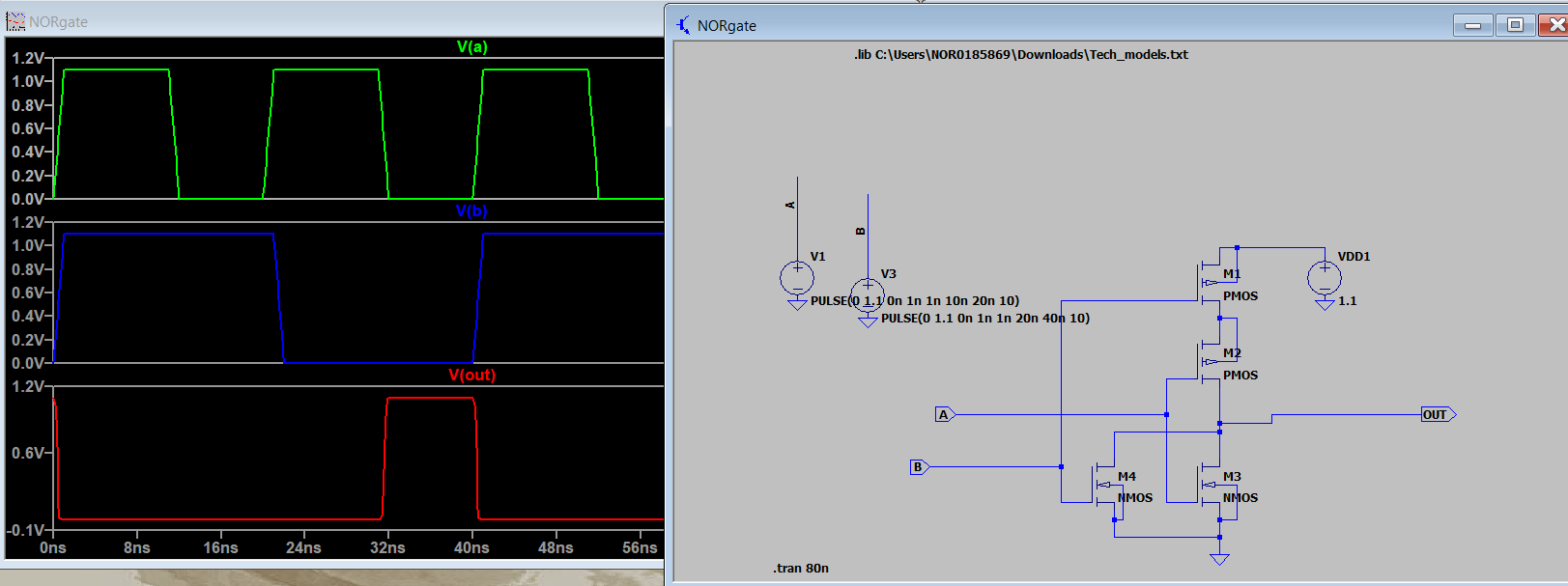


3-D-Flipflop

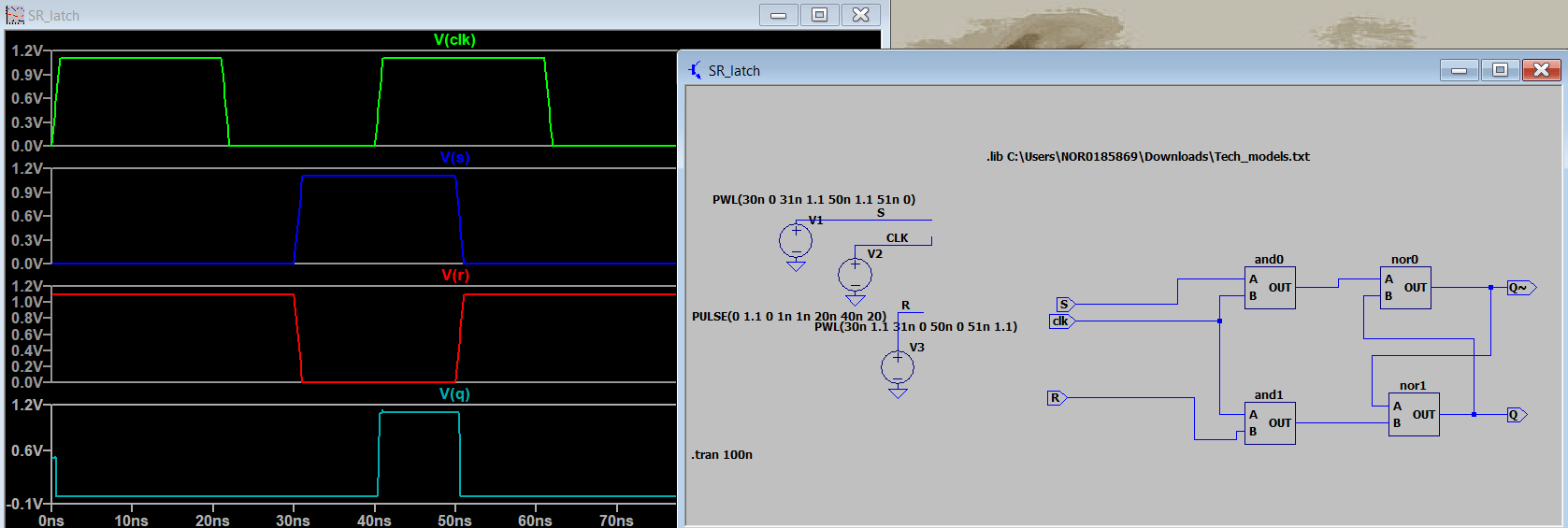
AND\_Gate



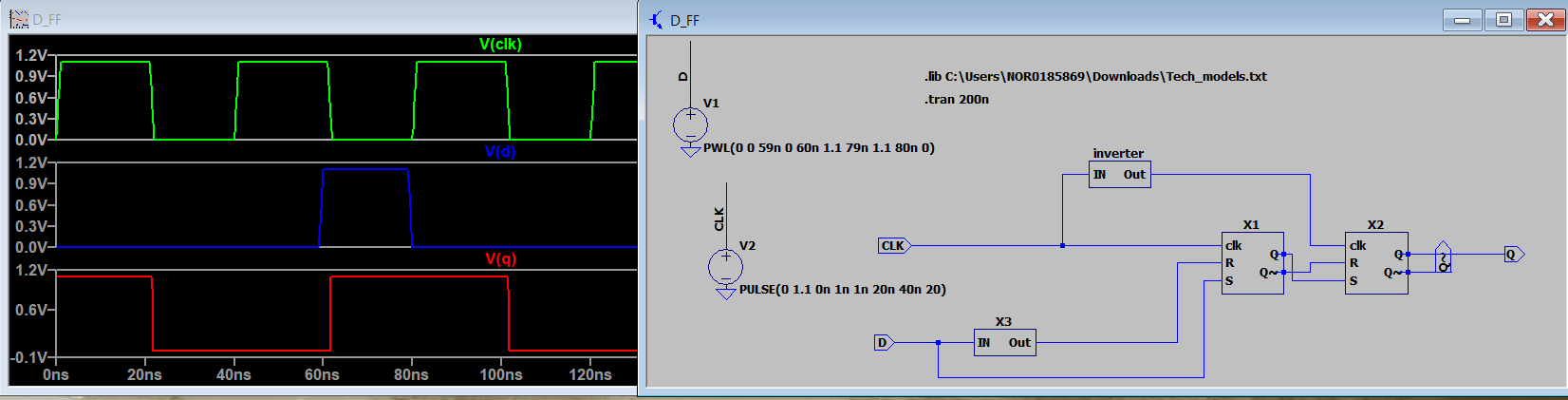
NOR\_gate



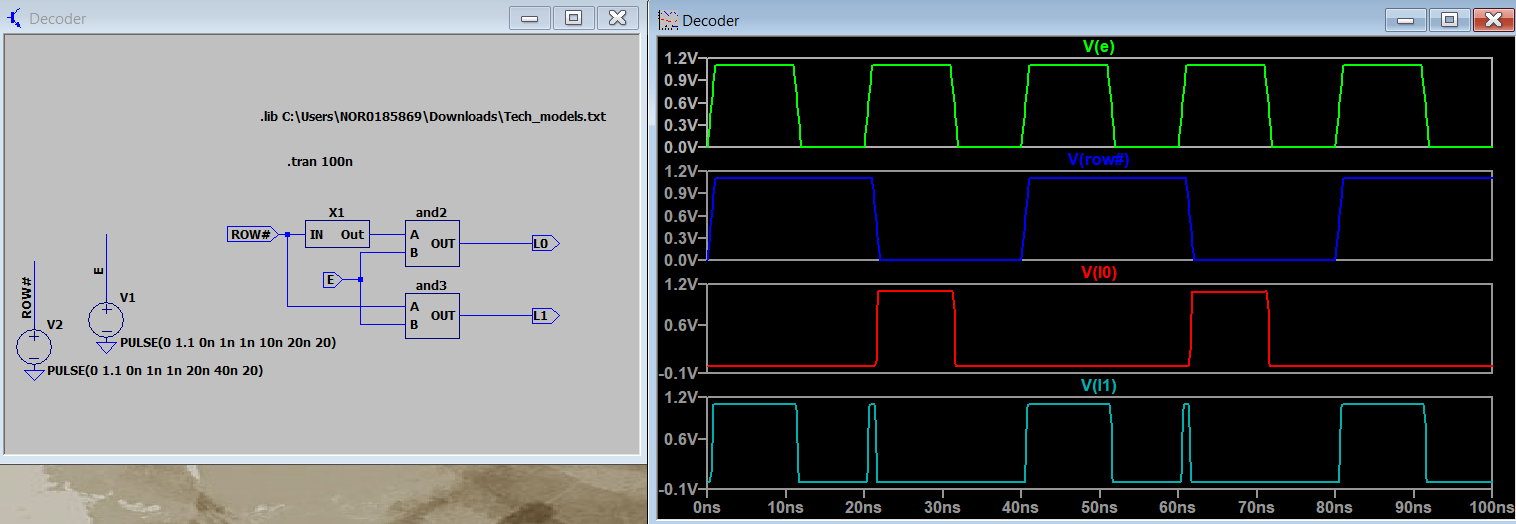
SR\_Latch



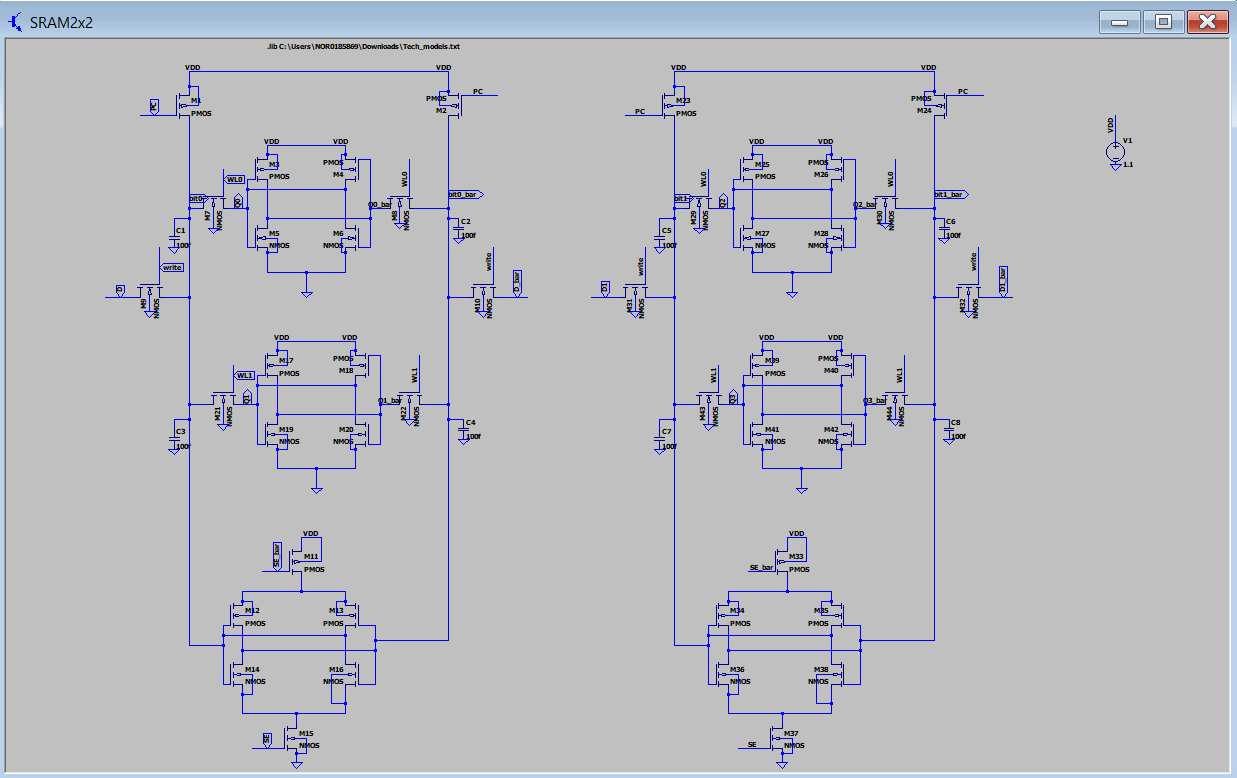
D\_FlipFlop

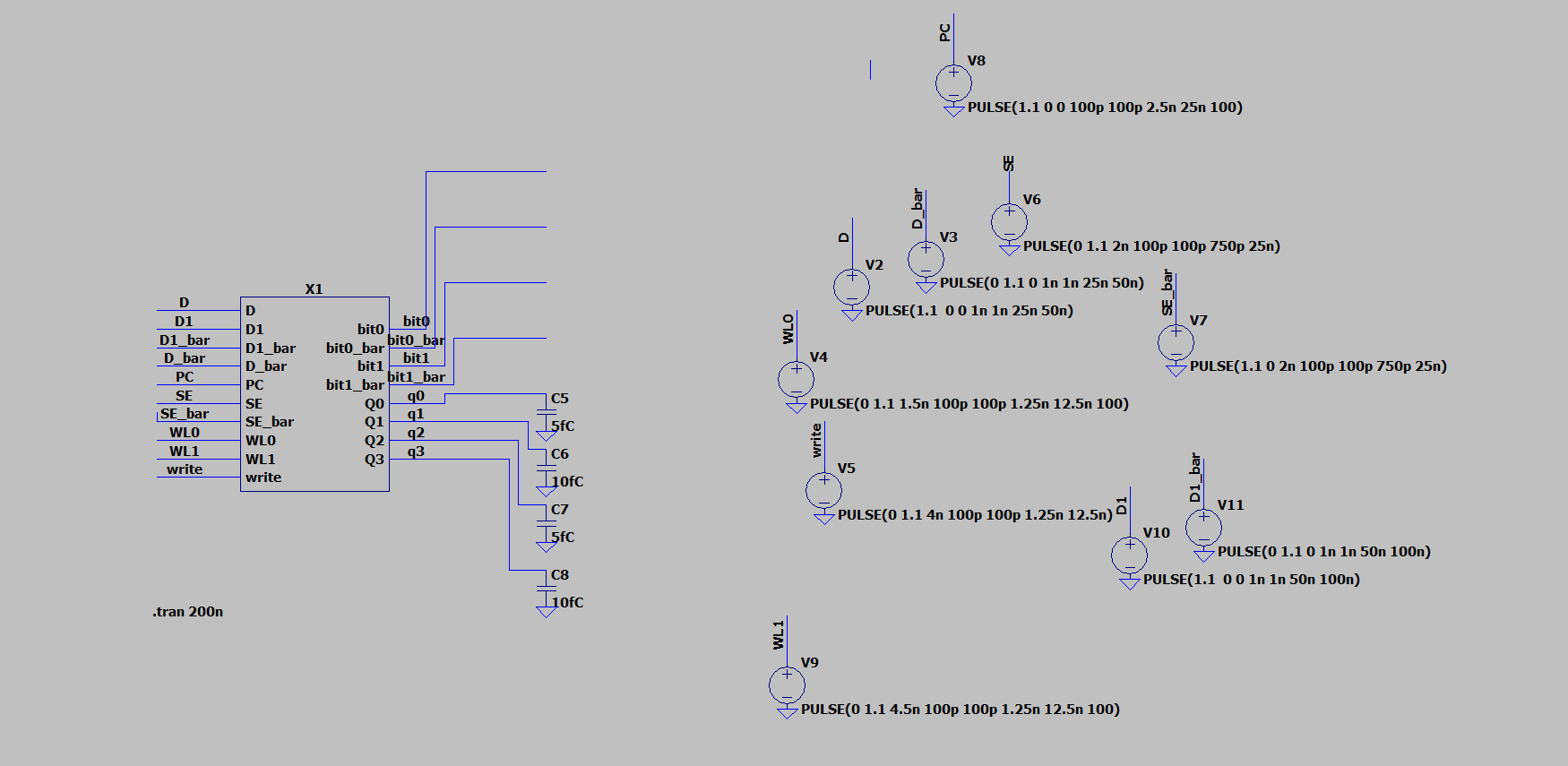


4-Decoder



5-SRAM2x2



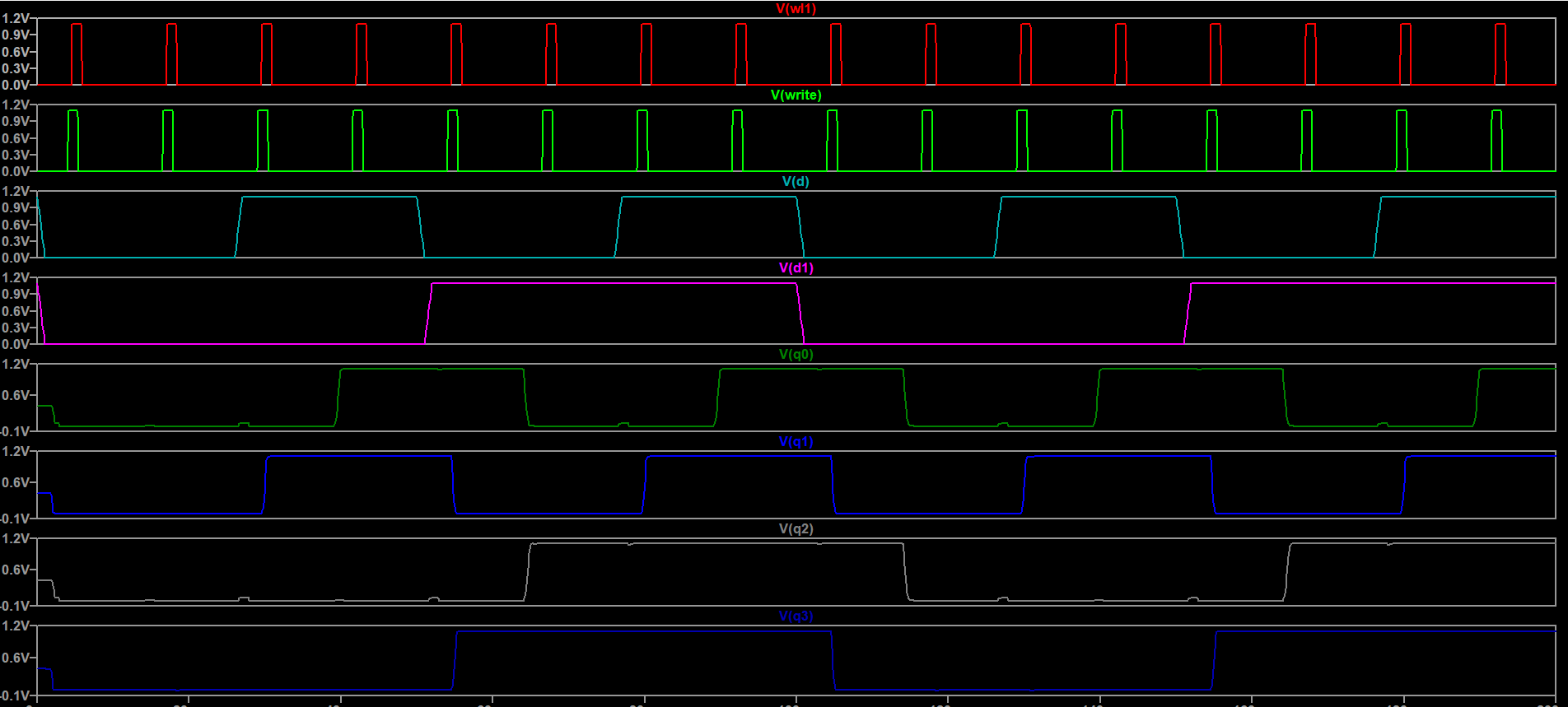


Read

A screenshot of a computer screen

Description automatically generated with low confidence

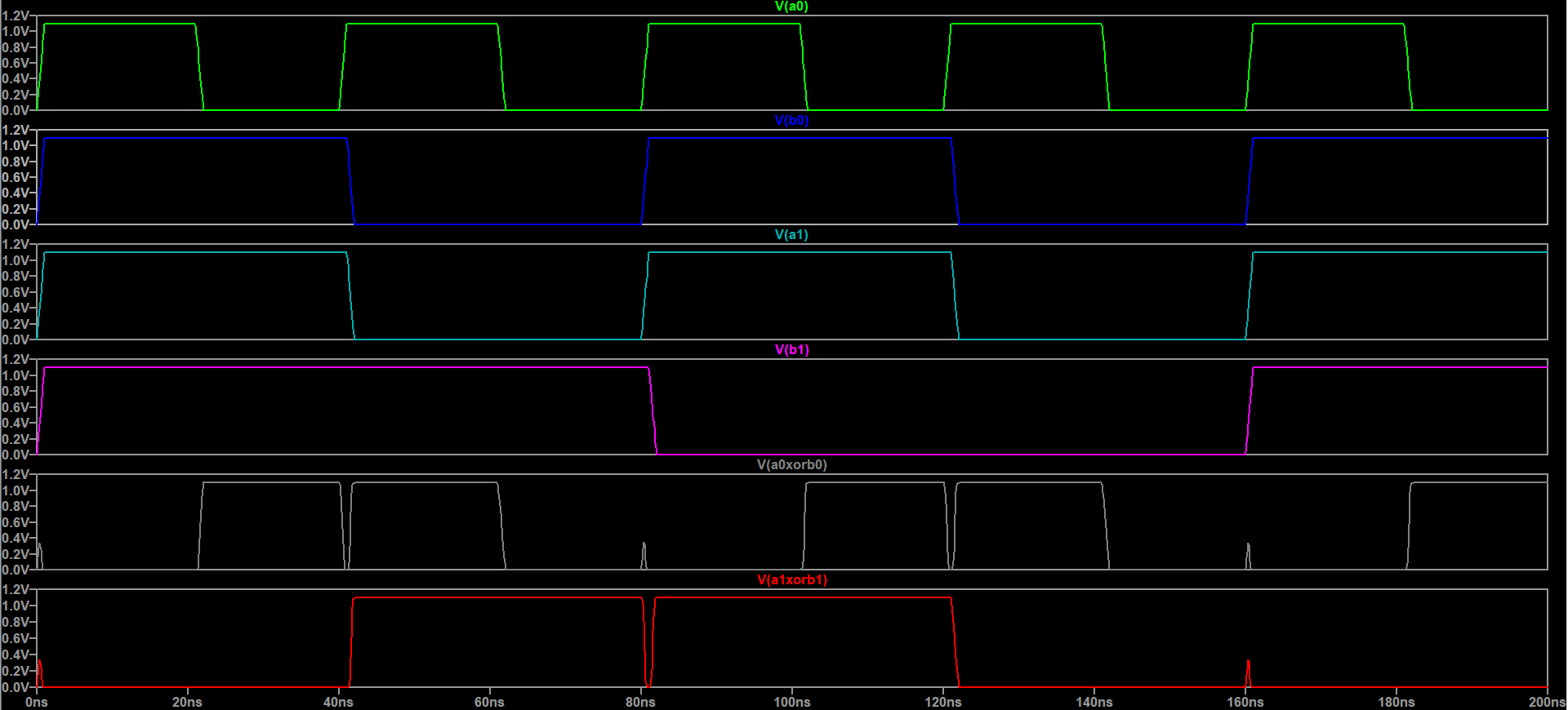
Write

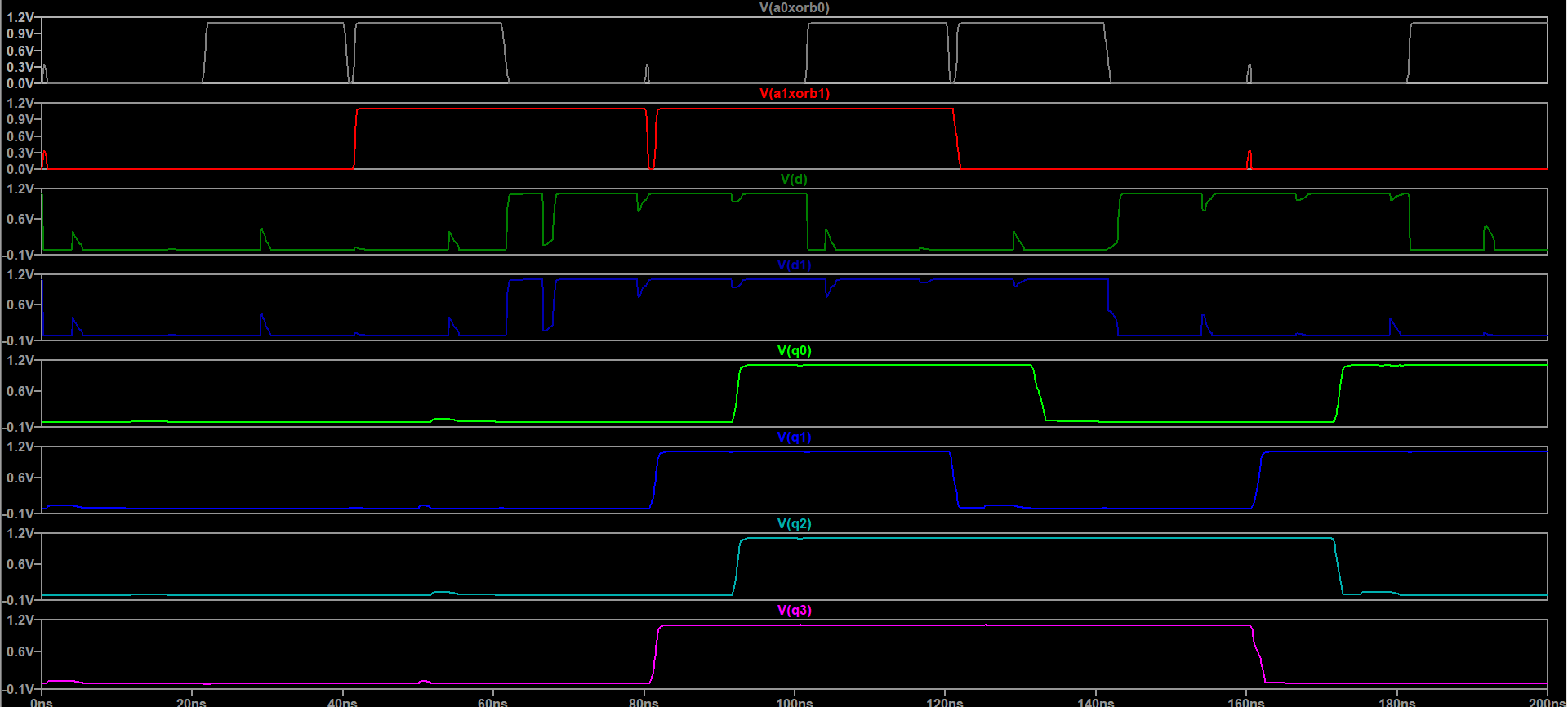


The whole system

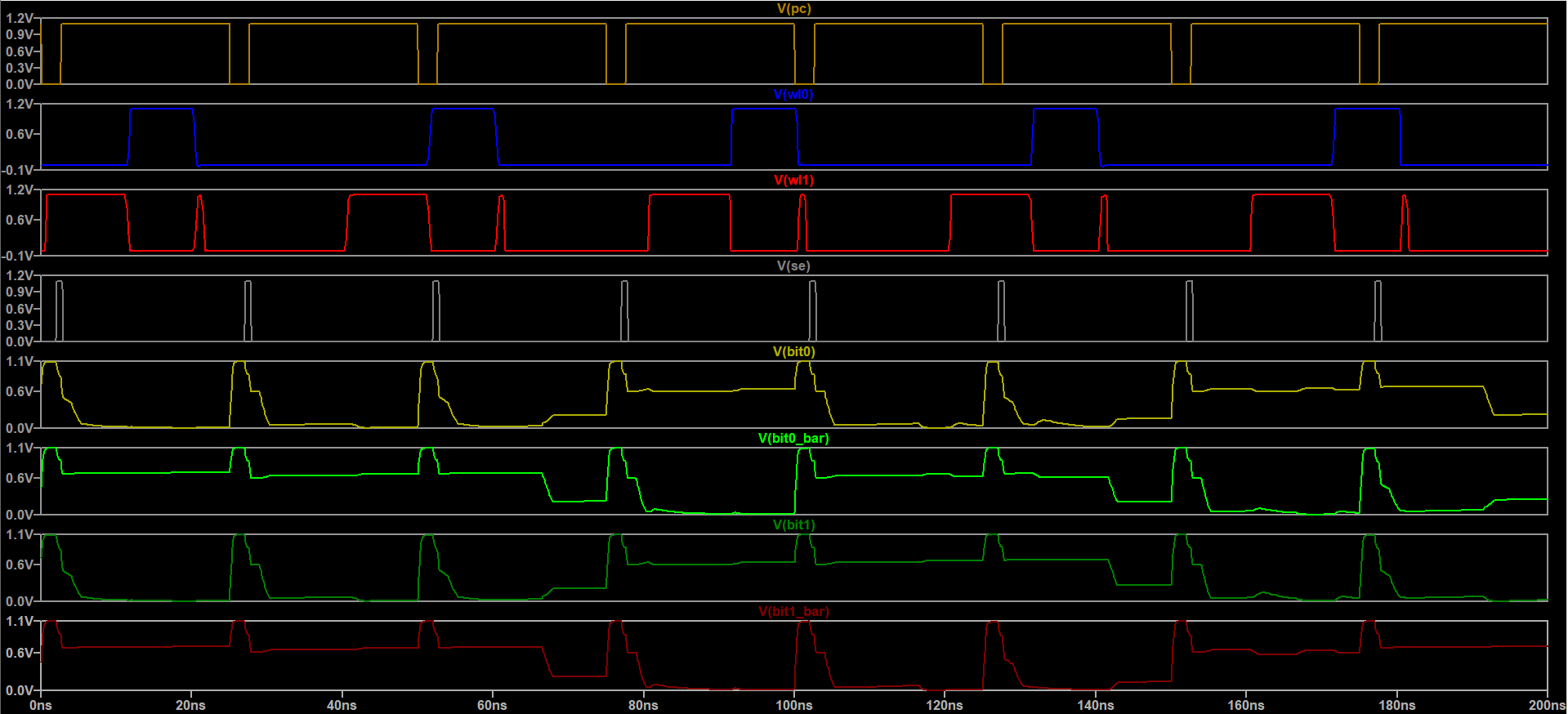
A picture containing text, diagram, screenshot, line

Description automatically generated

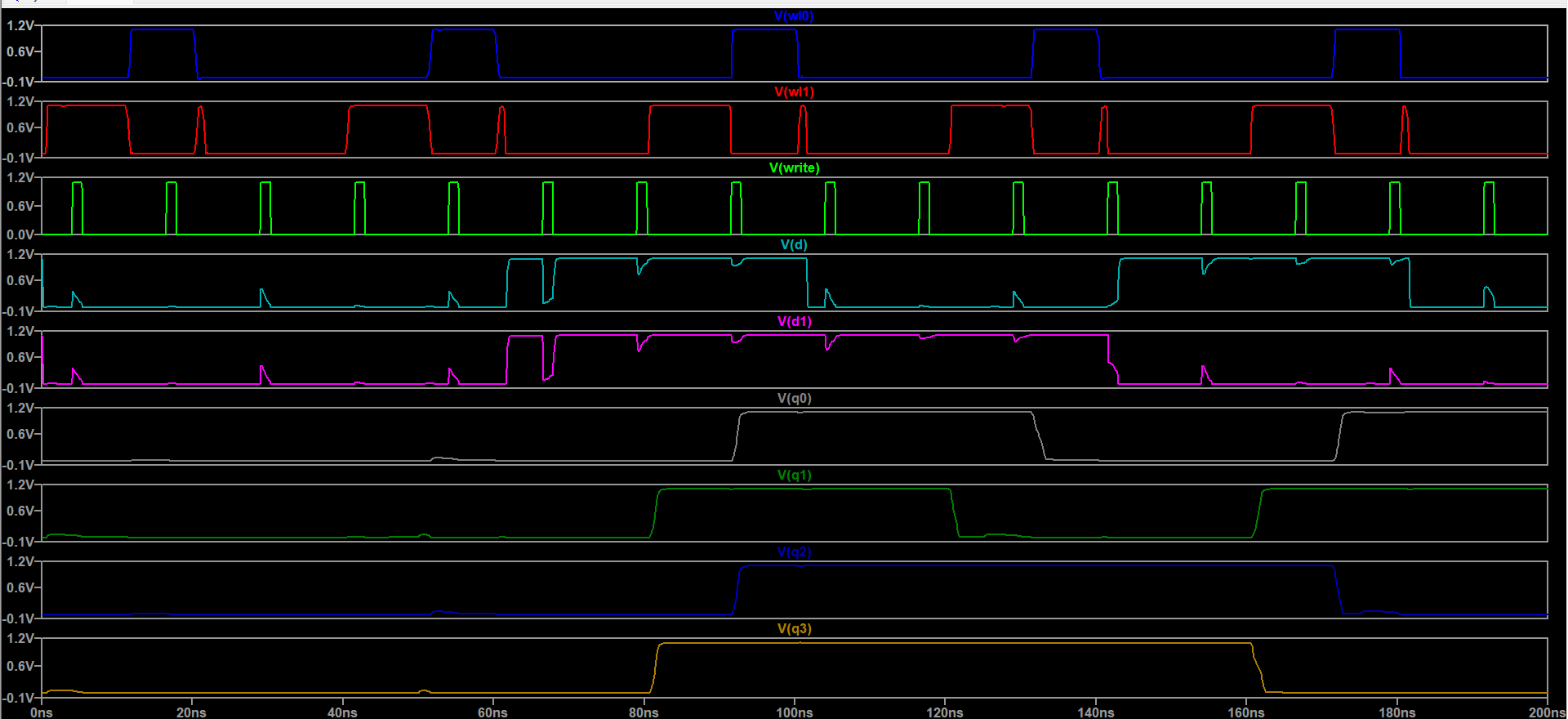




Sram2x2 in system Read

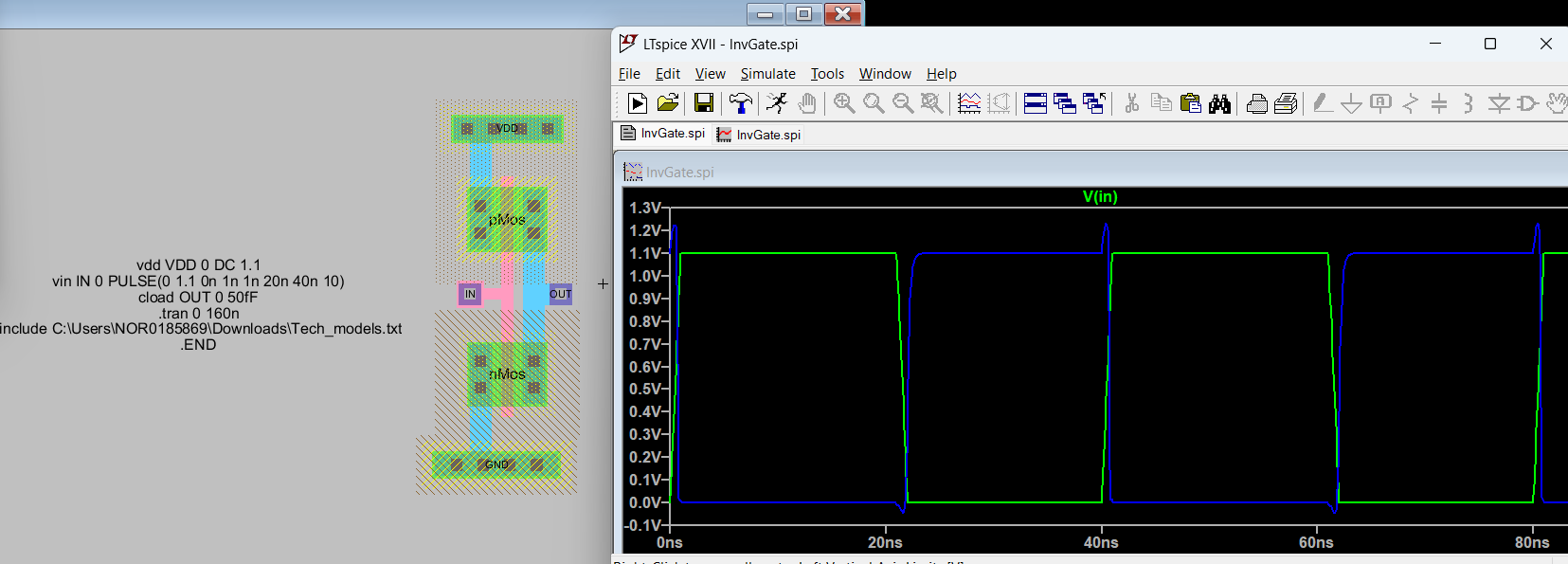


Sram2x2 in system Write

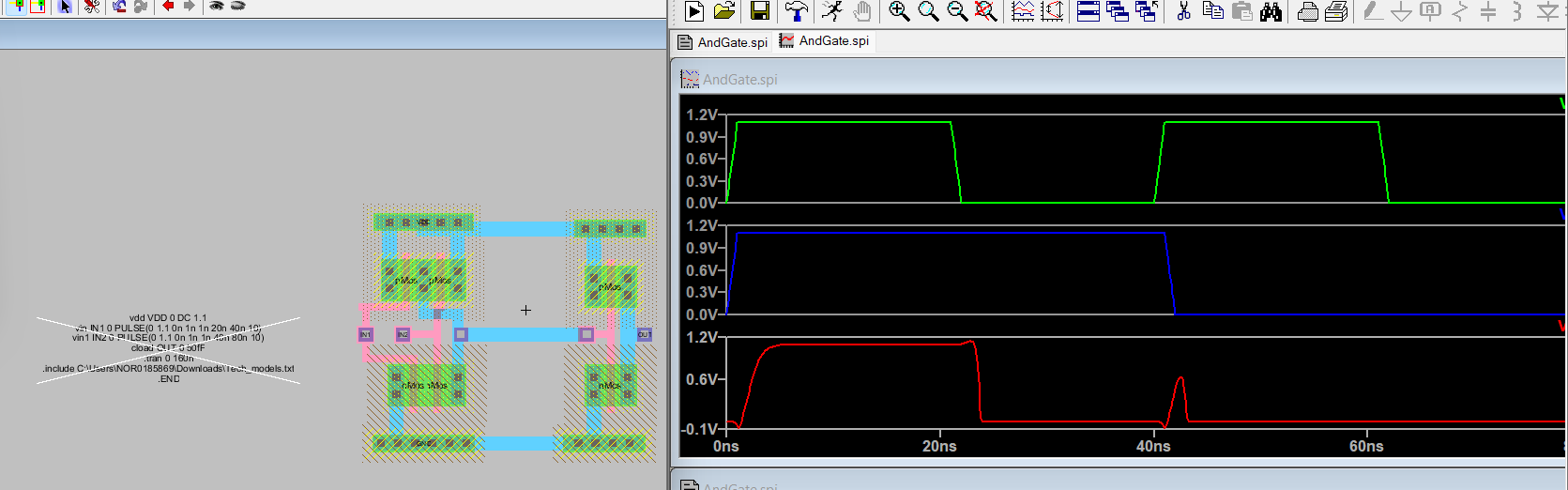


PART 2 Layout

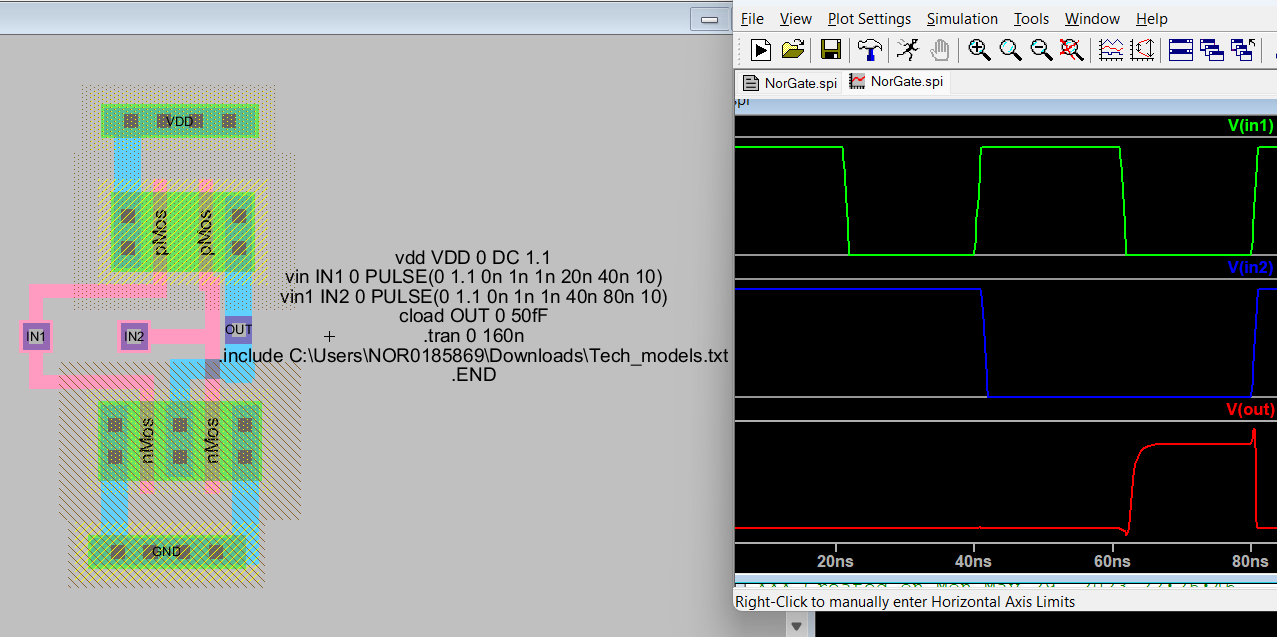
INV+test



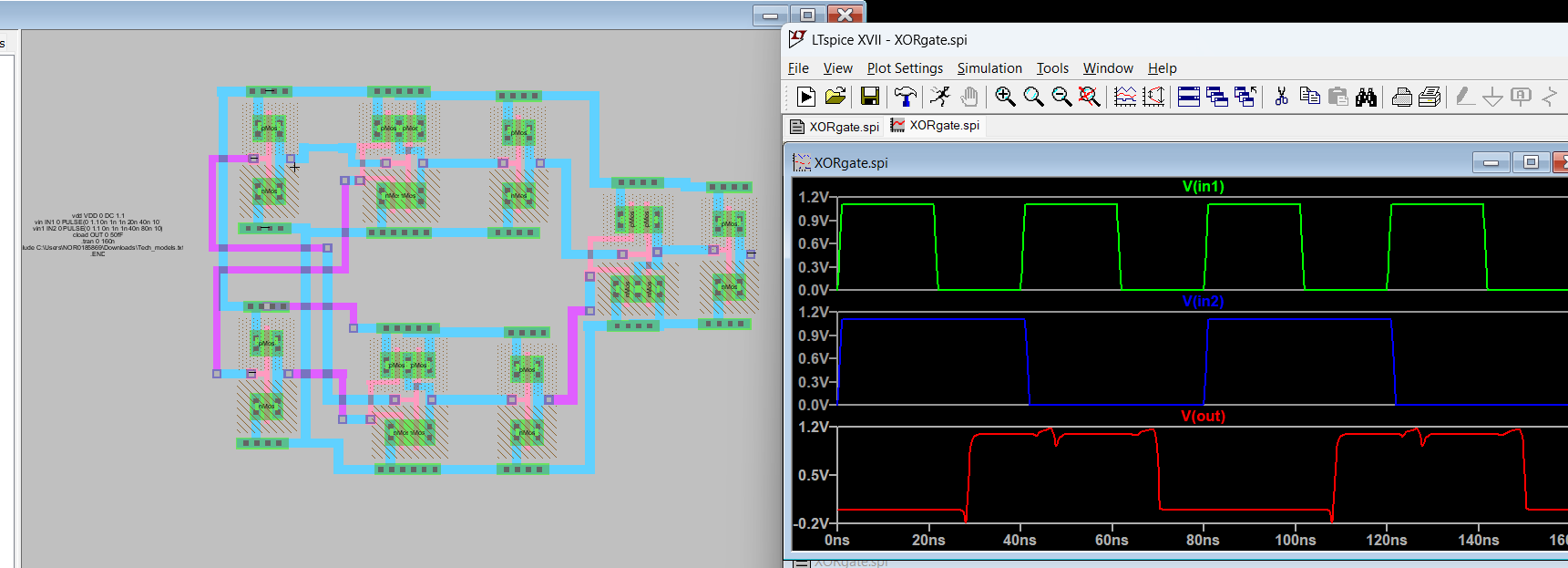
AND+Test



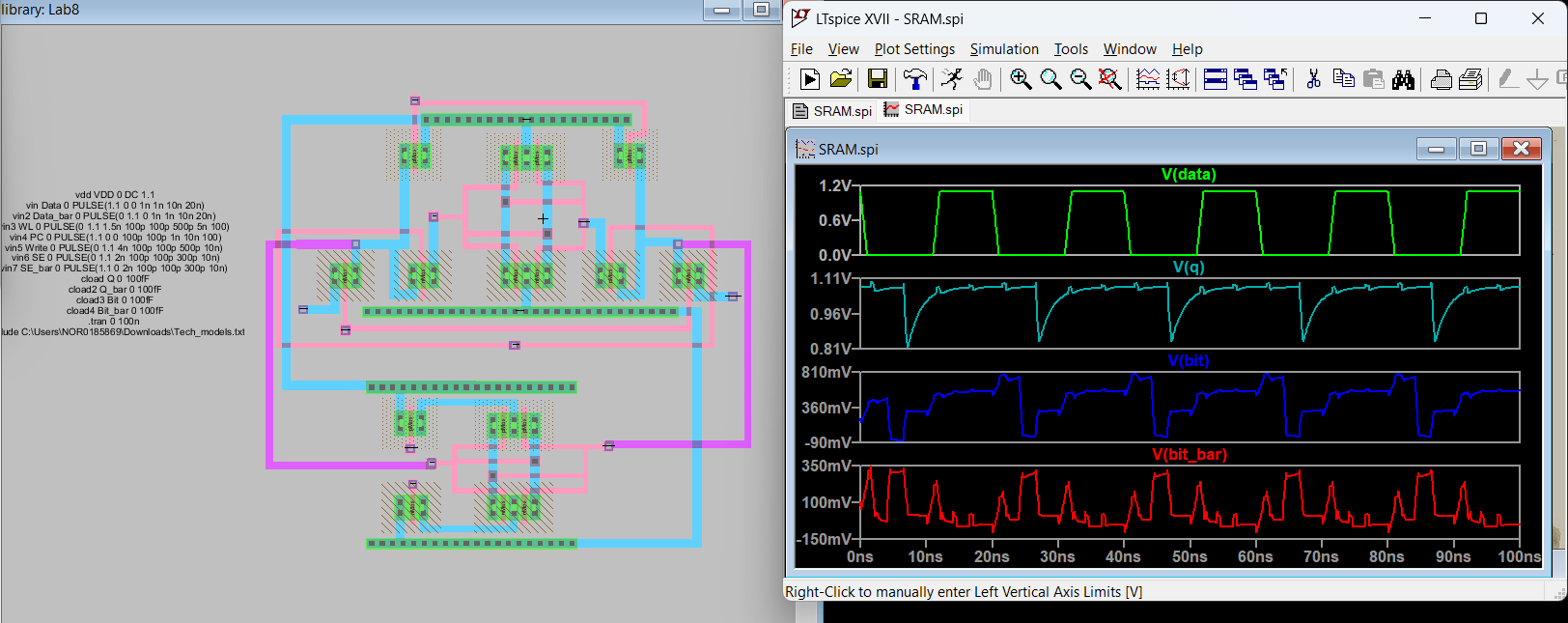
NOR+Test



XOR+Test



SRAM+Test



The System

