

Credit Hours System CMPN301 Computer Architecture



Phase 1 Report

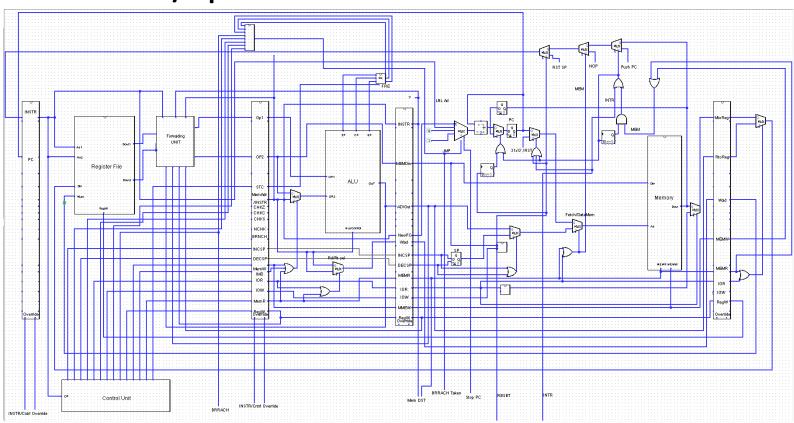
Submitted to: Eng. Maheed Hatem

Eng. Ahmed Soliman

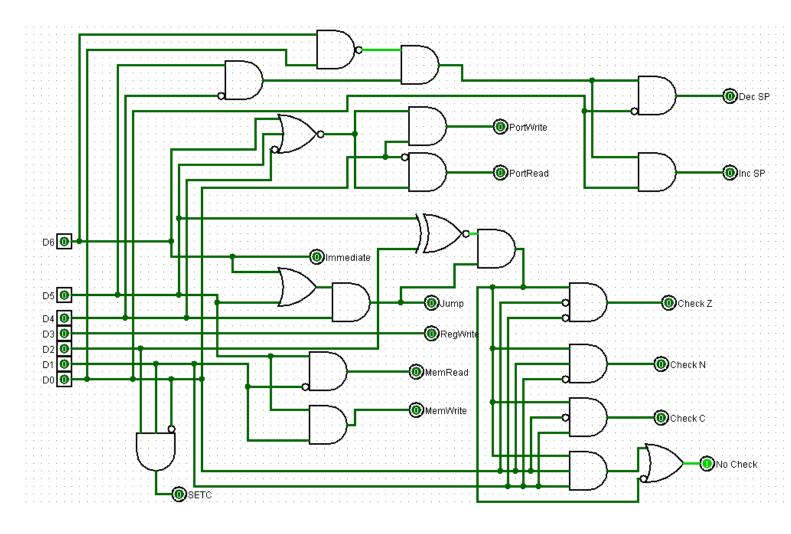
Team Members:

Noureldin Ashraf Elsayed	(1190219)
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Schematic/Pipeline:



Control Unit:



OpCodes:

D31~D25 D24~D21 D20~D17 D16D15D14D13 D15D14D13~D0
OpCode RD RT RS Immediate

Immediate D31 Memory D30

Jump D29 AND (D30 OR D31)

Bits in Red are Overlapped

Mnemonic	Opcode
NOP	0000000
HLT	000001
SETC	0000110
NOT	0000011
INC	0000100
OUT	0011000
IN	0010001
MOV	0001111
SWAP	0001101
ADD	0001001
SUB	0001010
AND	0001011
IADD	1001001
PUSH	0100010
PUSHI	1100010
POP	0101001
LDM	1001000
LDD	1101001
STD	1100011
JZ	1010000
JN	1010001
JC	1010010
JMP	1010011
CALL	1010100
RET	0110001
INT	1110001
RTI	0110001