

EE-230 Analog Circuits Lab Schedule

DATE	Lab	Experiment
4 Jan 5 Jan	Lab-1	1. Video on basics of AFG, DSO, Power Supply, and DMM 2. RC time and frequency response 3. Basics of probing the circuit 4. Half wave rectifier 5. Non Inverting (Buffer + amplifier)
11 Jan 12 Jan	Lab-2	Inverting Differentiator circuit Mathematical Equation Solver (LDE) Schmitt Trigger
18 Jan 19 Jan	Lab-3	Center tapped full wave rectifier Precision Rectifier using Opamps Multivibrator (Monostable, Astable)
25 Jan 26 Jan	Lab-4	Simulation based: Log Amplifier Antilog Amplifier Square root amplifier
01 Feb 02 Feb	Lab-5	Non-idealities in OPAMP using TL084 and LM324
08 Feb 09 Feb	Lab-6	Implementation: Log amplifier and Antilog Amplifier + Analog multiplier, if doable
15 Feb 16 Feb	Lab-7	Application around non-idealities (Log-Antilog amplifier or Square root) [Tentative]
17th Feb	Midsem Exam	