



**EE214 Digital Circuits Laboratory**  
Mid-Semester Exam  
Department of Electrical Engineering, IIT Bombay

Instructions For Midsem

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### General Instructions:

1. Download all necessary resources (**LAB handouts, supporting documents and the submissions [necessary vhdl codes] for the previous experiments and homework**) from Moodle and EE214 Webpage well in advance to your local storage.
2. The venue for the exam is **WEL-1, WEL-2, WEL-4, TI-DSP Lab and LA002**. The seating arrangement for the same will be posted in Moodle current week section.
3. Students not using the WEL systems are advised keep all previous in-lab resources in local storage in an organized way.
4. Students using the WEL systems can bring **LAB handouts, supporting documents and the submissions [necessary vhdl codes] for the previous experiments and homework** in a pendrive or upload your files in a drive and can download the files in the first 5 mins that is being allotted to you. You will not allowed to download after that.
5. The midsem will be having both **RTL Simulation and Board Implementation**, so the students using their personal laptops should make sure that there are no issues regarding your **laptop and Quartus**. **P.S. People having any kind of issues kindly mail us in ASAP.**
6. Do not forget to bring your laptop charger and charge your laptops fully before reaching the examination hall.
7. **Any electronics devices other than your laptop is strictly prohibited for use.**
8. Mobile phones are only allowed at the starting to download the provided files and at the end of the examination for submission.
9. During the test any kind of **hardcopy materials** are not allowed. You will be given rough sheets with your question paper.
10. As there will be **pen-paper design** please bring all necessary materials for writing the exam.
11. **Institute ID card** is mandatory and smart watches will not allowed inside examination hall.
12. Please be seated according to the seating arrangement in your respective venue by **2:00 pm**.
13. **No student to be allowed to write the midsem exam after 2.00 pm**

### Exam Instructions:

1. Students are supposed to download the provided files for the midsem exam by 2:05 pm.
2. Internet will be not allowed after 2:05 pm. Anyone violating this instruction will be prohibited to process with the exam.
3. **Switch off your mobile phones and keep it on the table and make sure your laptop is in airplane mode after the download. Question paper will only be distributed after that.**
4. If anyone is caught using internet between the duration of exam strict action will be taken.
5. You can switch on your internet only for uploading the files when you are instructed to do so.
6. **The Final Design (VHDL Design files, qpf file) in a zip format only (file format: Rollnumber\_Name.zip) should be uploaded in Moodle.**
7. The question paper and the hard copy of the handwritten design will be collected by your respective TA.
8. Demonstrate your pen-paper design to your respective TA.
9. Don't leave the venue until and unless your TA instructs you to do so.
10. The exam will be of **2 hours 30 minutes** and extra **1 hour** for evaluations.