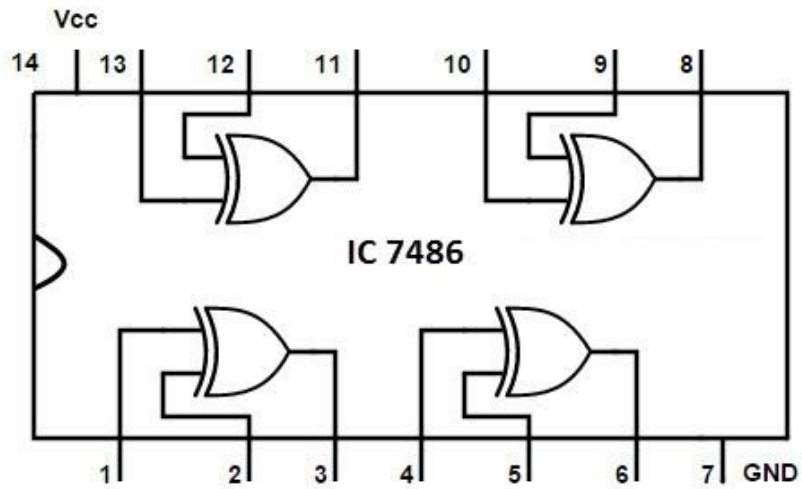
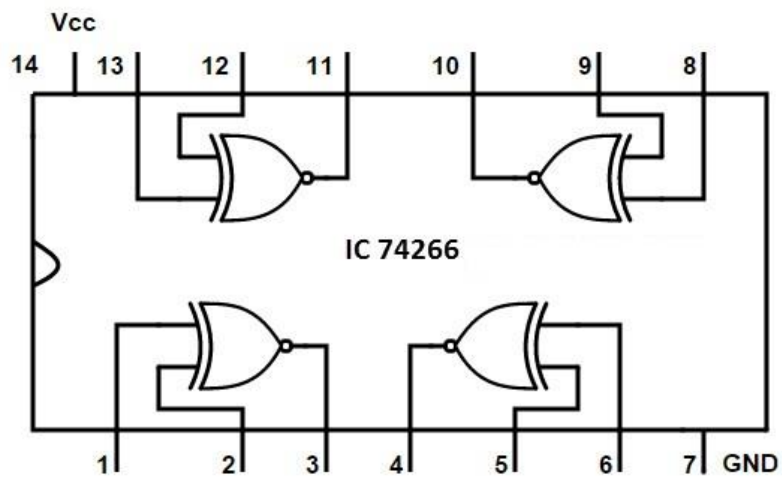


All Pin Diagrams

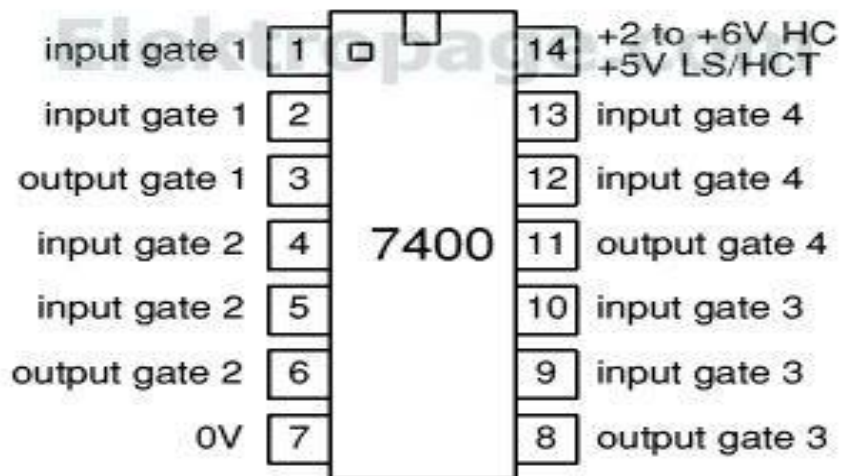
XOR Gate:



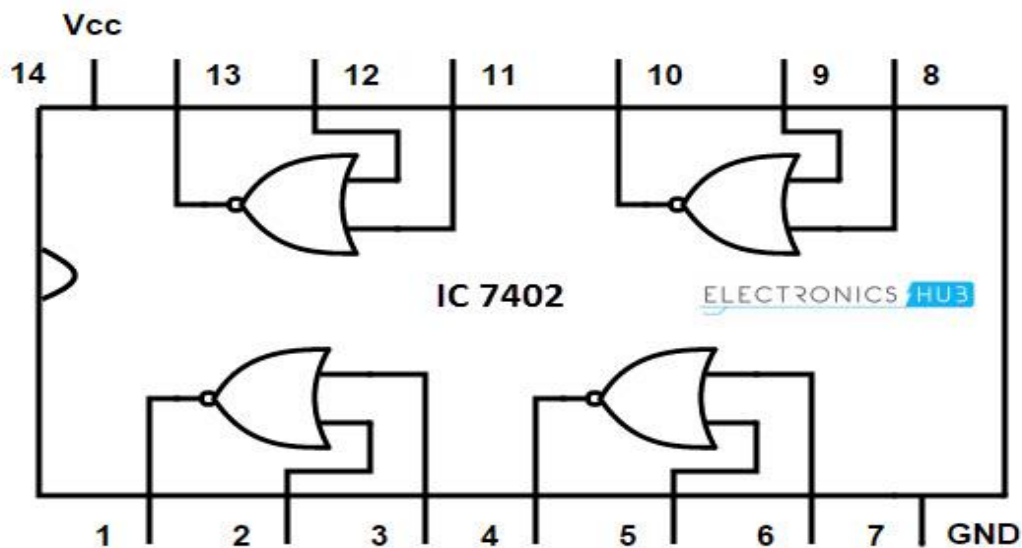
XNOR Gate:



NAND Gate:



NOR Gate:



8*1 MUX:

