

Chrys Sean T. Sevilla

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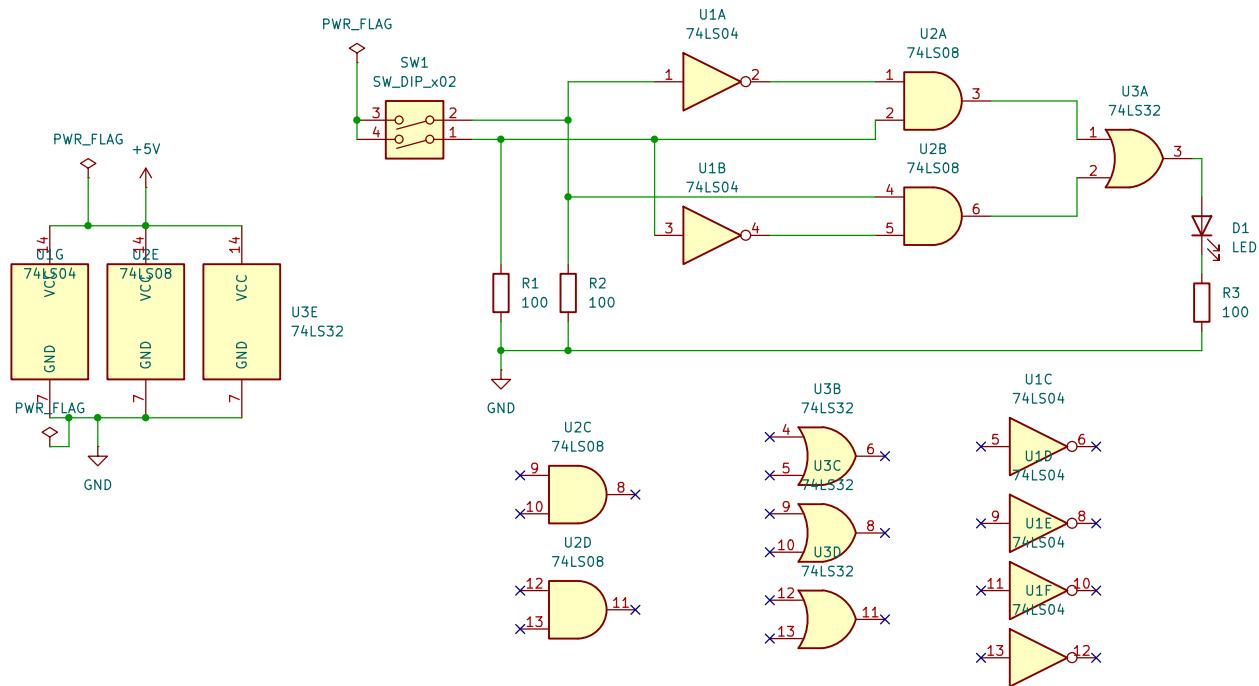
**Title: Logic Circuit Example #1**

Size: USLetter Date: 2025-06-24

KiCad E.D.A. 9.0.2

Rev: 1.0

Id: 1/1



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File: SEVILLA\_PA1-2.kicad\_sch

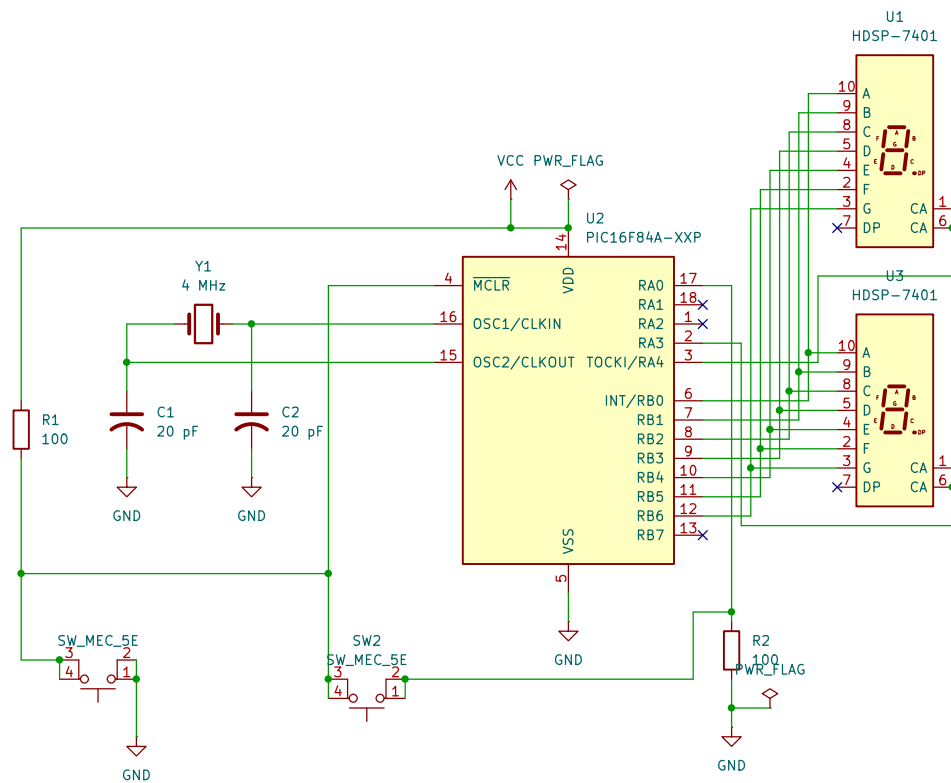
**Title: Logic Circuit Example #2**

Size: USLetter Date: 2025-06-24

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### Title: Logic Circuit Example #3

Size: USLetter Date: 2025-06-24  
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