POKHARA ENGINEERING COLLEGE

		Internal A	ssessment Examina	ation	
		Level: Bachelor Programme: BCE Course: Embedded System	Semester - Fall	Year : 2025 Full Marks: 100 Pass Marks: 45	
				Time : 3hrs.	
		Candidates are required to as practicable.	give their answers in th	heir own words as far	
		The figures in the margin is	ndicate full marks.		
		Attempt all the questions.			
1.	a)	Discuss the essential features general-purpose computing sy	/stems.	edded systems rom	8
	b)	List and describe at least the systems.	ree key application do		7
2.	a)	Draw and explain the AVR components			3+5
	b)	Write a program to store a memory of an AVR			7
3.	a)	What is an RTOS, and happlications?			2+5
	b)	Describe the conditions that embedded systems.			8
4.	a)	What are the primary differ modeling styles in VHDL? Ex	plain briefly with some	ral and behavioral simple examples.	2+5
	b)	Write a VHDL code to implem	ent a Multiplexer.		8
5.	a)	A company is designing a sma sensors (temperature, motion, locks) communicate with a ce wired and wireless communica	gas, etc.) and actuators entral hub. The system	must support both	4+5
		Questions:		tagale (wired and	
	1.	Identify and justify suitable wireless) for this system.	communication pro	locois (n	

Compare SPI, 12C, and UART in terms of data rate, complexity, and

ii.

wireless) for this system.

power consumption for sensor communication.

Difference between LoRa and Bluetooth.

GSM/GPRS

		6
a)	What are the key layers of the TCF/IF moder. Briefly desired	8
b)	functions. Describe the working principle of the MQTT protocol and explain its key components	7
	Write short notes on any two:	2×5
a)	Sensors and Actuators	
b)	Timers and Counters in AVR	