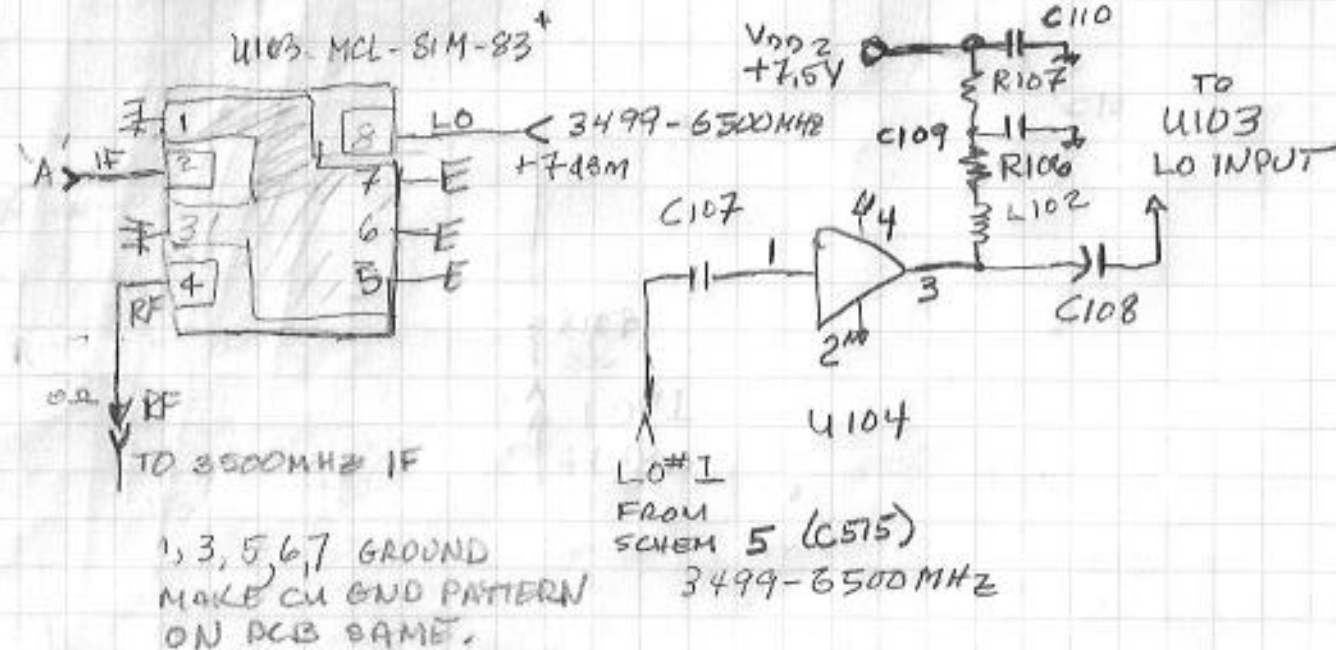
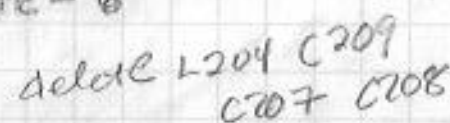


18 Feb 2018


$$V_{DD2} = +7.5V @ 80mA$$

3500 MHz IF

(civ)

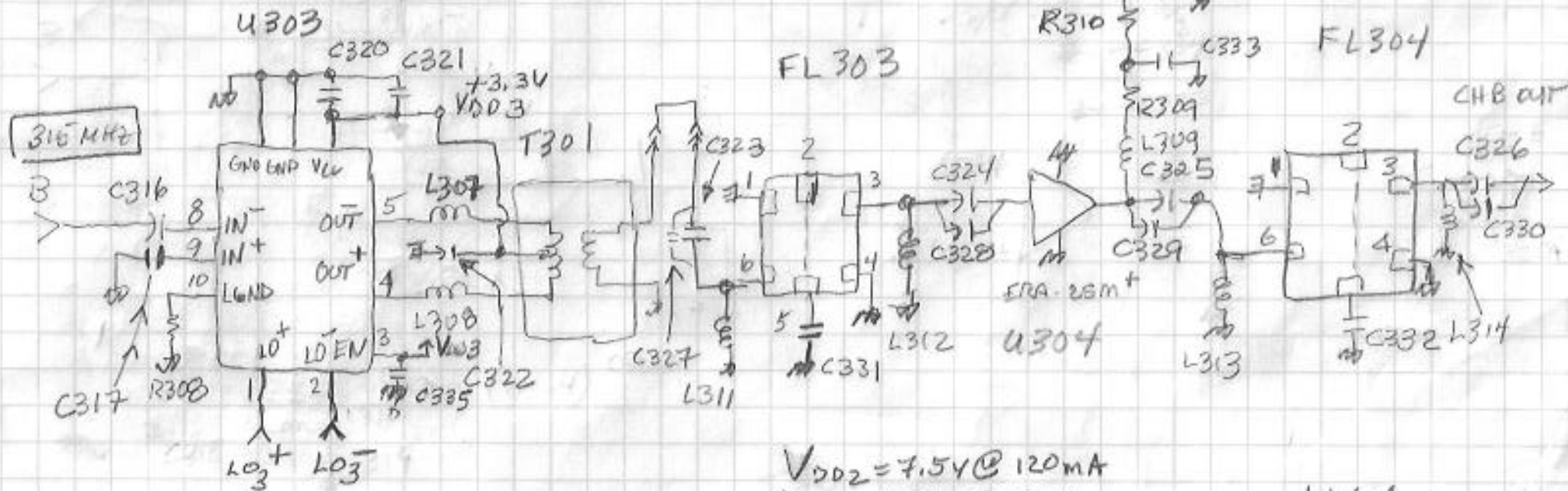
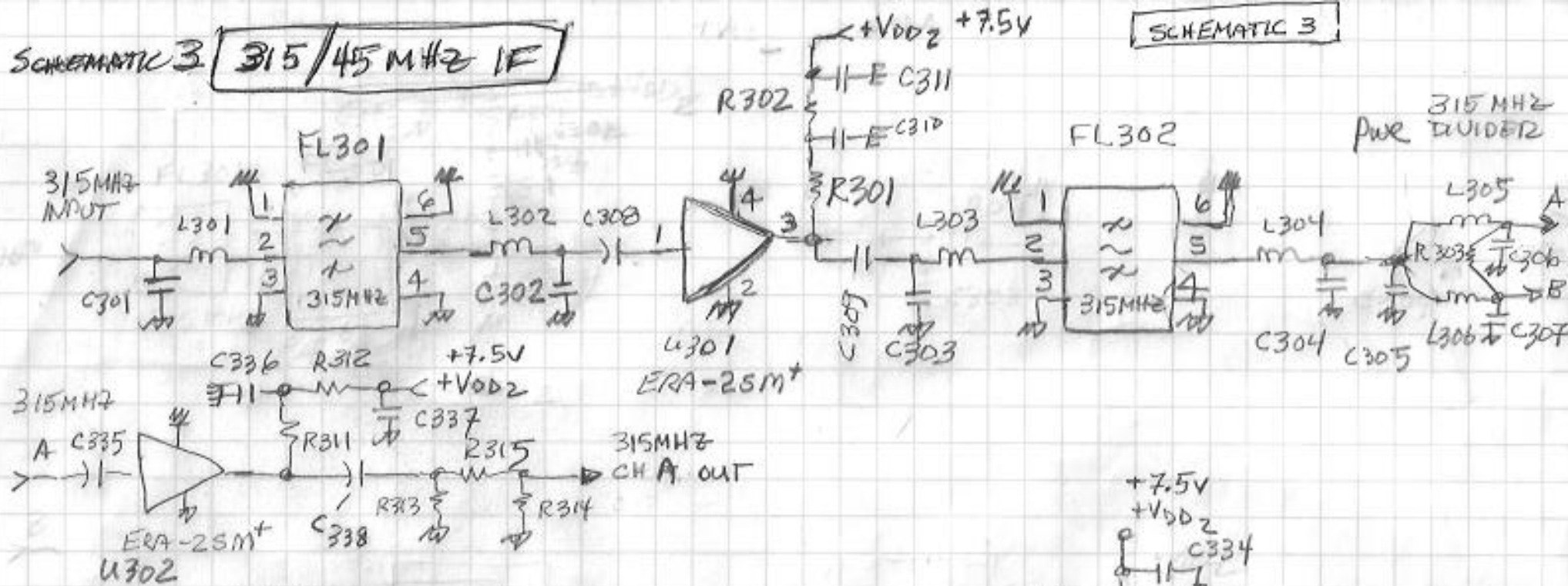


Deleted:

L204, C207-C209

SCHEMATIC 3 **315/45 MHz IF**

SCHEMATIC 3

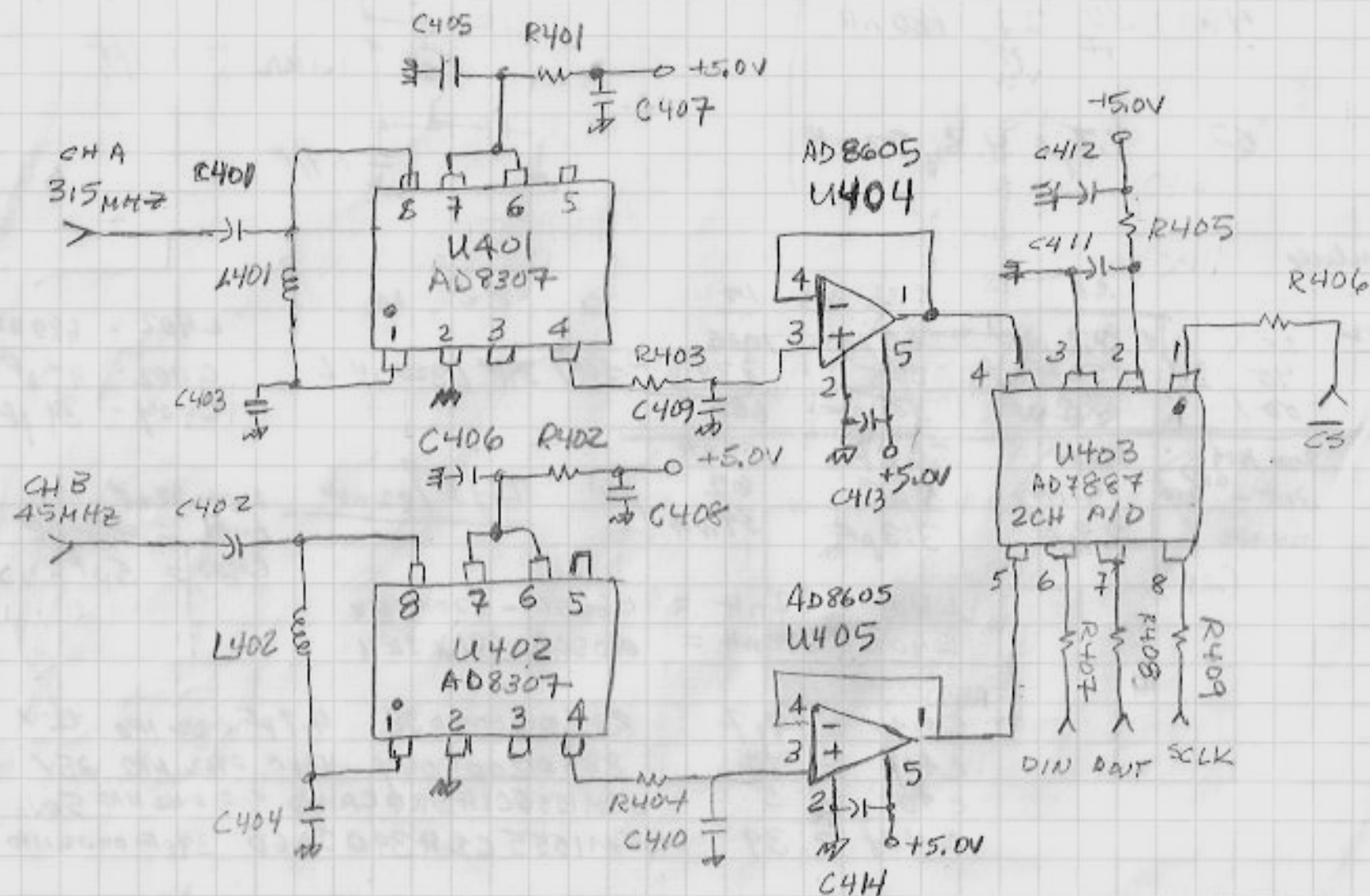


$V_{DD2} = 7.5V @ 120mA$
 $V_{DD3} = 3.3V @ 40mA$

deleted
 L310
 C318, C319

SCHEMATIC 4 BASEBAND

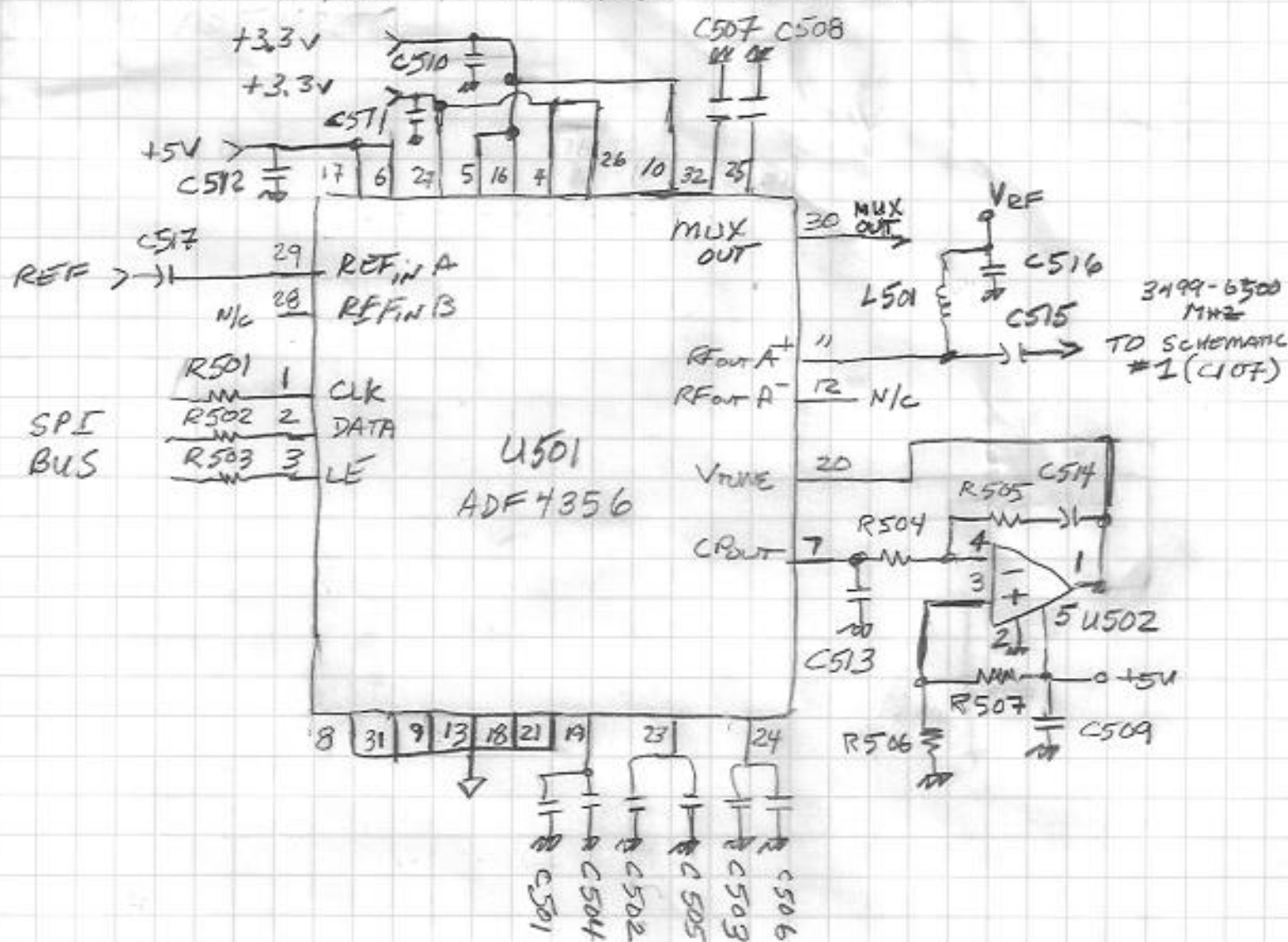
REVISED 2 FEB 2019



SYNTHESIZER'S

SCHEMATIC 5

LO1 3499 - 6500 MHz 1 MHz STEPS

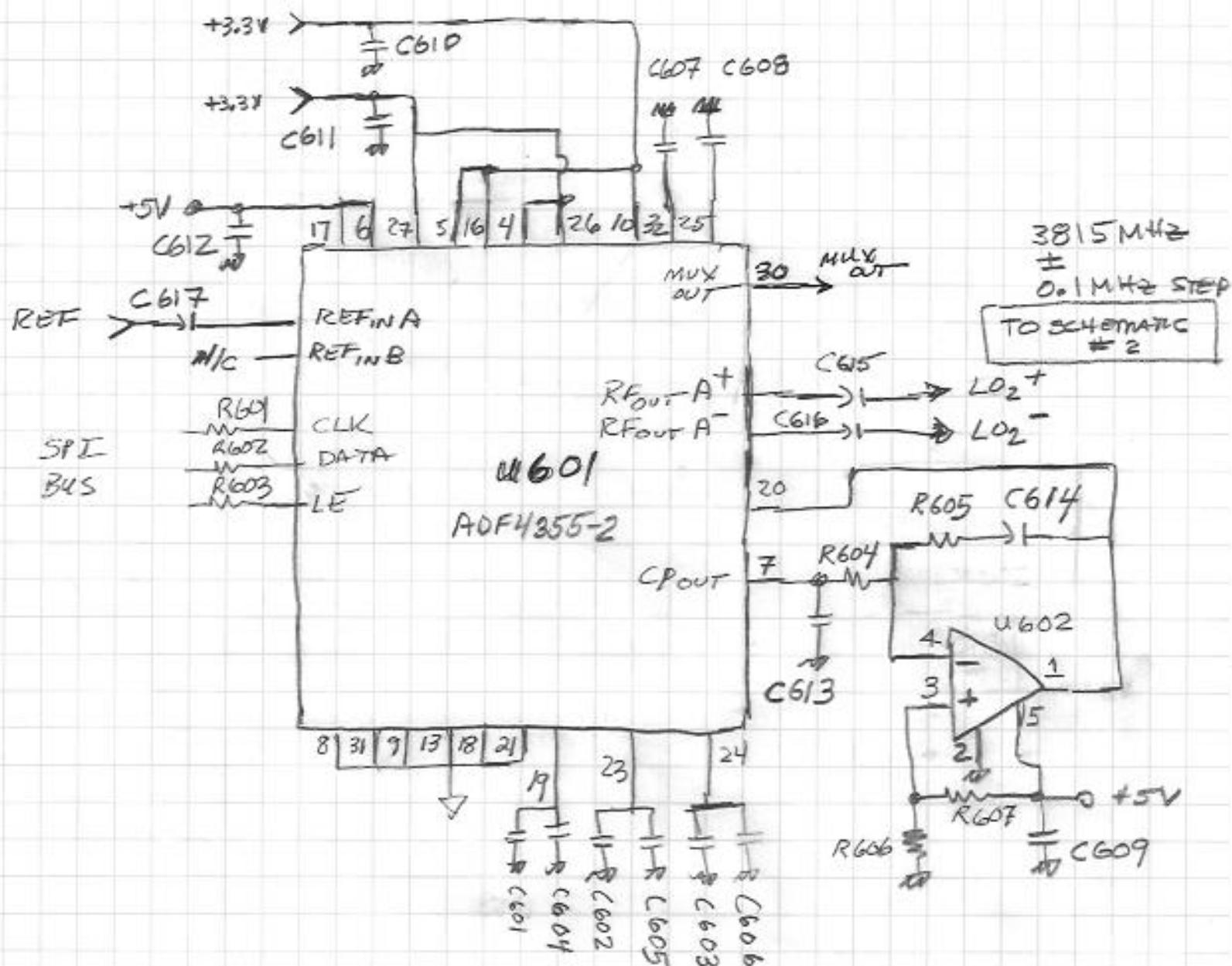


+ 5.0V @ 94 mA

+ 3.3V @ 139 mA

SYNTHESIZERS

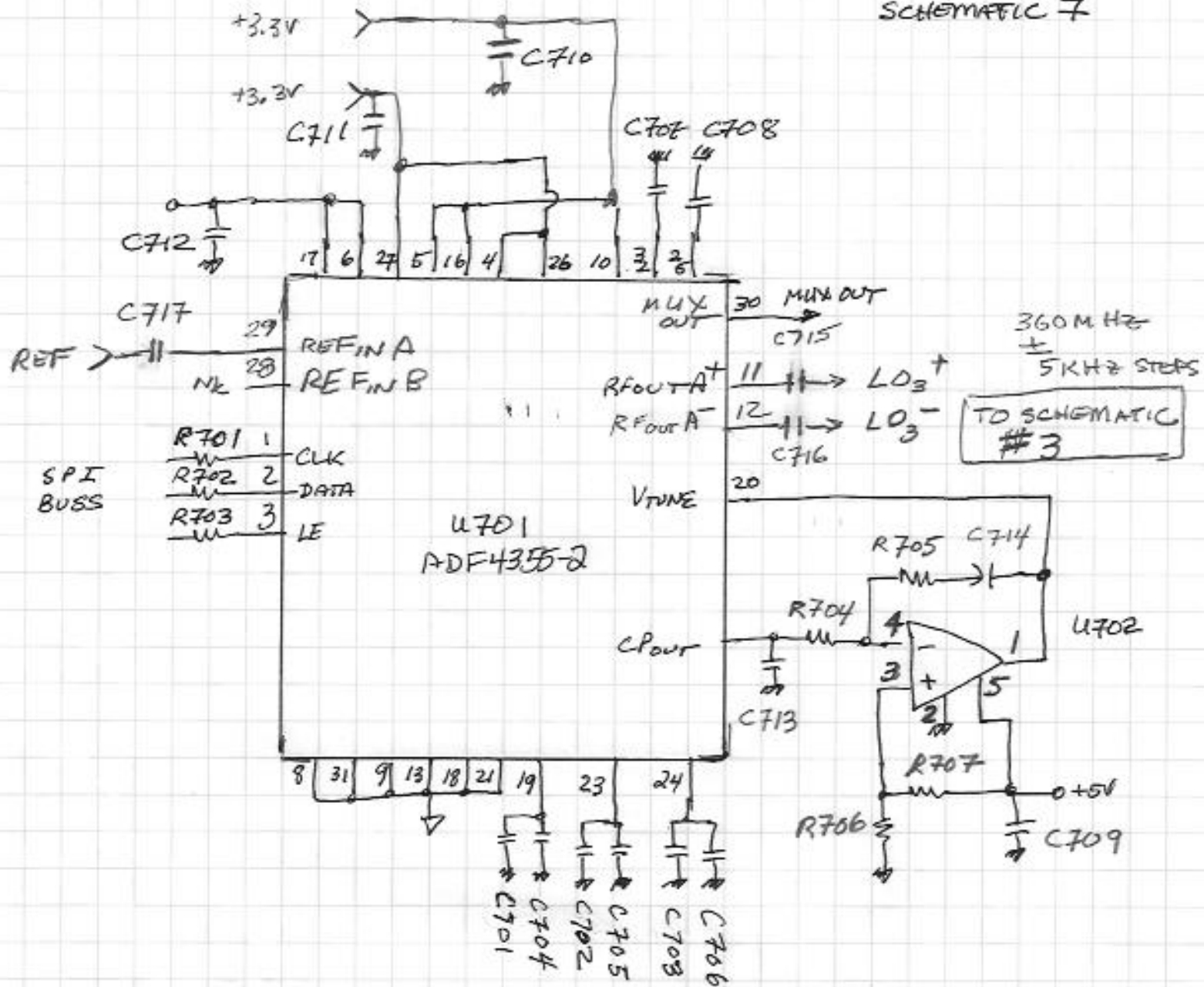
SCHEMATIC 6



+5.0V @ 94 mA

+3.3V @ 139 mA

SYNTHESIZER SCHEMATIC 7

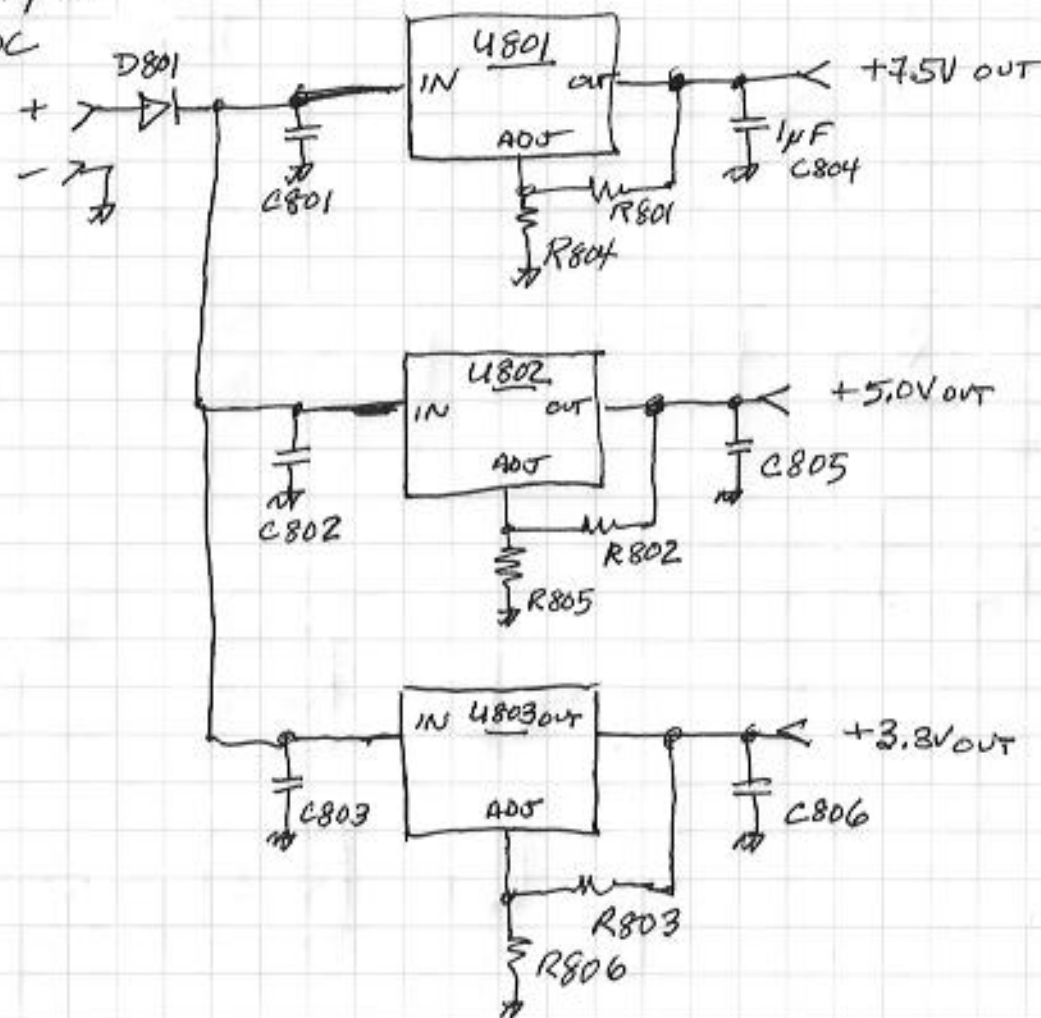


+5.0V @ 94 mA
+3.3V @ 139 mA

POWER REGULATION (one suggested solution)

SCHEMATIC #8

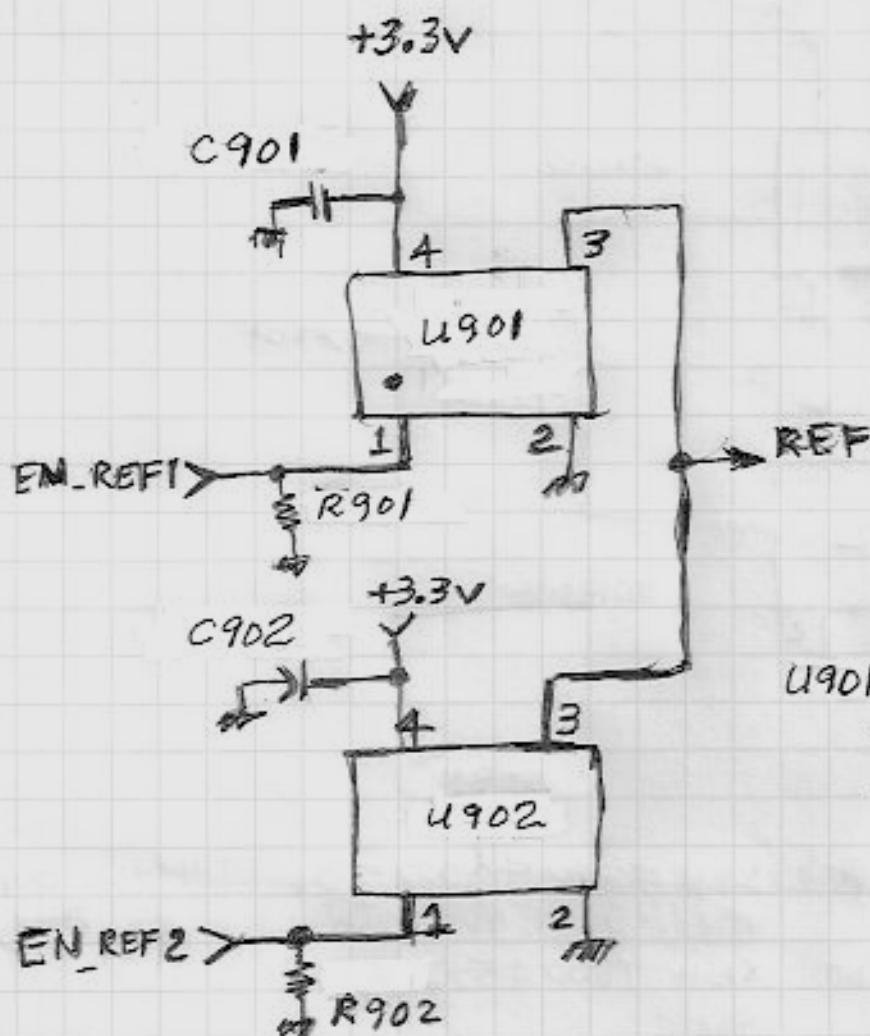
FROM
AC Adapter
12VDC



ALL THREE REGULATORS MUST
BE HEAT-SINKED. U803 WILL
BE DISSIPATING APPROX 4.3 WATTS.
AND WILL NEED A HEATSINK
TO KEEP THE CASE TEMP $\leq 125^{\circ}\text{C}$
A HEATSINK OF 15°C/W @JA
WILL KEEP $T_J \leq 125^{\circ}\text{C}$ FOR $T_A = 60^{\circ}\text{C}$.

SCHEMATIC 9 DUAL-FREQUENCY REFERENCE

2 FEB 2019



WHEN EITHER EN_REF1 OR EN_REF2 IS "HIGH", THEN U901 OR U902 IS ENABLED. SOFTWARE MUST NOT SET BOTH INPUTS AT THE SAME TIME.

U901	ASE - 40.000 MHz - L-C-T	DIGIKEY \$1.18	MOUSER \$1.52	ABRACON
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U902	ASE - 48.000 MHz - L-C-T	\$1.71	\$1.24	ABRACON
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C901	0.01 μ F, X7R, 0402 SIZE	WURTH 885012205031
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C902

R901
R902

150 K, 0402	VISHAY CRCW0402150KJNED
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