

# FOR Shift out Code - test - 6 C

R0 R1 R2 R3 R4 R5 R6 R7 R8 R9 R13  
 BYTE 0[] = { 0x00, 0x00, 0x00, 0x00, 0x00, 0x00 }  
 1[] = 0x00  
 2[] = 0x00  
 3[] = 0x00  
 R13 { 0xFF }  
 R13 { 0xFF }  
 R13 { 0xFF }  
 R13 { 0xFF }

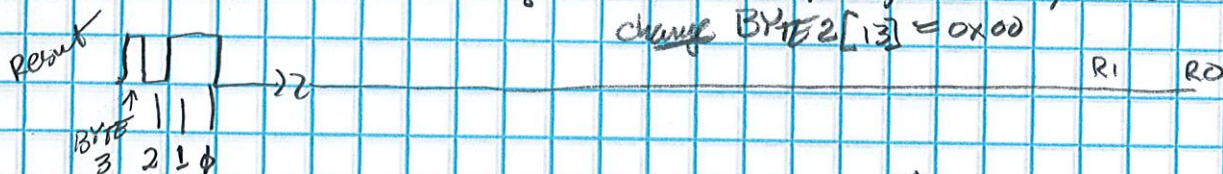
Check Register Order

SR

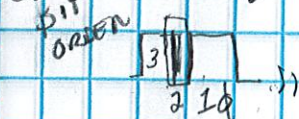


So the Register order is ok

Check BYTE ORDER: Change R13: BYTE 0[3], BYTE 1[3], BYTE 3[3] = 0xFF  
 Change BYTE 2[3] = 0x00

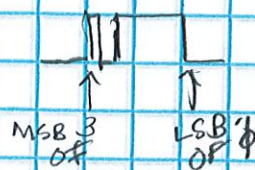
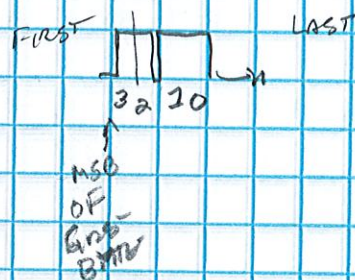


Change BYTE 2 to 0xF0, so Upper Nibble = F, Lower = 0



Change BYTE 2[3] to 0xF3

Change BYTE 3[3] to 0xF0  
 BYTE 0, 1, 2 = 0xFF



So Register or Word Order is correct (R13 first → R0)

Byte order within a word R13 leads, R13 follows  
 Bit order is MSB first