

V3021

Dual Channel 10 Gigabit Ethernet FPGA AMC

Benefits

Provides 10 Gigabit Ethernet line-rate processing via its high performance architecture

Improves overall system performance by providing additional processing capabilities

Ideal for high bandwidth applications that require real time digital signal processing

Features

Single-width mid-size or full-size AdvancedMC

Available in a range of user-programmable Xilinx Virtex-5 FPGA options

High performance SDRAM and SRAM memory architecture

Two 10 Gigabit Ethernet SFP+ optical ports

10/100/1000BASE-T RJ-45 port

Fat pipe fabric for data exchange

Pigeon Point controller for maximum interoperability

Software in-system programming for field upgrades

Thermal sensors for monitoring card temperature

RS-232 utility for fast application debugging

Overview

Available in single-width AdvancedMC form factor, this high performance packet processing module features dual 10 Gigabit Ethernet SFP+ optical ports and is available in a range of Xilinx Virtex-5 FPGA options providing scalable processing performance. Its memory architecture is based on multiple independent high-speed SDRAM and SRAM memory interfaces that enable advanced 10 Gigabit Ethernet network traffic processing. The V3021 supports PCI Express or XAUI fat pipes for data exchange. It comes standard with expressXG Development Framework, an advanced FPGA development infrastructure that gives programmers the advantage to develop their own high bandwidth applications rapidly and efficiently.



