

## Transcript of records

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**Name, First Name** Schmid, Michael (5015104)  
**Matriculation Number** 17-180-282  
**Course of Study** Electrical Engineering

Assessment	Status
To conferment of the bachelor diploma	passed

<b>ECTS Points required</b>	180
<b>ECTS Points acquired</b>	200

ECTS per Category	ECTS	
	required	acquired
Engineering	148	168
Fundamentals of Electrical Engineering	64	72
Advanced Topics of Electrical Engineering	32	44
Mathematics	30	34
Science	14	14
Communication and Language	12	12
Society, Economy and Law	8	8
Bachelor Thesis Electrical Engineering	12	12

The detailed terms may be found in the Examination Regulations.

Modules attended	Semester**	Mark	ECTS
<a href="#">Analysis 1a for Electrical Engineering</a>	AS 2017	5.0	4
<a href="#">Analysis 1b for Electrical Engineering</a>	AS 2017	5.5	4
<a href="#">Analysis 2a for Electrical Engineering</a>	SS 2018	6.0	4
<a href="#">Analysis 2b for Electrical Engineering</a>	SS 2018	5.5	4
<a href="#">Bachelor Thesis Electrical and Computer Engineering</a>	SS 2020	6.0	12
<a href="#">Business and Law 2</a> - Information-, Technology- and Licence Law - Management Simulation	SS 2019	5.0	4
<a href="#">Certificate in Advanced English is equivalent to English: Where Tech Meets BEC</a>	Transfer	*BN	4
<a href="#">Communications Engineering 1</a>	AS 2018	5.0	3
<a href="#">Communications Engineering 2</a>	SS 2019	5.5	3
<a href="#">Complex Numbers and Fourier Series</a>	SS 2018	5.5	4
<a href="#">Computer Engineering 1</a>	SS 2018	5.0	3
<a href="#">Computer Engineering 2</a>	AS 2018	5.5	3
<a href="#">Control Systems 1</a>	AS 2018	5.5	3
<a href="#">Control Systems 2</a>	SS 2019	5.5	3
<a href="#">Deep Learning</a>	SS 2020	5.0	4
<a href="#">Digital Circuits</a>	AS 2017	5.0	3
<a href="#">Digital Design</a>	SS 2019	5.5	3
<a href="#">Digital Image Processing 1</a>	AS 2019	6.0	4

<a href="#">Digital Image Processing 2</a>	SS 2020	5.5	4
<a href="#">Digital Microelectronics</a>	AS 2019	5.0	4
<a href="#">Digital Signal Processing 1</a>	AS 2019	6.0	4
<a href="#">Digital Signal Processing 2</a>	SS 2020	5.0	4
<a href="#">Economics and History of Engineering</a> - History of Engineering and Appraisal of Consequences of Engineering - Economics and Economic Policy	SS 2018	5.5	4
<a href="#">Electrical Engineering 1</a>	AS 2017	6.0	3
<a href="#">Electrical Engineering 2</a>	SS 2018	6.0	3
<a href="#">Electrical Engineering 3</a>	AS 2018	4.5	3
<a href="#">Electrical Engineering 4</a>	SS 2019	5.0	3
<a href="#">Electronics 1</a>	AS 2018	5.5	3
<a href="#">Electronics 2</a>	SS 2019	5.5	3
<a href="#">Embedded Software Engineering 1</a>	AS 2019	5.0	4
<a href="#">Embedded Software Engineering 2</a>	SS 2020	5.0	4
<a href="#">English: The World of Science</a>	AS 2017	5.5	4
<a href="#">Functions of Several Variables</a>	SS 2019	5.0	4
<a href="#">Integral Transformations</a>	AS 2018	5.0	2
<a href="#">Java for C++ Programmers</a>	AS 2018	5.0	3
<a href="#">Lab Electrical Engineering 1</a>	AS 2017	*BN	2
<a href="#">Lab Electrical Engineering 2</a>	SS 2018	*BN	2
<a href="#">Linear Algebra: Introduction</a>	AS 2017	5.0	4
<a href="#">Mathematical Seminar 1</a>	SS 2019	5.5	2
<a href="#">Mathematical Seminar 2</a>	SS 2020	5.5	2
<a href="#">OO Analysis and Design</a>	SS 2019	5.5	3
<a href="#">Physics 1</a>	AS 2017	6.0	4
<a href="#">Physics 2</a>	SS 2018	5.5	4
<a href="#">Physics 3</a>	AS 2018	5.5	4
<a href="#">Practical Physics</a>	SS 2019	6.0	2
<a href="#">Programming in C</a>	AS 2017	4.5	4
<a href="#">Programming in C++</a>	SS 2018	4.5	4
<a href="#">Project Electrical Engineering</a>	AS 2019	5.5	8
<a href="#">Project Management and Software Engineering</a>	AS 2018	4.0	3
<a href="#">Python</a>	SS 2019	6.0	3
<a href="#">Signals and Systems 1</a>	AS 2018	5.5	3
<a href="#">Signals and Systems 2</a>	SS 2019	5.5	3
<a href="#">Statistical Machine Learning</a>	AS 2019	5.0	4
<a href="#">Team Communication for Engineers</a>	SS 2018	5.5	4
<a href="#">Theory of Probability and Statistics</a>	AS 2018	5.5	4

\*BN = passed, \*nBN = failed, \*BT = attended, \*nBT = not attended

\*\*SS=Spring Semester, SuS=Summer Semester, AS=Autumn Semester, WS=Winter Semester

Compulsory Modules*	Status
<a href="#">Bachelor Thesis Electrical and Computer Engineering</a>	completed
<a href="#">Project Electrical Engineering</a>	completed

\*Module passed according to requirements.

Compulsory Modules (Participation)*	Status
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\*Module participation with all requirements. Mark not relevant.