Shoot1

							EEPRO HLT	OM 1 MEM ADDRES REG IN (MI)	RAM IN (RI)	RAM OUT (RO)	INSTRUCTION REG OUT(IO)	INSTRUCTION REG IN(II)	A REG IN (AI)	A REG IN (AO)	EEPRO SUM OUT Σ SM	M 2 SUBSTRACT (SU)	B REG IN (BI)	OUTPUT REG IN (OI)	COUNT ENABLE (CE)	PRG COUNTER OUT (CO)	JUMP (J)
FETCH	×	x x				0 0 0 1		1 0	0	0 1	0	0	0	0	0	0	0	0	0	1	0
NOP	0 0 0	0	0	0	0	1 0 1 1 0 0	0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0
LOAD A	0 0 0	0	0 0 0	1	0	1 0 1 1 0 0	0	1 0 0	0 0 0	0 1 0	1 0 0	0 0 0	0 1 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0	0 0 0
ADDITION	0 0 0		1 1 1	0	0	1 0 1 1 0 0	0	1 0 0	0 0 0	0 1 0	1 0 0	0 0 0	0 0 1	0 0 0	0 0 1	0 0 0	0 1 0	0 0 0	0 0 0	0 0 0	0 0 0
SUBSTRACTION	0	0		1	0	1 0 1 1 0 0	0	1 0 0	0 0	0 1 0	1 0 0	0 0 0	0 0 1	0 0 0	0 0 1	0 0 1	0 1 0	0 0 0	0 0 0	0 0	0 0 0
STORE A	0		0	0 0 0	0	1 0 1 1 0 0	0	1 0 0	0 1 0	0 0 0	1 0 0	0 0 0	0 0 0	0 1 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0
LOAD IMMEDIATE	0	1 1 1	0	1	0	1 0 1 1 0 0	ō	0 0 0	0 0 0	0 0 0	1 0 0	0 0 0	1 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0	0
JUMP	0	1 1 1	1	0	0	1 0 1 1 0 0	0	0 0 0	0 0 0	0 0 0	1 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0	1 0 0
ОИТРИТ	1 1 1		1	0	0	1 0 1 1 0 0	0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	1 0 0	0 0 0	0 0 0	0 0 0	1 0 0	0 0 0	0 0 0	0 0 0
HALT SYSTEM	1 1 1	1	1	1	0	1 0 1 1 0 0	0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0	0 0 0