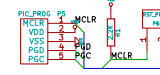


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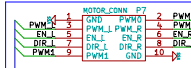
Programming Header

Programming Header

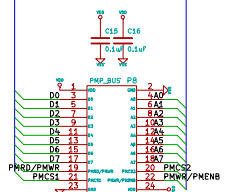
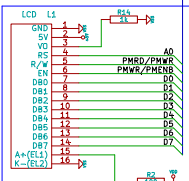
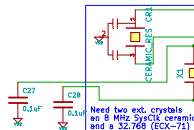


title: power.sch

Motor Controls

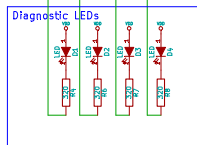
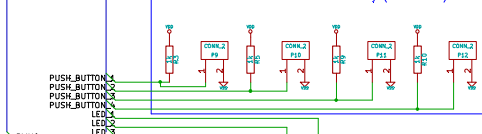


Oscillators



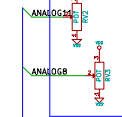
Parallel Bus Communication

Push Button Circuitry (active low)

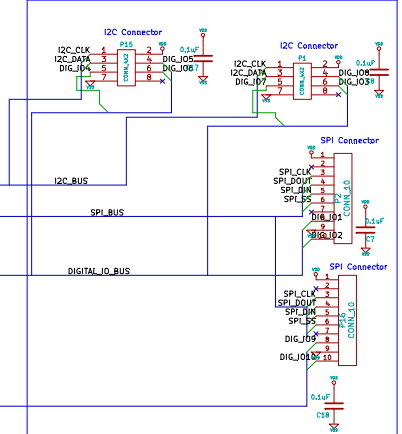


Diagnostics

Analog Input POTs



Serial Data Communication



These two connectors will be specifically for expansion for I/O signals. They each have 14 unique I/Os, and share 8 special I/Os corresponding to interrupts or timer pins. Of the unique pins, 6 are capable of taking in analog signals, while 8 are pure digital I/Os.

General I/O Connections. Will be used for sensors.

By: Jake Vander Ploeg
Completion Date: 10/15/2012
Review Date: 10/25/2012
Order Date: N/A

File: PIC32MX795F512L2.sch

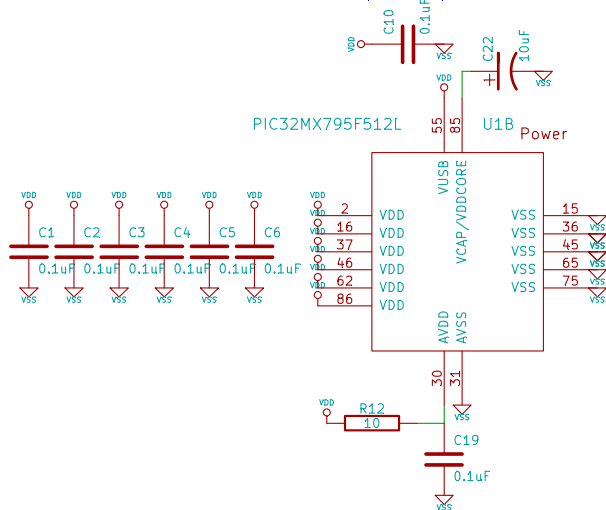
Sheet: /

Status: A2 Date: 9 mar 2013

KICad E.D.A. eeschema (2012-01-19 BZR 3256)-stable

Rev: 1/2

Cap on Vcap/Vddcore must be 1 ohm ESR



The schematic diagram illustrates the power supply section of the PCB. It features a 3.3V voltage regulator (U1) with its input connected to the VDD pin of the BAT_CONN P3 header. The regulator's output is connected to the IN pin of a power switch (P4), which is labeled PWR_SWITCH. The power switch has three positions: IN, OFF, and OUT. The output of the power switch is connected to the V+ pin of a power jack (J1). A 0.1uF capacitor (C9) is connected between the output of the regulator and ground. The power jack (J1) has three pins: V+, GND, and GND. The GND pins are connected to the common ground of the PCB.

Diagram showing four parallel connections from TP7, TP8, TP9, and TP10 to VSS.

File: power.sch		
Sheet: /power_sheet/		
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Size: A4	Date: 9 mar 2013	Rev:
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