11/11/24, 1:42 AM Makefile

SEM 3\Exp9\Makefile

```
1 # Compiler to use
   CC = qcc
2
3
  # Compiler flags
5 CFLAGS = -Wall -Wextra -g
 6
7 # Object files to compile
8 OBJS = main.o Bucket_Sort.o counting_sort.o Heap_Sort.o Merge_Sort.o Radix_sort.o
9
10 | # The final executable name
11 TARGET = Exp9_sorting_program
12
13 # Default target to build the executable
14 all: $(TARGET)
15
16 # Rule to link object files into the final executable
17
   $(TARGET): $(OBJS)
       $(CC) -o $(TARGET) $(OBJS)
18
19
20 # Rule to compile each .c file into a .o file
21 %.o: %.c
       $(CC) $(CFLAGS) -c $<
22
23
24 # Clean target to remove object files and the executable
25
   clean:
       rm -f $(OBJS) $(TARGET)
26
27
```