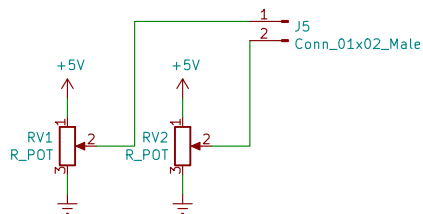
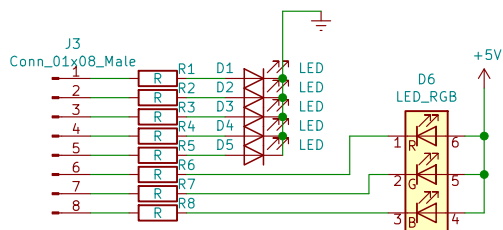


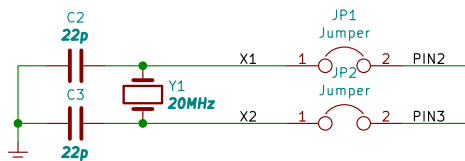
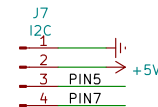
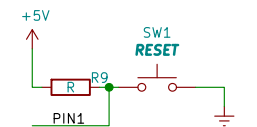
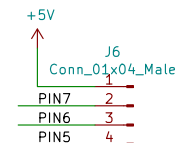
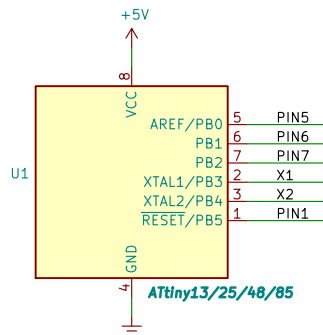
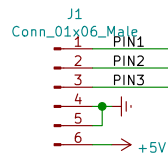
USB for Power Only



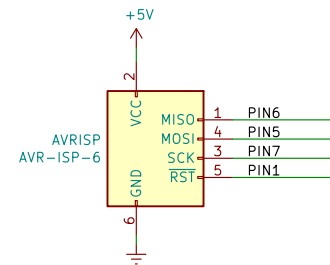
2 X ADC



LOGIC Test



Optional for use.
If using internal clock please short JP1 and JP2 to use PIN2 and PIN3 as I/O



Design Note:

Maximum 20MIPS @20MHz crystal.
Be careful for fusebit setting.

O ELECTRIC™

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