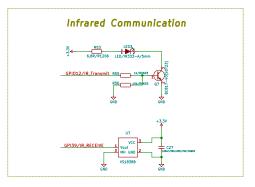
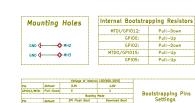


Softwar	e Selectab	le Pins
Interface	Signal	Pin
EMAC	EMAC_MDC_out EMAC_MDL.In EMAC_MDD_out EMAC_CRS_out EMAC_COL_out	Any GPIO
I2C	IZCEXTO_SCL_in IZCEXTO_SDA_in IZCEXT3_SCL_in IZCEXT3_SCL_in IZCEXT3_SDA_in IZCEXT0_SCL_out IZCEXT0_SDA_out IZCEXT3_SCL_out IZCEXT3_SCL_out	Any GPIO
General Purpose SPI	HSPIQ_In/_out HSPIC_In/_out HSPICCSQ_In/_out HSPI_CSQ_In/_out HSPI_CSQ_out HSPI_CSQ_out YSPIQ_In/_out YSPIC_IN/_out YSPIC_ISQ_out YSPIC_SQ_in/_out YSPI_CSQ_in/_out YSPI_CSQ_out YSPI_CSQ_out	Any GPIO

E	xtensio	n
#900/08/JUNE #900/08 JUNE #900	EXIT 1 2 3 5 6 6 7 7 8 6 7 9 10 5 6 7 9 10 5 6 7 9 10 7 11 12 12 12 12 12 12 12 12 12 12 12 12	##0.0000 ##0.00



Buttons



		Voltage of Interi	nal_LDO(VDD_SDIO)			
PTo	Default	3.3V	1.87	Bootstrapping Pins		
GPI012/WTDI	Pull-Down	0	1			
		Beotin	g Mode			
#fo	Default	SPI Flash Boot	Download Boot	Set	Settings	
GPI00	Pull-Up	1	0			
GPI02	Pull-Down					
		Debugging Log on I	JOTXD During Booting			
Pie	Default	USTXD Toggifing	UOTXD Silent			
GP1015/WT00	Pull-Up					
		Timing of	SDIO Slave			
Pie	Default	Felling-edge:Output	Falling-edgesinput Rising-edgesOutput	Rising-edgesinput Falling-edgesOutput	Rising-edgerinput Rising-edgerOutput	
GPI015/WT00	Pull-Up	0	0	1	1	
GPI05	Pull-Up		1		1	

