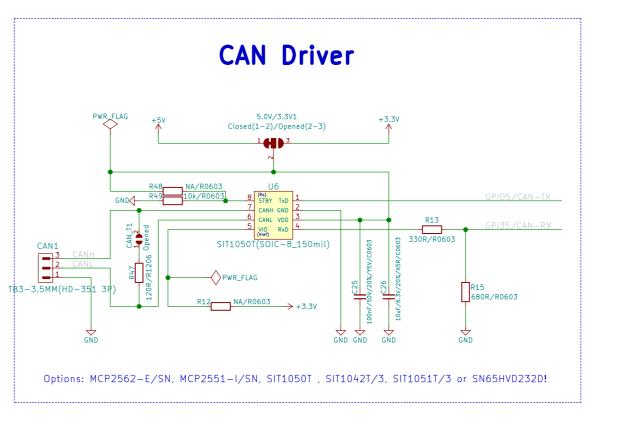


Mounting Holes



EMAC_CRS_out EMAC_CRS_out EMAC_CRS_out EMAC_COL_out 12CEXTO_SCL_in 12CEXTO_SDA_in 12CEXTI_SCL_in 12CEXTI_SDA_out 12CEXTO_SDA_out 12CEXTI_SDA_out 12CEXTI_SOA_out 12CEXTI_SOA_	Softwar	e Selectab	le Pins
EMAC_MDLin EMAC_MDQ_out EMAC_MDQ_out EMAC_CRS_out EMAC_CS_out EMAC_COL_out I2CEXTO_SCL_in I2CEXT1_SDL_in I2CEXT1_SDL_in I2CEXT1_SDL_in I2CEXT1_SDL_out ISPICIK_in/_out HSPICSL_out HSPICSL_out HSPICSL_out HSPICSL_out VSPIO_in/_out	Interface	Signal	Pin
12CEXTO_SDA_In 12CEXTO_SDA_In 12CEXTO_SCL_in 12CEXTO_SCL_out 12CEXTO_SDA_out 12CEXTO_SDA_O	EMAC	EMAC_MDI_in EMAC_MDO_out EMAC_CRS_out	Any GPIO
General HSPID_in/_out HSPICLSO_in/_out HSPLCSO_in/_out HSPLCSO_out HSPLCSO_out HSPLCSO_out VSPIQ_in/_out VSPIQ_in/_out VSPIQ_in/_out VSPICLK_in/_out	I2C	I2CEXTO_SDA_in I2CEXT1_SCL_in I2CEXT1_SDA_in I2CEXT0_SCL_out I2CEXT0_SDA_out I2CEXT1_SCL_out	Any GPIO
VSPL_CS1_out VSPL_CS2_out	Purpose	HSPID_in/_out HSPICLK_in/_out HSPLCSO_in/_out HSPLCS1_out HSPLCS2_out VSPIQ_in/_out VSPID_in/_out VSPICLK_in/_out VSPLCS0_in/_out VSPLCS1_out VSPLCS1_out	Any GPIO

GND 👉	♦ MH2		GPI02: Pull-		ll-Down	
GND (MH3		MTDO/GPI	015: Pu	Pull-Up		
OND (GND (MIL)		GF	P105: Pu	Pull-Up	
		Voltage of Intern	al LDO(VDD_SDIO)			
Pin	Default	3.3V	1.8V			
GPI012/MTDI			1	4		
		Booting	Mode		pping Pins	
Pin	Default	SPI Flash Boot			tings	
GPI00	Pull-Up	1	0			
GPI02	Pull-Down	Don't-care	0			
		Debugging Log on U	OTXD During Booting			
Pin	Default	UOTXD Toggling	UOTXD Silent			
GPI015/MTD0	Pull-Up	1	0			
		Timing of	SDIO Slave			
Pin	Default	Falling—edge:Output	Falling—edge:Input Rising—edge:Output	Rising—edge:Input Falling—edge:Output	Rising—edge:Input Rising—edge:Output	
	Doll He	0	0	1	1	
GPI015/MTD0	Pull-Up					

Internal Bootstrapping Resistors

Pull-Up

MTDI/GPI012: GPI00:

