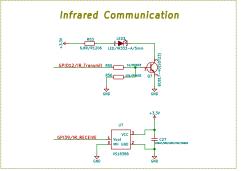
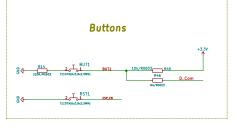


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	e Selectab	
Interface	Signal	Pin
EMAC	EMAC_MDC_out EMAC_MDLIn EMAC_MDO_out EMAC_CRS_out EMAC_COL_out	Any GPIO
I2C	IZCEXTO_SCL_in IZCEXTS_SDL_in IZCEXTS_SCL_in IZCEXTS_SCL_out IZCEXTS_SCL_out IZCEXTS_SCL_out IZCEXTS_SCL_out IZCEXTS_SCL_out	Any GPIO
General Purpose SPI	HSPIQ_In/_out HSPIQ_In/_out HSPICLK_In/_out HSPI_CSQ_In/_out HSPI_CSQ_out HSPI_CSQ_out YSPIQ_In/_out YSPIQ_In/_out YSPICLK_In/_out YSPIC_ESQ_in/_out YSPIC_ESQ_out YSPI_CSQ_out	Any GPIO

Mounting Holes		Holes	Internal Boo	otstra	pping Resistors	
		motes	MTDI/GPI012:		Pull-Down	
			GPIC	00:	Pull-Up	
GND ← → MH2 GND ← → MH3		GPI02:		Pull-Down		
			GPI04: MTD0/GPI015:		Pull-Down Pull-Up	
		P MH3				
		GPI05:		Pull-Up		
		Voltage of Interna				
Pile	Default	3.3V	1.87			
GPI012/MT0I	Pull-Down	0	1	D4-4		
		Beoting	Mode	ВО	otstrapping Pins	
Pto	Default	SPI Flash Boot	Download Boot	Settings		
GPI00	Pull-lin					

		Voltage of Intern	al L00(V00,50I0)					
Pio	Default	3.3V	1.89		Bootstrapping Pins			
GPI012/WTDI	Pull-Down	0	1					
		Beotin	g Mode					
#fo	Default	SPI Flash Boot	Download Boot	== Set	Settings			
GPI00	Pull-Up	1	0					
GPI02	Pull-Down	Don't-care						
		Debugging Log on I	JOTXD During Booting					
Pie	Default	USTXD Toggifing	UOTXD Silent					
GP1015/WT00	Pull-Up							
		Timing of	S010 Stave					
Pie	Default	Felling-edge:Output	Falling-edgesinput Rising-edgesOutput	Rising-edgesinput Falling-edge:Output	Rising-edgerinput Rising-edgerOutput			
GPI015/WT00	Pull-Up		0	1	1			
GPI05	Pull-Up							

