ESP32-C5-WROOM-1

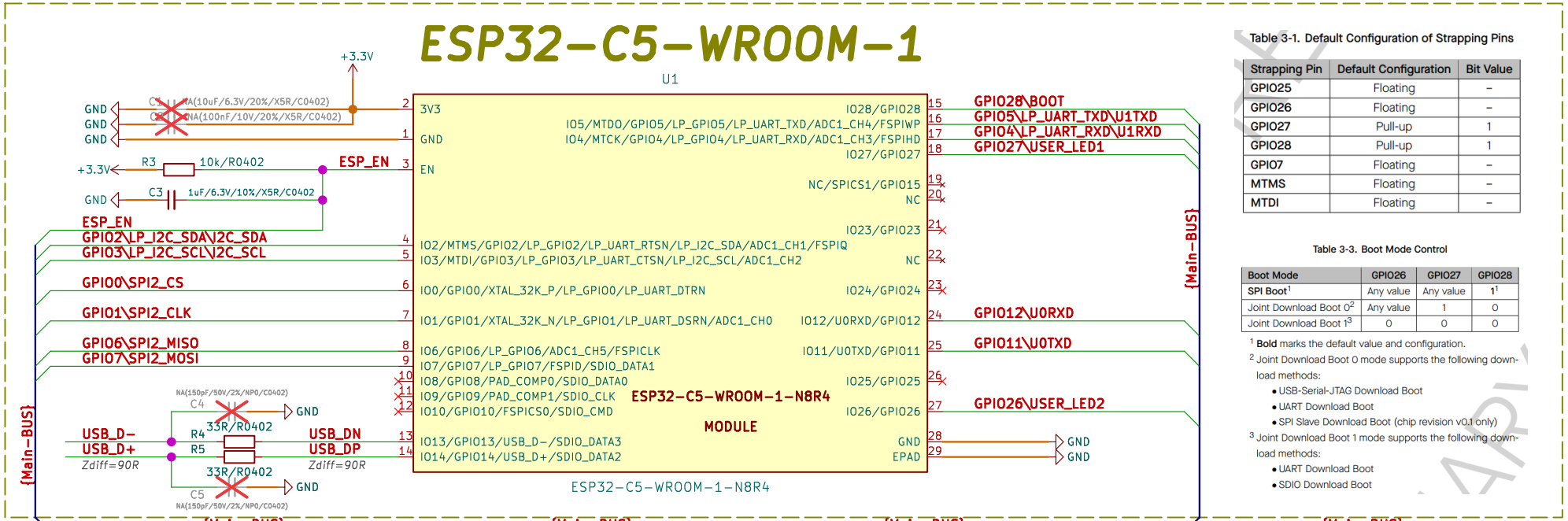


Table 3-1. Default Configuration of Strapping Pins

Strapping Pin	Default Configuration	Bit Value
GPIO25	Floating	–
GPIO26	Floating	–
GPIO27	Pull-up	1
GPIO28	Pull-up	1
GPIO7	Floating	–
MTMS	Floating	–
MTDI	Floating	–

Table 3-3. Boot Mode Control

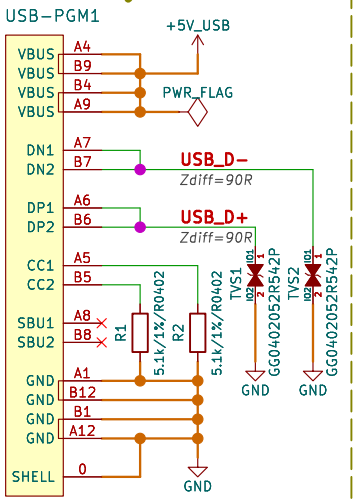
Boot Mode	GPIO26	GPIO27	GPIO28
SPI Boot[1]	Any value	Any value	1[1]
Joint Download Boot 0[2]	Any value	1	0
Joint Download Boot 1[3]	0	0	0

[1] **Bold** marks the default value and configuration.

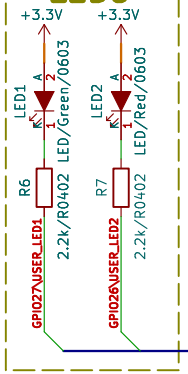
[2] Joint Download Boot 0 mode supports the following download methods:
- USB-Serial-JTAG Download Boot
- UART Download Boot
- SPI Slave Download Boot (chip revision v0.1 only)

[3] Joint Download Boot 1 mode supports the following download methods:
- UART Download Boot
- SDIO Download Boot

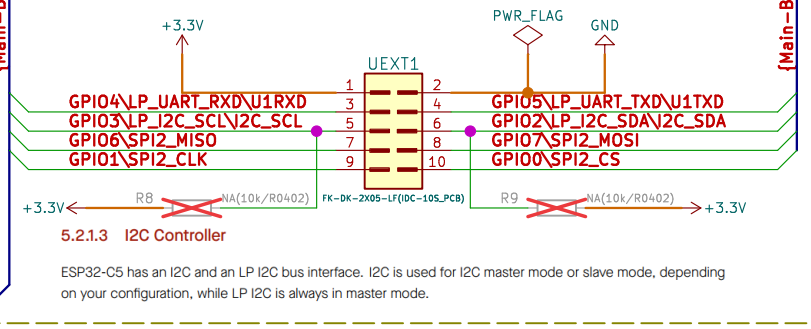
USB/JTAG



LEDs



UEXT

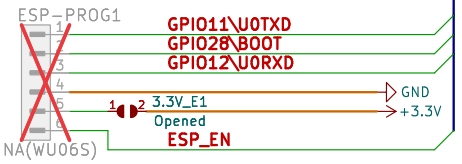


5.2.1.3 I2C Controller

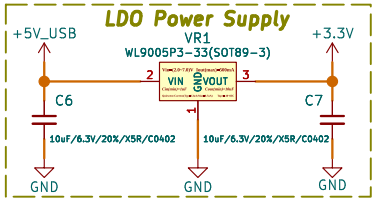
ESP32-C5 has an I2C and an LP I2C bus interface. I2C is used for I2C master mode or slave mode, depending on your configuration, while LP I2C is always in master mode.

Programming Connector. Via ESP-PROG

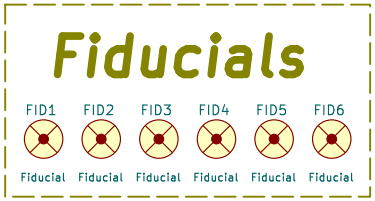
https://www.olimex.com/Products/IoT/Programmer/ESP-PROG/open-source-hardware



LDO Power Supply



Fiducials



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