

## Multi Channel Option!

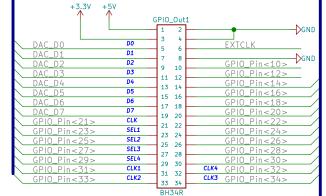
GPIO_Pin<31>	Soldered 2	1CLK_E1	
GPIO_Pin<33>	Opened <sub>2</sub>	1CLK_E2	l
GPIO_Pin<34>	Opened <sub>2</sub>	1CLK_E3	I
GPIO_Pin<32>	Opened <sub>2</sub>	1CLK_E4	DAC_CLK

FUNCTION/MODE	MODE 0	MODE 1
	MODE PIN CONNECTED TO DGND	MODE PIN CONNECTED TO DVDD
Input code format	Binary(default)	Twos complement

for a digital supply voltage DVDD of 3 V to 3.6 V. Maximum output update rate = 100MSPS when DVDD = 4.5 V to 5.5 V. Because of this, DVDD has been set to 5V and SN74ACT244DWR buffer is used for levels shifter

Note that the update rate is limited to 67 MSPS

## **Extension Out**



U2A

