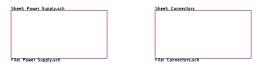


# LPDDR4 6GB

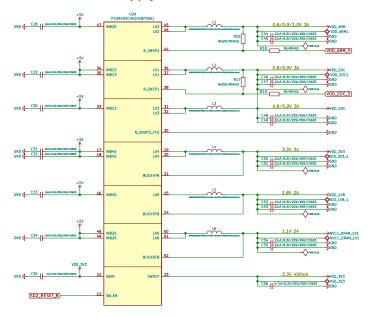
#### Note:

- 1. LPDDR4 ODT on i.MX 8M Plus is command—based, ODT\_CA\_A/B of LPDDR4 part should be connected directly to VDD2.
- 2. We have used a number of fully compatible, but different LPDDR4 memories due to supply unavailability. In such cases the memory part name in the schematic might remain outdated. It is recommended to always refer to the exact memory name printed on the component itself.



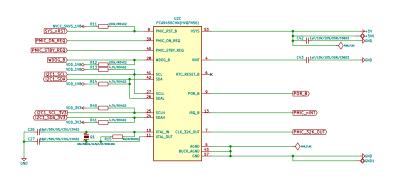


### Power Supply - PMIC

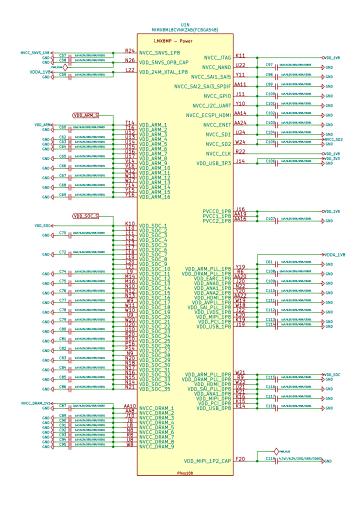


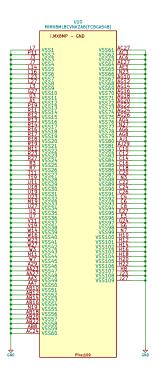


#### PCA9450 I2C Slave Address: 0x25



## Power Supply - MCU





# **EEPROM**12C Address: 0x50-0x57



