



# **IMX8MP-SOM-xGB-IND**

## **User Manual**

**Document revision 2.0** 

www.olimex.com

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#### **iMX8MP-SOM-4GB-IND** board features

<u>iMX8MP-SOM-4GB-IND</u> exposes all MIMX8ML8CVNKZAB GPIOs and features in a very compact format. It takes care for power supply and high speed memory signals complexities.

- MIMX8ML8CVNKZAB Quad core Cortex-A53 @1600Mhz + Arm Cortex-M7 @800 Mhz
- 4GB LPDDR4 RAM
- PMIC PCA9450
- 24 Mhz crystal
- 32.768 kHz crystal
- EEPROM memory (for Linux configuration or settings)
- Power LED
- User LED
- 5 x 40 pin + 1 x 20 pin total 220 pin connectors with 1.27 mm/0.05" step
- LVDS, 2 x MIPI CSI, MIPI DSI ribbon connectors on top
- Extended operating temperature from -20°C to +85°C
- Dimensions: (70 x 43)mm
- 4 holes for mounting
- Open source hardware design, all KiCAD design files available for download

Compatible with <u>iMX8MP-SOM-EVB-IND</u> – iMX8MP-SOM-4GB-IND can be placed on top of expansion board for easier R&D and prototyping.

## MIMX8ML8CVNKZAB chip features

The main chip has the following features:

- MIMX8ML8CVNKZAB Quad core Cortex-A53 @1600Mhz + Arm Cortex-M7 @800 Mhz
- 512KB Cache
- Vivante GC520L, Vivante GC7000UL
- HDMI HD1080p60 H.264, HD1080p60 H.265, VP8 video codec, VP9 video codec
- MIPI-DSI
- LVDS 4/8 lanes
- NPU 2.3 TOPS
- 2 x Gigabit Ethernet (1 x TSN)
- 2 x MIPI-CSI camera
- 1 x ISP camera
- 2 x USB3 with OTG
- 1 x PCIe 3.0
- 2 x CAN FD
- 3 x SPI
- 5 x I2C
- 3 x SDIO eMMC 5.1
- 4 x UART 5Mbit
- 4 x PWM
- Audio: ASRC, HiFi 4 DSP, SAI/I2S, eARC

## iMX8MP-SOM-EVB-IND board of peripherals features

<u>iMX8MP-SOM-EVB-IND</u> can be placed on top of expansion board for easier R&D and prototyping. It has the following features:

- iMX8MP-SOM matching connectors
- Two Gigabit Ethernet connectors
- Two CAN transceivers
- HDMI
- Power jack 5V
- UEXT connector
- Two GPIO connectors
- Two serial debug UARTs (for A53 and M7)
- Optional ARM JTAG connector
- Micro SD-card connector
- Flash module connector
- USB 3 hosts
- Headphone 3.5mm connector
- Microphone 3.5 mm connector
- · Reset button
- PWR button
- Boot slide switch
- Industrial grade temeprature range: (-45+85)C
- Dimensions: (155 x 102)mm

#### Order codes for iMX8MP-SOM and accessories

<u>iMX8MP-SOM-4GB-IND</u> Main module with iMX8MPLUS, 4GB LPDDR4, EEPROM, PMIC

<u>iMX8MP-SOM-EVB</u> Evaluation board with peripherals that can be used as a reference

design, compatible with iMX8MP-SOM-4GB-IND

MICRO-SD-16GB-CLASS10 Blank 16GB microSD card

<u>SY1005E</u> Power adapter 5V 2A

<u>USB-SERIAL-F</u> Serial debug cable for console log

<u>CABLE-HDMI-50CM</u> HDMI cable

BATTERY-LIPO1400mAh Li-Po battery for standalone operation

<u>LCD/LCD-OLinuXino-5CTS</u> 5 inch LCD 800x480 pixels with capacities touch panel

<u>LCD-OLinuXino-7CTS</u> 7 inch LCD 1024x600 pixels with capacitive touch panel

<u>LCD-OLinuXino-10CTS</u> 10 inch LCD 1024x600 pixels with capacitive touch panel

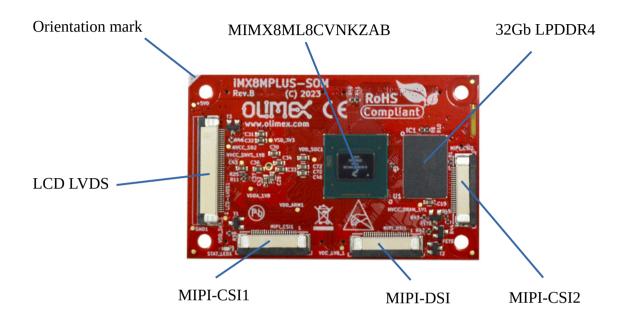
<u>UEXT modules</u> Expansion modules temperature, humidity, pressure, magnetic field,

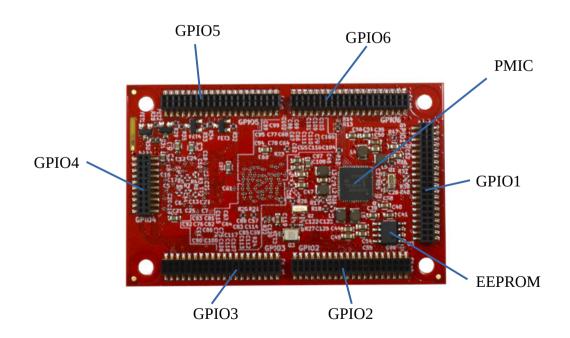
light sensors. Modules with LCDs, LED matrix, Relays, Bluetooth,

Zigbee, WiFi, GSM, GPS, RFID, RTC, EKG, sensors and etc.

## **HARDWARE**

## iMX8MP-SOM layout





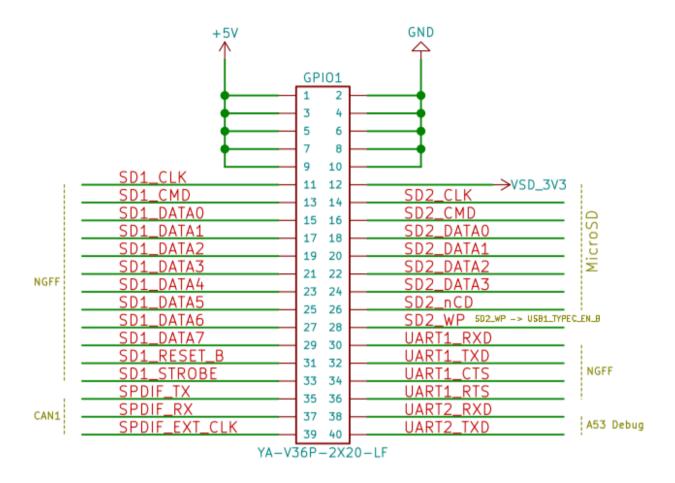
## **iMX8MP-SOM** schematics

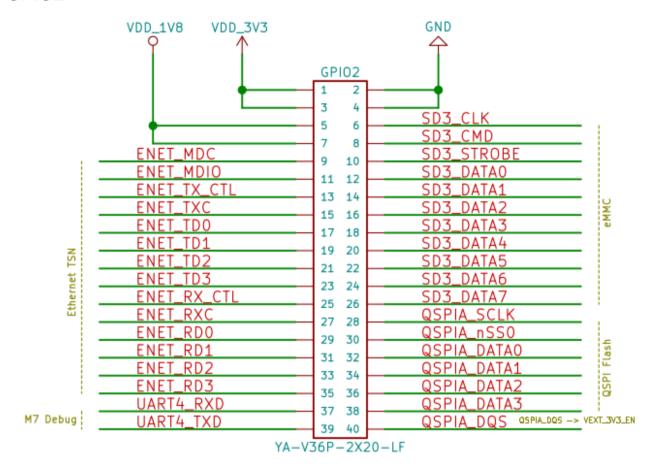
iMX8MP-SOM-4GB-IND latest schematic is at our GitHub.

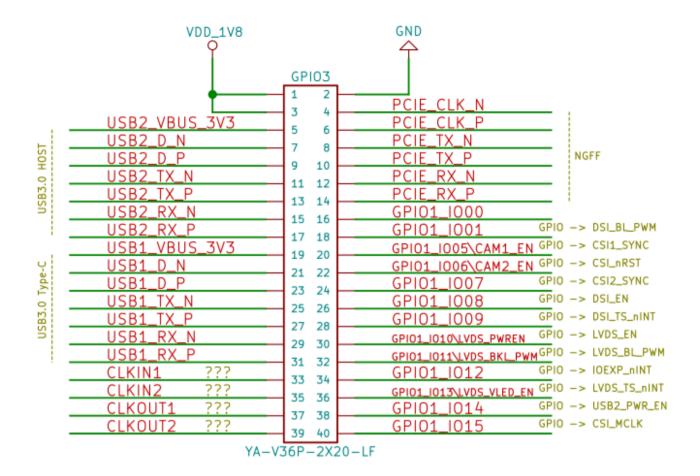
Hardware sources and more can also be found here:

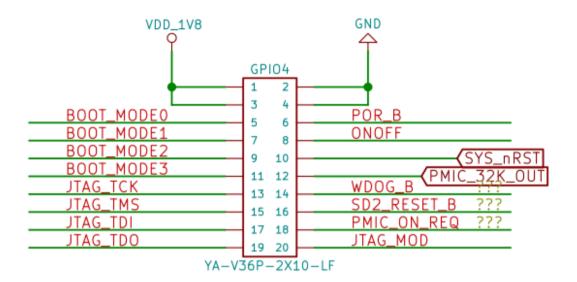
https://github.com/OLIMEX/iMX8MP-SOM/tree/main/HARDWARE

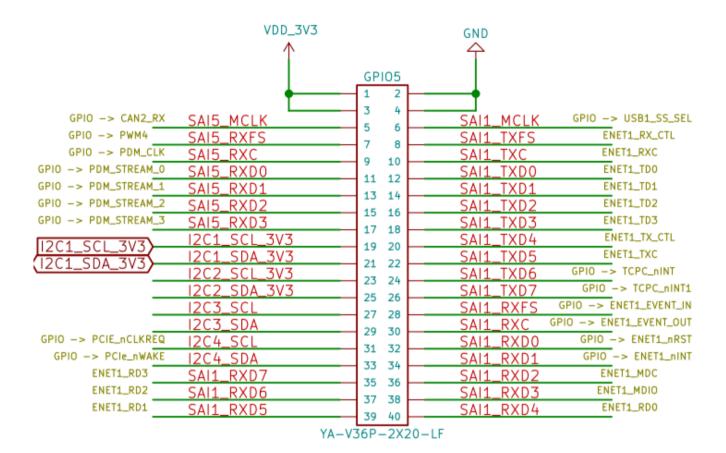
#### **iMX8MP-SOM** connectors

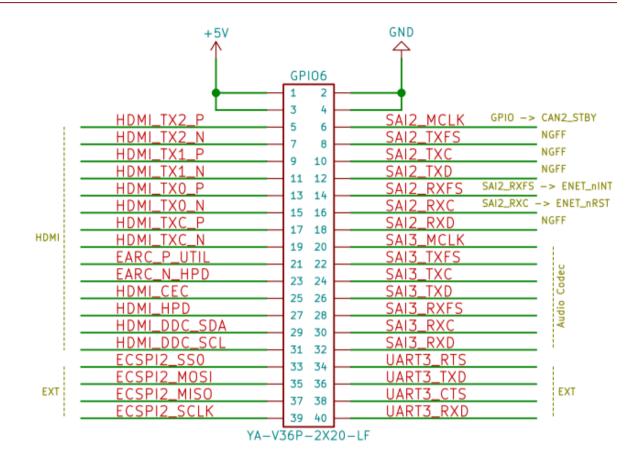




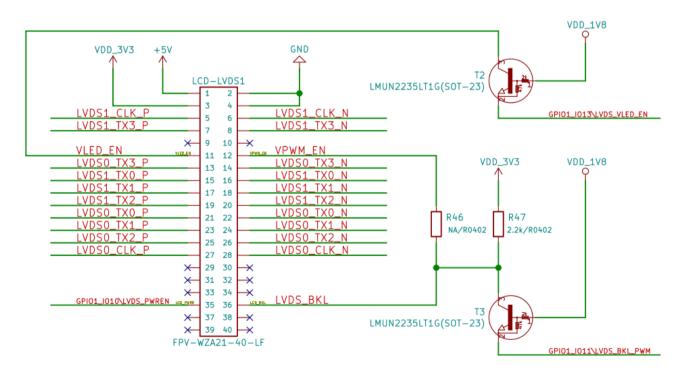




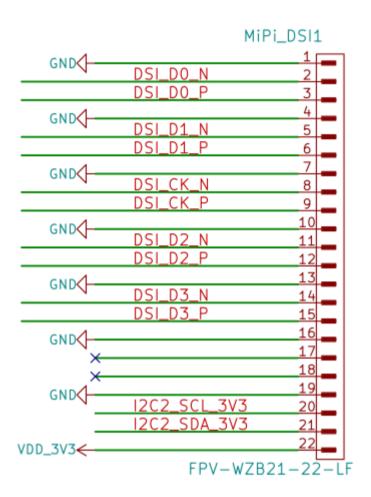




#### **LCD**

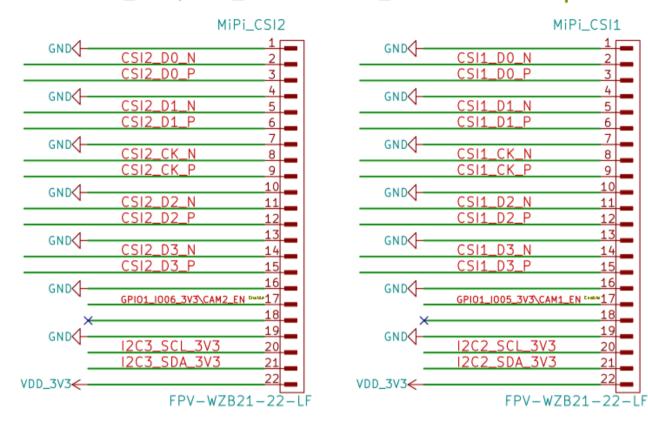


#### **MIPI-DSI**



#### **MIPI-CSI**

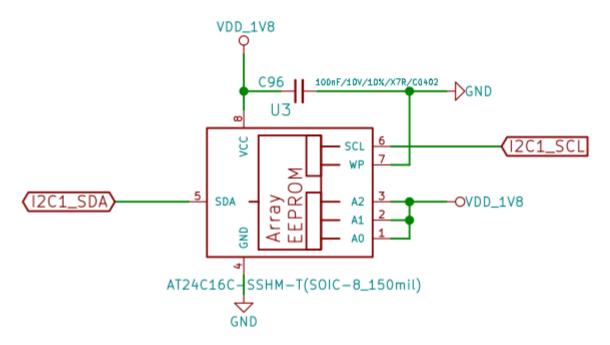
### Note: MiPi\_CSI1, MiPi\_CSI2 and MiPi\_DSI1 are RPi compatible!



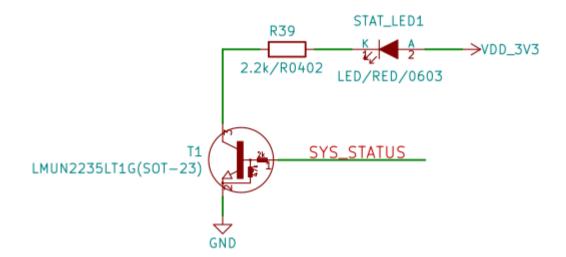
#### **EEPROM**

# **EEPROM**

12C Address: 0x50-0x57



## **User LED**



## **SOFTWARE**

Olimex provides buildroot for the board that can be found here:

https://images.olimex.com/release/imx8mp/

## **Document Revision History**

Revision 2.0 December 2024

- Improved formatting
- Fixed wrong info about Linux available

Revision 1.0 May 2024

- Initial document