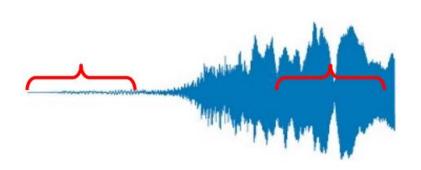
Convolutional Neural Networks

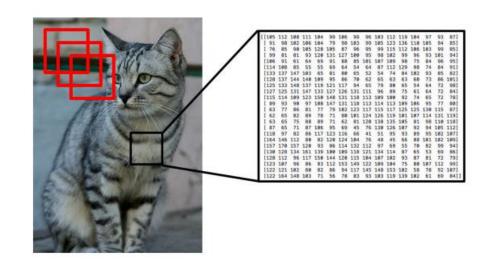
Jisang Han

onground@korea.ac.kr KUGODS

Department of Computer Science and Engineering, Korea University



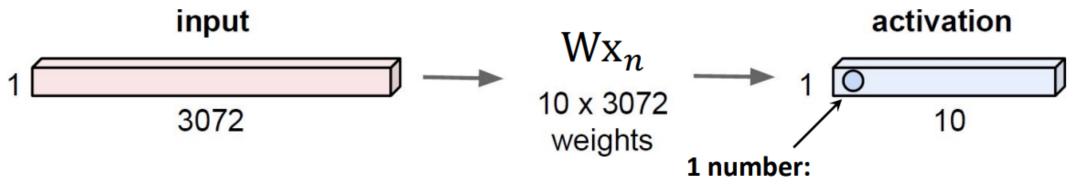




- Local processing of data
- Parameter Sharing
- Sparsity of Connections



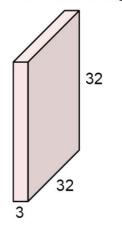
• The Perceptron: $32\times32\times3$ image \rightarrow stretch to 3072×1



the result of taking a dot product between a row of W and the input (a 3072-dimensional dot product)

- Stretching 2D image into 1D vector is not suitable in considering a spatial context of 2D image.
- Too many network parameters are required even for an image of moderate size.

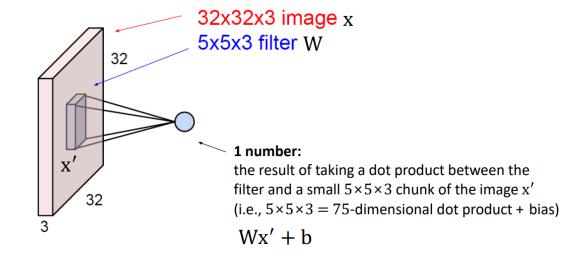
32x32x3 image

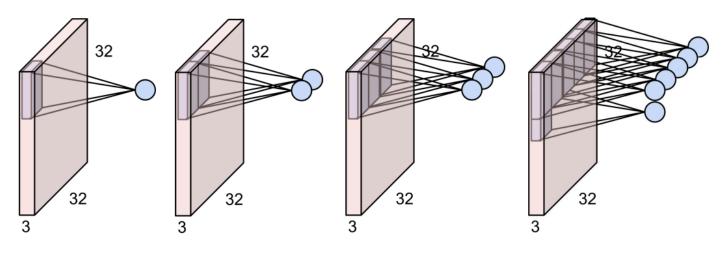


5x5x3 filter



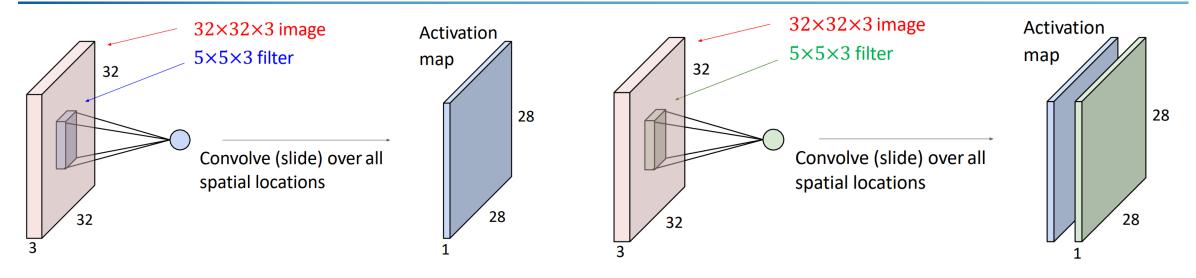
Convolve the filter with the image, i.e., "slide over the image spatially, computing dot products"



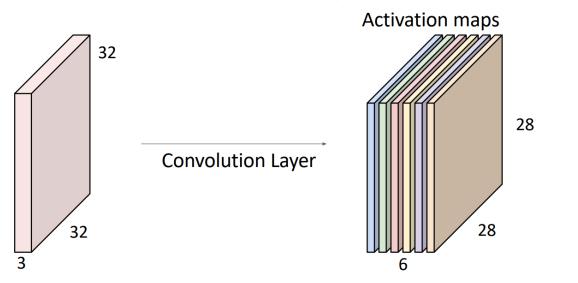


Convolve (slide) over all spatial locations



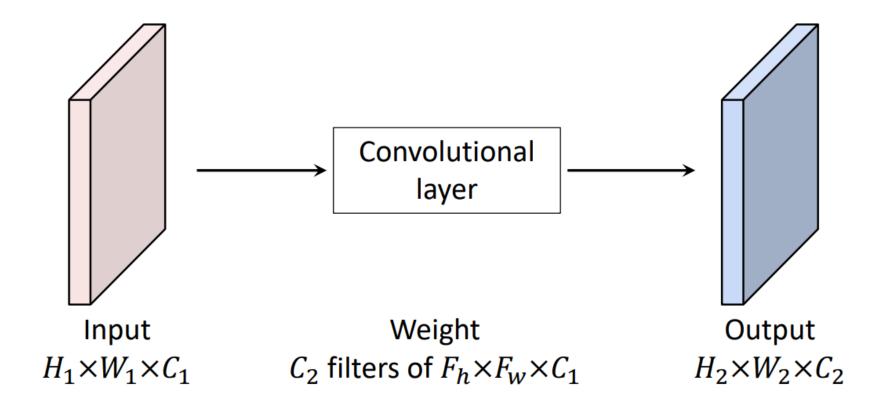


For example, if we had 6.5×5 filters, we'll get 6 separate activation maps.





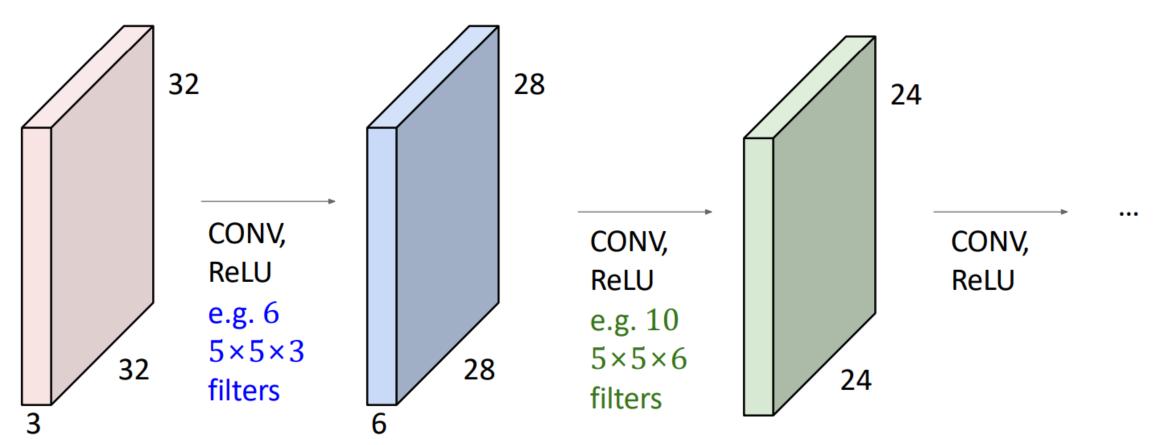
The number of parameters in convolutional layer



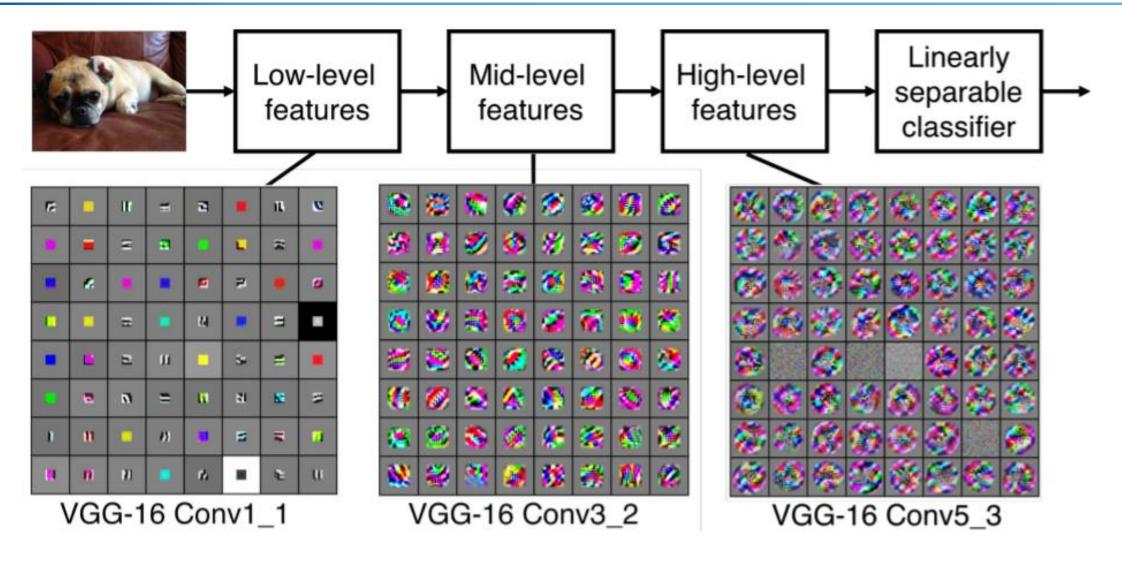
 \rightarrow The number of weights: $C_2 \times (C_1 \times F_h \times F_w)$, The number of bias: C_2



 ConvNet is a sequence of convolution layers, interspersed with nonlinear activation functions (e.g., ReLU)





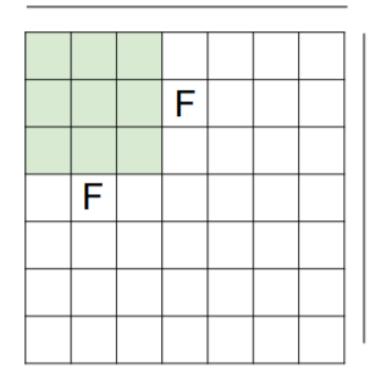




Lecture Note from CS231n

Activation Map





Ν

Output size:

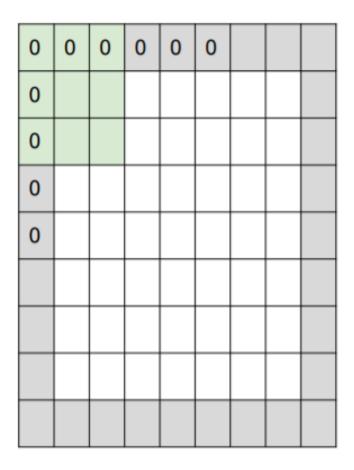
e.g.
$$N = 7$$
, $F = 3$:

stride
$$1 \Rightarrow (7 - 3)/1 + 1 = 5$$

stride
$$2 \Rightarrow (7 - 3)/2 + 1 = 3$$

stride
$$3 \Rightarrow (7 - 3)/3 + 1 = 2.33 : \$$

Activation Map



e.g. input 7x7

3x3 filter, applied with stride 1

pad with 1 pixel border => what is the output?

7x7 output!

in general, common to see CONV layers with stride 1, filters of size FxF, and zero-padding with (F-1)/2. (will preserve size spatially)

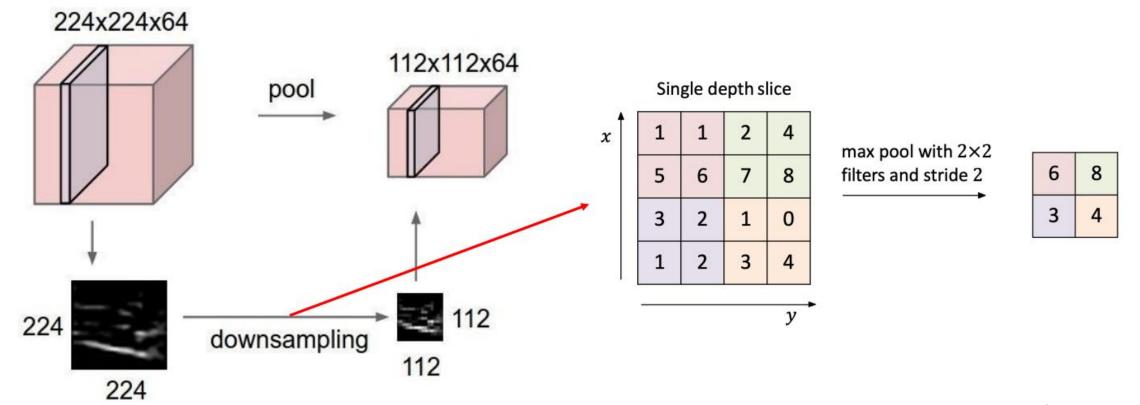
```
e.g. F = 3 => zero pad with 1
F = 5 => zero pad with 2
F = 7 => zero pad with 3
```



Lecture Note from CS231n

Pooling Layer

- Max Pooling / Average Pooling
- Makes the representations smaller and more manageable.





Lecture Note from CS231n

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Conv Layer

https://pytorch.org/docs/stable/generated/torch.nn.Conv2d.html

CONV2D

CLASS torch.nn.Conv2d(in_channels, out_channels, kernel_size, stride=1, padding=0, dilation=1, groups=1, bias=True, padding_mode='zeros', device=None, dtype=None) [SOURCE]

```
nn.Conv2d(
   in_channels=1,
   out_channels=64,
   kernel_size=3,
   stride=1,
   padding=1
)
```

```
self.conv1 = nn.Conv2d(1, 64, 3, 1, 1)
```

MaxPooling

https://pytorch.org/docs/stable/generated/torch.nn.MaxPool2d.html

MAXPOOL2D

```
CLASS torch.nn.MaxPool2d(kernel_size, stride=None, padding=0, dilation=1, return_indices=False, ceil_mode=False) [SOURCE]
```

```
nn.MaxPool2d(
    kernel_size=2,
    stride=2
)
```

```
self.maxpool1 = nn.MaxPool2d(2, 2)
```

ConvNet

```
self.convlayers = nn.Sequential(
            nn.Conv2d(1, 64, 3, 1, 1),
            nn.ReLU(),
            nn.MaxPool2d(2, 2),
            nn.Conv2d(64, 128, 3, 1, 1),
            nn.ReLU(),
            nn.MaxPool2d(2, 2),
            nn.Conv2d(128, 256, 3, 1, 1),
            nn.ReLU(),
            nn.MaxPool2d(7, 1)
```

 \rightarrow Input: 1 x 28 x 28

→ Conv: 64 x 28 x 28

 \rightarrow MaxPool : 64 x 14 x 14

 \rightarrow Conv: 128 x 14 x 14

 \rightarrow MaxPool: 128 x 7 x 7

 \rightarrow Conv : 256 x 7 x 7

 \rightarrow MaxPool : 256 x 1 x 1

 \rightarrow Output: 256 x 1 x 1

Classifier

 \rightarrow Input: 256 x 1 x 1

 \rightarrow Flatten : 256

 \rightarrow Linear : 128

 \rightarrow Linear : 10

Naïve ConvNet

```
• • •
class Naive_ConvNet(nn.Module):
    def __init__(self):
        super().__init__()
        self.convlayers = nn.Sequential(
            nn.Conv2d(1, 64, 3, 1, 1),
            nn.ReLU(),
            nn.MaxPool2d(2, 2),
            nn.Conv2d(64, 128, 3, 1, 1),
            nn.ReLU(),
            nn.MaxPool2d(2, 2),
            nn.Conv2d(128, 256, 3, 1, 1),
            nn.ReLU(),
            nn.MaxPool2d(7, 1)
        self.classifier = nn.Sequential(
            nn.Flatten(),
            nn.Linear(256, 128),
            nn.ReLU(),
            nn.Linear(128, 10)
    def forward(self, x):
       x = self.convlayers(x)
       x = self.classifier(x)
        return x
```



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Naïve ConvNet

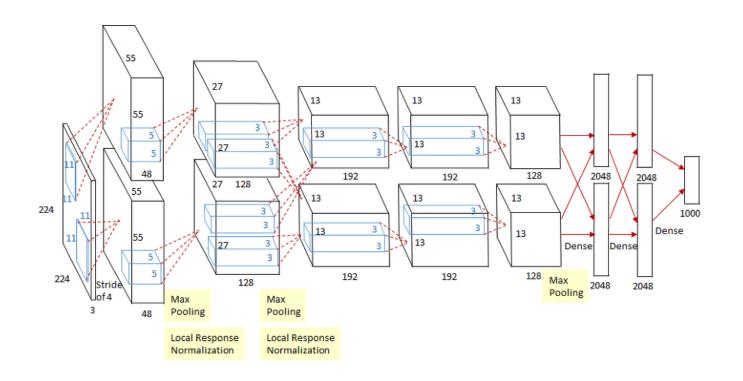
```
• • •
class Naive_ConvNet(nn.Module):
    def __init__(self):
        super().__init__()
        self.convlayers = nn.Sequential(
            nn.Conv2d(1, 64, 3, 1, 1),
            nn.ReLU(),
            nn.MaxPool2d(2, 2),
            nn.Conv2d(64, 128, 3, 1, 1),
            nn.ReLU(),
            nn.MaxPool2d(2, 2),
            nn.Conv2d(128, 256, 3, 1, 1),
            nn.ReLU(),
            nn.MaxPool2d(7, 1)
        self.classifier = nn.Sequential(
            nn.Flatten(),
            nn.Linear(256, 128),
            nn.ReLU(),
            nn.Linear(128, 10)
    def forward(self, x):
       x = self.convlayers(x)
       x = self.classifier(x)
        return x
```

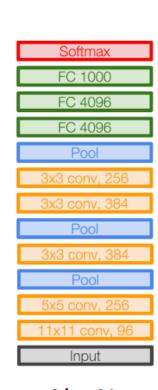


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Assignment

Implementation of AlexNet and VGG16





FC 1000 FC 4096 FC 4096 Pool Pool Pool Pool Pool Input

Softmax

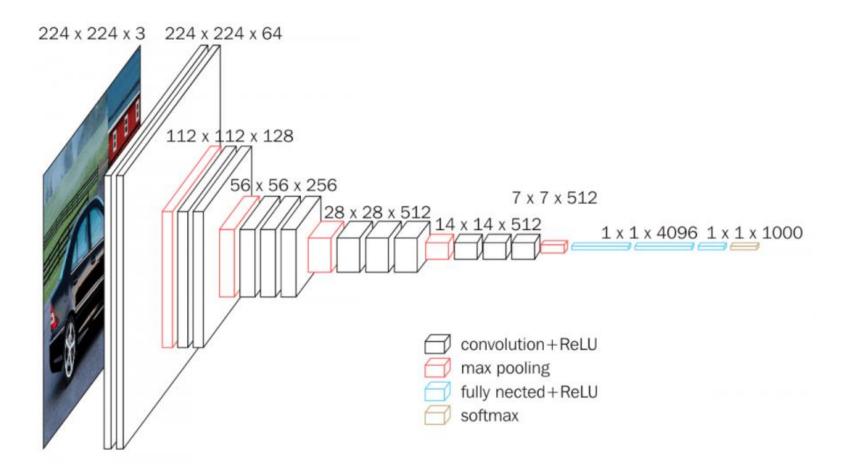
AlexNet

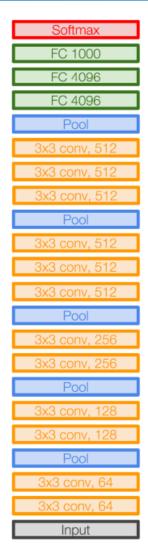
VGG16

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Assignment

Implementation of AlexNet and VGG16





VGG16

Assignment

- Implementation of AlexNet and VGG16
- You should implement at least one architecture
- Due Date 11/13

 Lectures, Exercises, and Assignments will be uploaded in Github (<u>https://github.com/ONground-Korea/KUGODS-2022-GDSC-Deeplearning-Session</u>)



Thank you! Q&A

