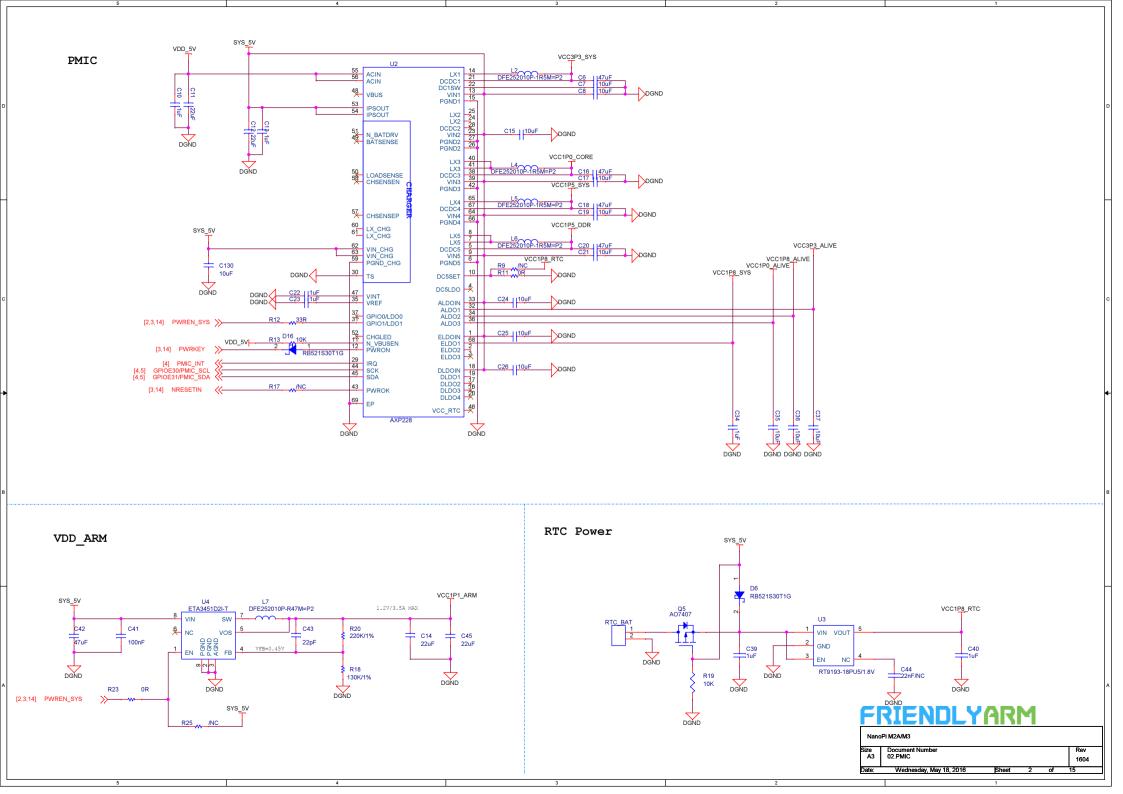
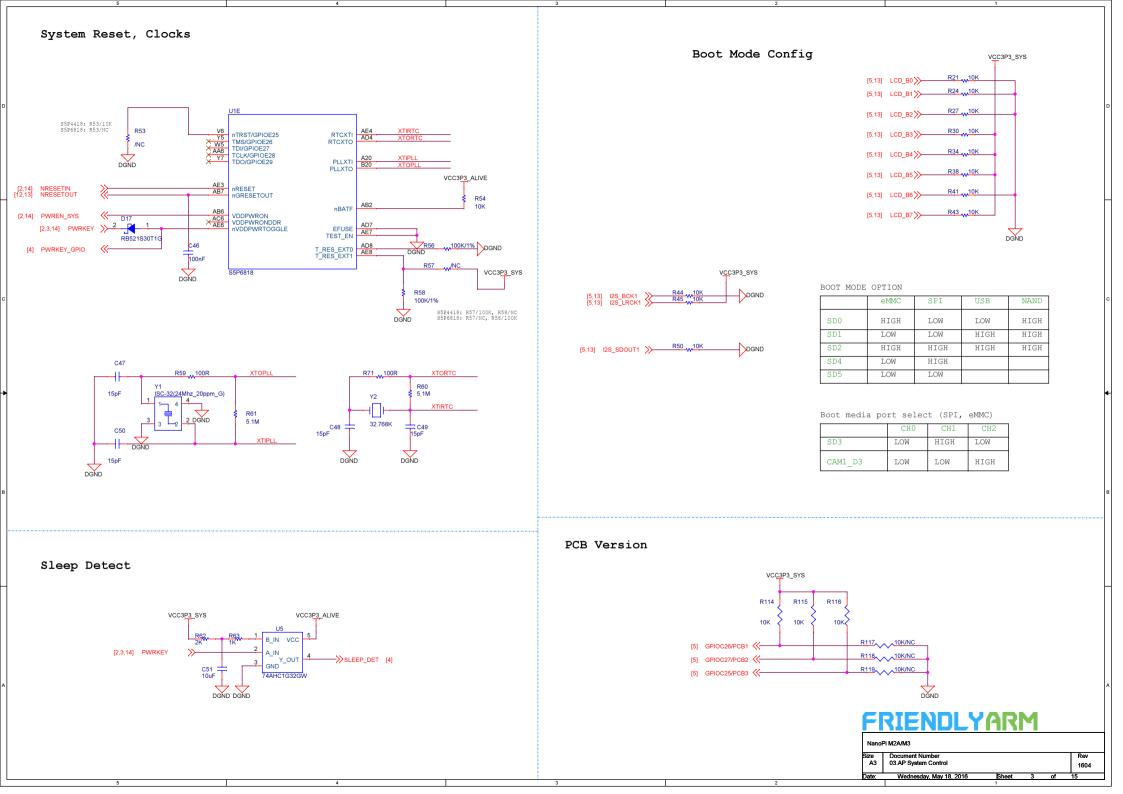
# NanoPi M2A/M3

Revision

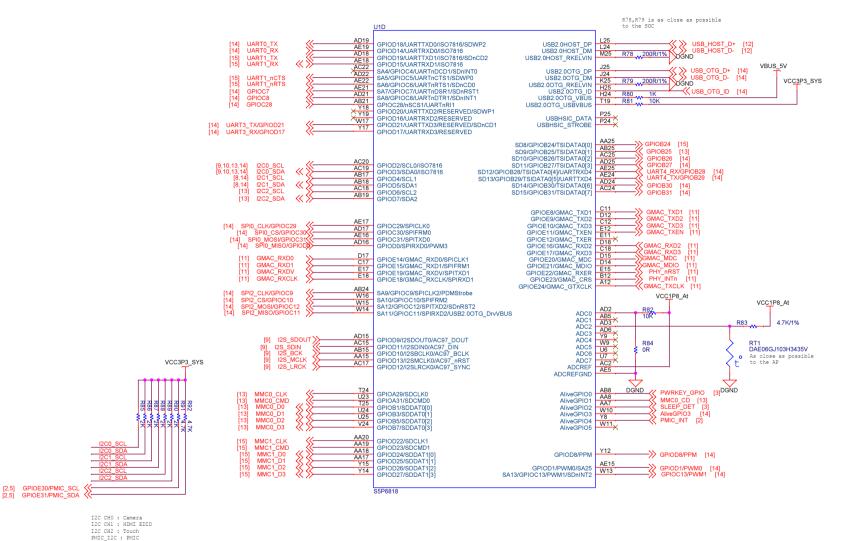
1604 , First Release





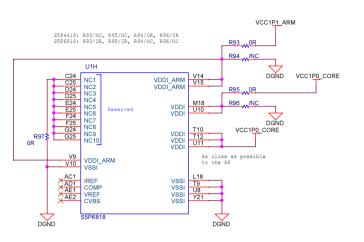


# AP Peripherals



### AP VIP&Display U1C LCD\_B0 LCD\_B1 LCD\_B2 LCD\_B3 LCD\_B4 LCD\_B5 LCD\_B6 LCD\_B7 LCD\_G0 [3 13] I2S BCK1 GPIOA30/VID1[0]/SDEX0/I2SBCLK1 GPIOA1/DISD0 GPIOA30/VID1[0]/SDEX0/12SBCLK1 GPIOB0/VID1[1]/SDEX1/12SLRCK1 GPIOB2/VID1[2]/SDEX2/12SBCLK2 GPIOB4/VID1[3]/SDEX3/12SLRCK2 [3,13] I2S LRCK1 GPIOA2/DISD1 DGND GPIOA3/DISD2 GPIOA4/DISD3 [3,13] I2S\_SDOUT1 [15] GPIOB8 GPIOB6/VID1[4]/SDEX4/I2SDOUT1 GPIOA5/DISD4 GPIOB8/VID1[5]/SDEX5/I2SDOUT2 GPIOB9/VID1[6]/SDEX6/I2SDIN1 GPIOA6/DISD5 [13] I2S\_SDIN1 VCC3P3\_SYS GPIOA7/DISD6 GPIOB10/VID1[7]/SDEX7/I2SDIN2 GPIOA28/VICLK1/I2SMCLK2/I2SMCLK1 GPIOA8/DISD7 [13] I2S\_MCLK1 GPIOA9/DISD8 GPIOE13/GMAC\_COL/VIHSYNC1 GPIOE7/GMAC\_TXD0/VIVSYNC1 GPIOA10/DISD9 [11] GMAC TXD0 >> GPIOA11/DISD10 LCD\_G2 LCD\_G3 LCD\_G4 LCD\_G5 LCD\_G6 LCD\_G7 LCD\_R0 LCD\_R1 LCD\_R2 LCD\_R3 LCD\_R4 GPIOA12/DISD11 GPIOA13/DISD12 DIFY -LSQ-160330 GPIOA14/DISD13 GPIOA15/DISD13 L23 M21 GPIOA16/DISD15 GPIOA17/DISD16 GPIOA18/DISD17 AE12 SA23/GPIOC23/SDDAT2[3]/VID2[0] AD12 LATADDR/GPIOC24/SPDIFRX/VID2[7] SA14/GPIOC14/PWM2/VICLK2 SA23/GPIOC23/SDDAT2131/VID216 GPIOA19/DISD18 GPIOA20/DISD19 [14] GPIOC14/PWM2 AD14 AD14 SA14/GPIOC14/PWM2/VICLK2 SA15/GPIOC15/TSICLK0/VIHSYNC2 SA16/GPIOC16/TSISYNC0/VIVSYNC2 GPIOA21/DISD20 R21 GPIOA22/DISD21 GPIOC16 GPIOA23/DISD22 GPIOA23/DISD22 GPIOA0/DISCLK J21 J20 H22 GPIOA25/DISVSYNC LCD\_VSYNC LCD\_HSYNC GPIOD28/VID0[0]/TSIDATA1[0]/SA24 GPIOD29/VID0[1]/TSIDATA1[1] GPIOD30/VID0[2]/TSIDATA1[2] GPIOA26/DISHSYNC GPIOA27/DISDE LCD\_DE [13] AD9 AC11 CAM0\_D2 CAM0\_D3 GPIOD31/VID0[3]/TSIDATA1[3 CAM0\_D4 CAM0\_D5 GPIOE0/VID0[4]/TSIDATA1[4] GPIOE1/VID0j5j/TSIDATA1j5 13] CAM0\_D5 13] CAM0\_D6 13] CAM0\_D7 CAM0\_PCLK CAM0\_HYNC AD11 | GPIOE2/VID0[6]/TSIDATA1[0] | AE11 | GPIOE3/VID0[7]/TSIDATA1[7] | AA11 | GPIOE3/VID0[7]/TSIDATA1[7] | AD11 | GPIOE5/VIHSYNC0/TSISYNC1 | GPIOE6/VIVSYNC0/TSIDP1 GPIOE2/VID0[6]/TSIDATA1[6 LVDS\_Y0P [13] LVDS\_Y0M [13] LVDS\_TP0 --> LVDS\_Y0P [13] --> LVDS\_Y0M [13] --> LVDS\_Y1P [13] --> LVDS\_Y1M [13] --> LVDS\_Y2P [13] --> LVDS\_Y2M [13] LVDS\_TN0 B14 LVDS\_TP1 A14 LVDS\_TN1 LVDS\_TP2 LVDS\_TN2 LVDS\_TN2 LVDS\_TP3 LVDS\_TN3 B18 ->> LVDS\_Y3P [13] ->> LVDS\_Y3M [13] LVDS\_IN3 LVDS\_TP4 LVDS\_TP4 LVDS\_TPCLK LVDS\_TPCLK LVDS\_TPCLK LVDS\_PQUIT LVDS\_CLKP [13] LVDS\_CLKM [13] LVDS ROUT 4.3K/1% R100 is as close as possible DGND B24 A24 HDMI\_TXOP [8] HDMI\_TXON [8] HDMI\_TX1P [8] HDMI\_TX1N [8] HDMI\_TX2P [8] HDMI\_TX2P [8] HDMI\_TXCN [8] HDMI\_TXCN [8] HDMI TXP0 HDMI\_TXN0 HDMI\_TXP1 HDMI TXN1 HDMI\_TXP2 HDMI\_TXN2 HDMI\_TXPCLK HDMI\_TXNCLK HDMI\_TXCN HDMI\_CEC HDMI\_CEC [8] SA3/GPIOC3/HDMI\_CEC/SDnRST0 HDMI\_HOT5V B21 HDMI REXT R104 is as close as possible S5D6818 P104 4.7K/1% C167 is as close as possible to the AP MODIFY -LSQ-160330 U1B AD20 AB20 AE20 AB23 Y23 W23 VCC3P3 SYS SD0/GPIOB13 nSCS0 → GPIOE31/PMIC\_SDA [2,4] → GPIOE30/PMIC\_SCL [2,4] → GPIOC26/PCB1 [3] → GPIOC27/PCB2 [3] DGND < SD1/GPIOB15 nSWE/GPIOE31 nSOE/GPIOE30 RDnWR/GPIOC26/PDMDATA0 SD2/GPIOB17 SD3/GPIOR10 V22 W21 AA24 DGND 🔨 SD4/GPIOB20 nSDQM/GPIOC27/PDMDATA1 SPIOC25/PCB3 SD5/GPIOR21 nSWAIT/GPIOC25/SPDIFTX SD6/GPIOB22 SD7/GPIOB23 Y25 Y24 W24 nNCS0 nNCS1 -AD23 CLEO/CLE1/GPIOB12 NFWE0/nNFWE1/GPIOB18 nNFOE0/nNFOE1/GPIOB18 RBB0/RnB1/GPIOB14 A23 W24^ V25 V25 V25 V25 V25 V27 A23 A23 SA0/GPIOC0/TSIERR0 →>> LED1 [14] AE23 AA22 SA1/GPIOC1/TSIERR1 SA2/GPIOC2 S5P6818

## Reserved Function

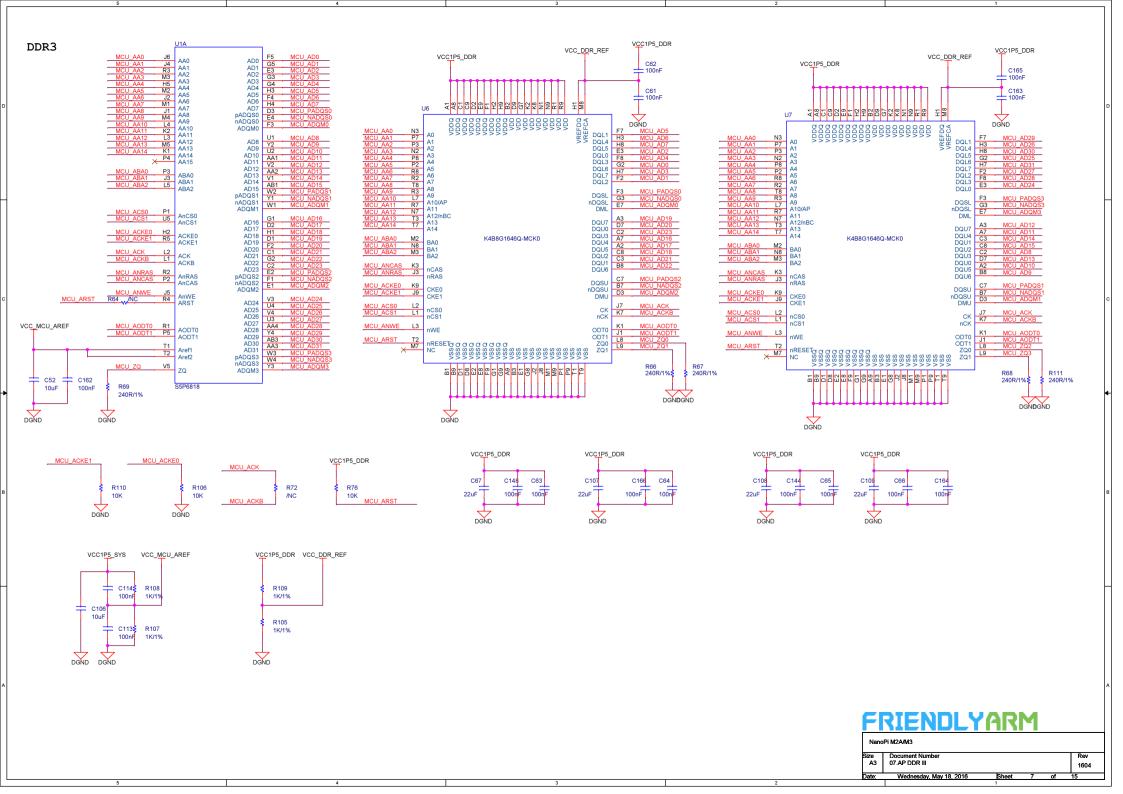




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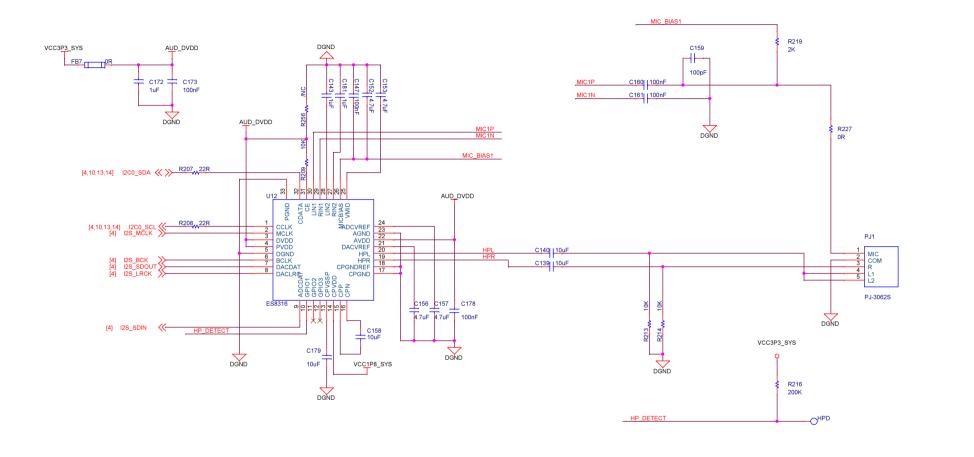
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Wednesday, May 18, 2016



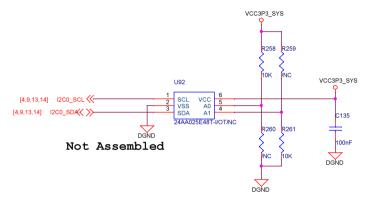
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Audio



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# EEPROM with MAC Address



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