NanoPi M2A/M3

Revision

1604 , First Release

FRIENDLYARM

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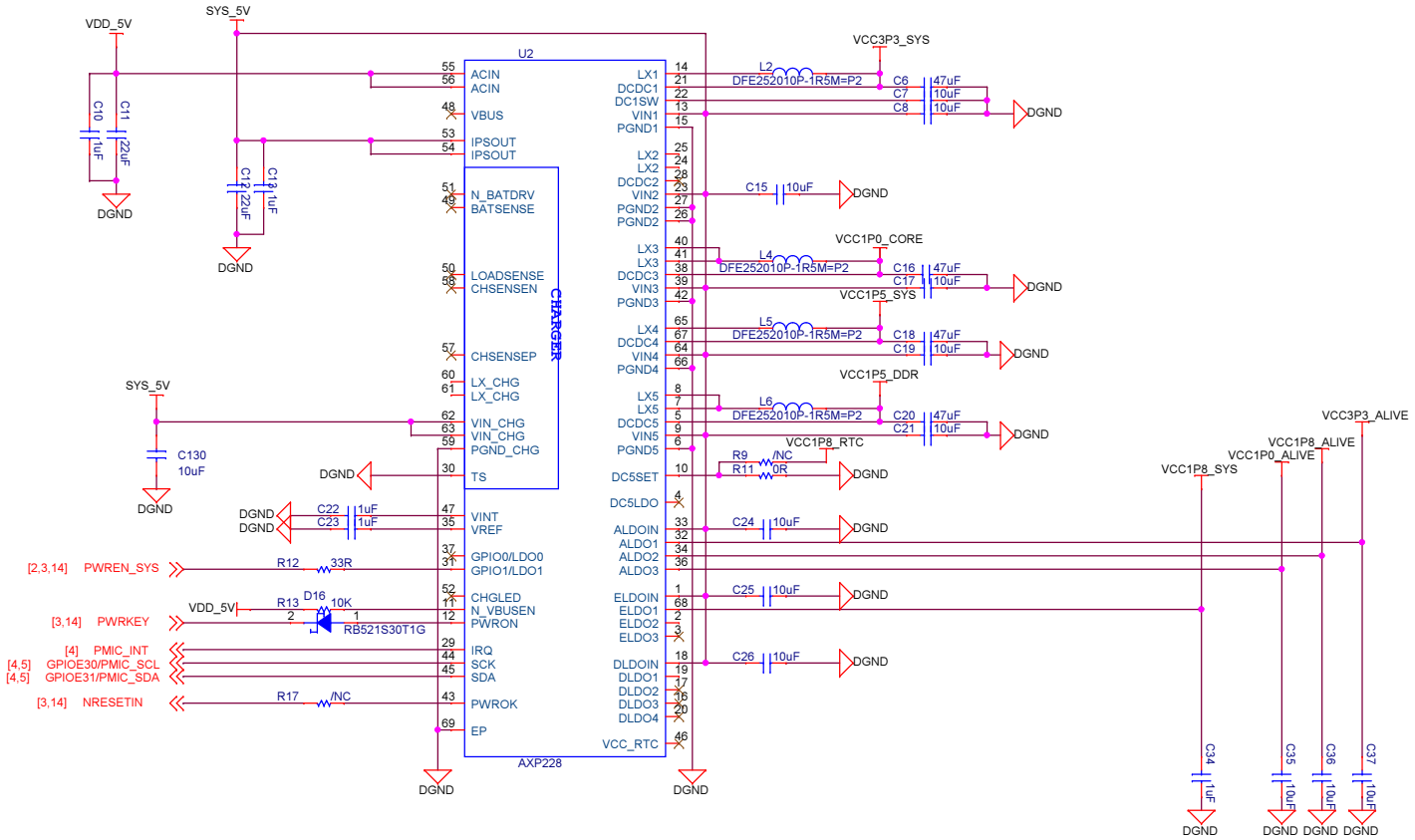
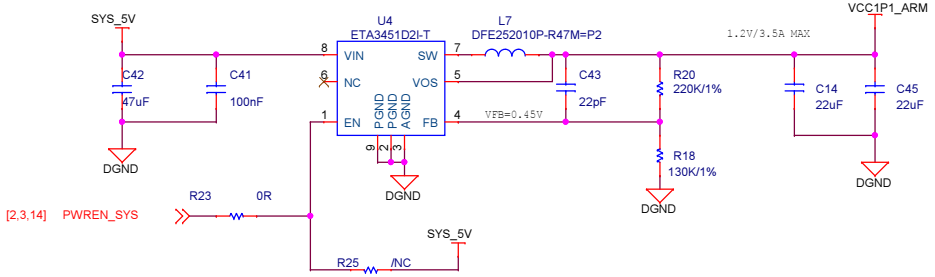
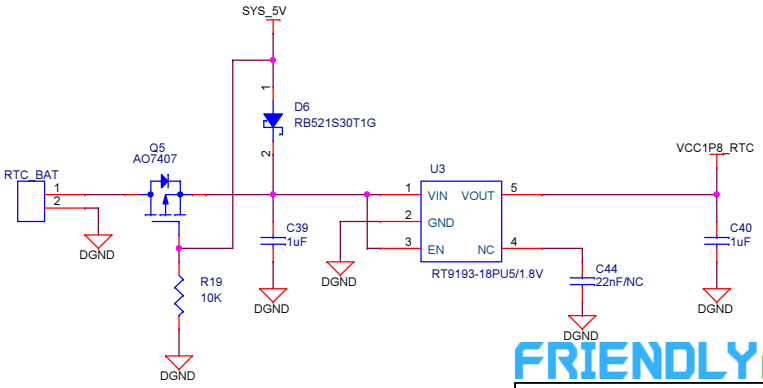
PMIC



VDD_ARM



RTC Power



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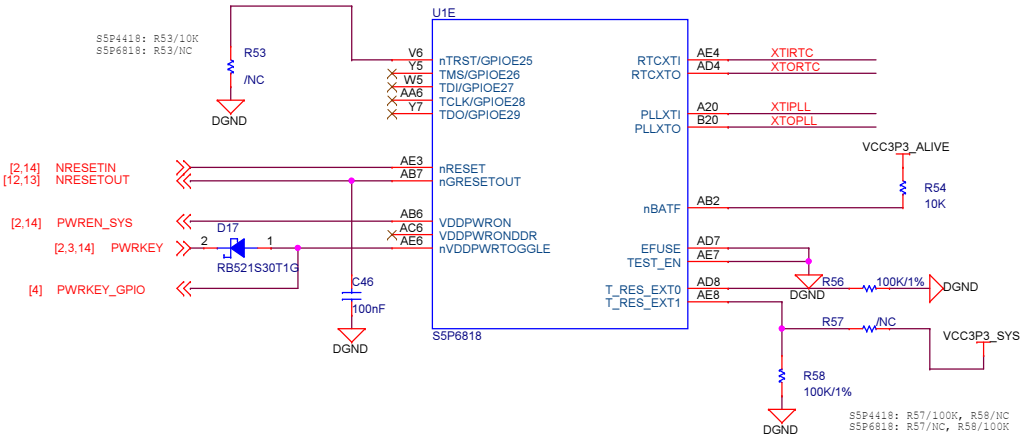
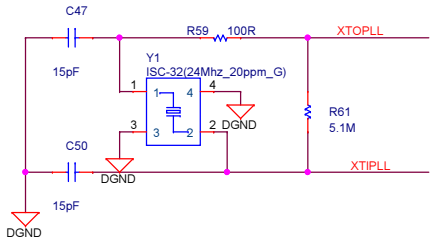
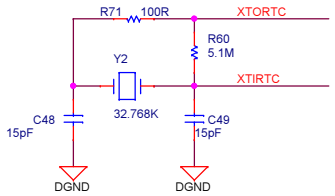
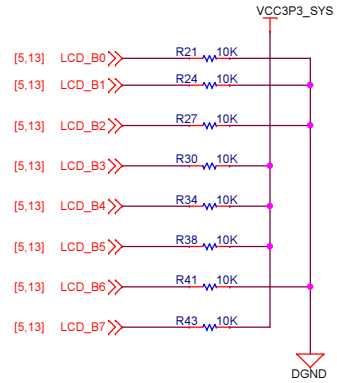
System Reset, Clocks

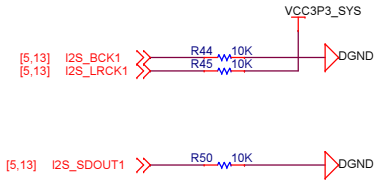






Boot Mode Config





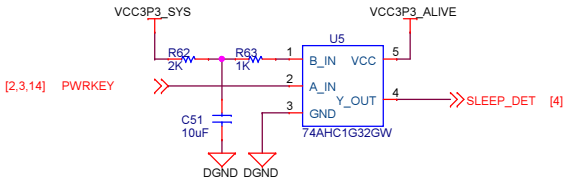
BOOT MODE OPTION

	eMMC	SPI	USB	NAND
SD0	HIGH	LOW	LOW	HIGH
SD1	LOW	LOW	HIGH	HIGH
SD2	HIGH	HIGH	HIGH	HIGH
SD4	LOW	HIGH		
SD5	LOW	LOW		

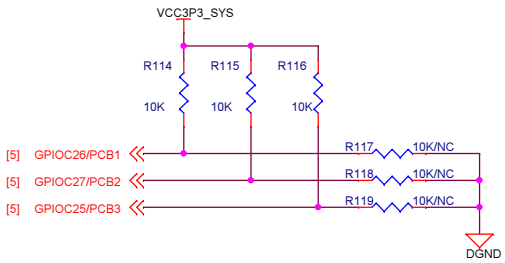
Boot media port select (SPI, eMMC)

	CH0	CH1	CH2
SD3	LOW	HIGH	LOW
CAM1_D3	LOW	LOW	HIGH

Sleep Detect



PCB Version



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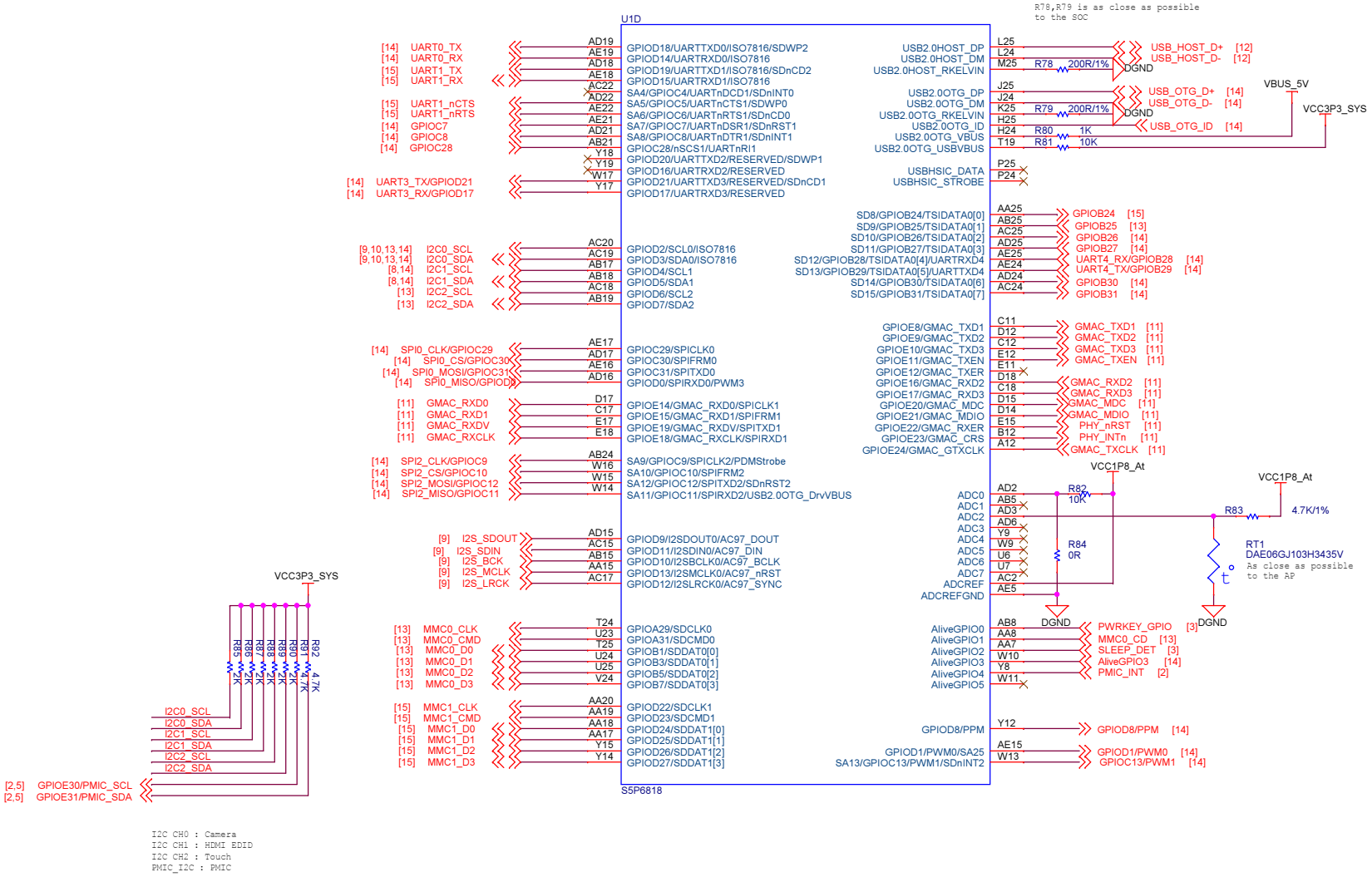
AP Peripherals



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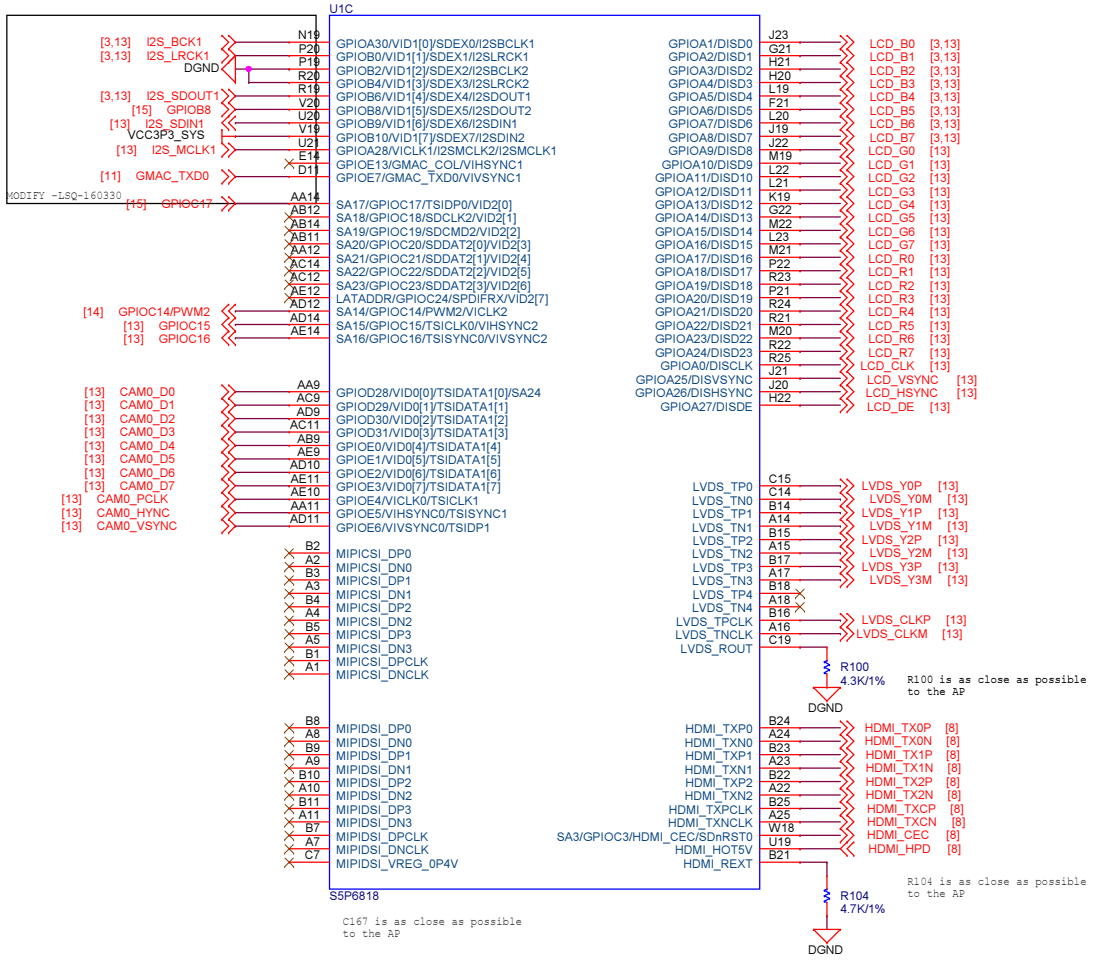
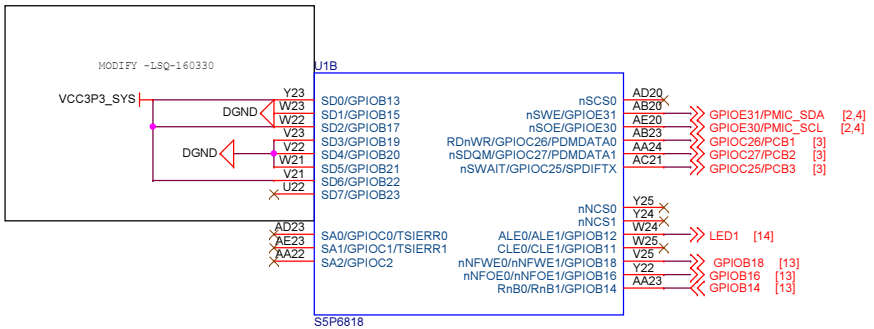
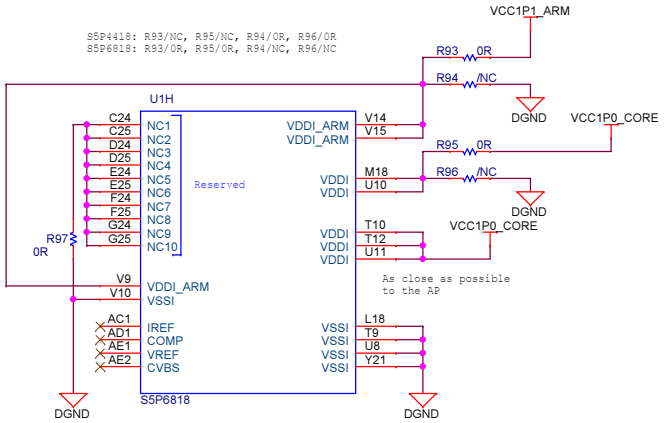
AP VIP&Display





Reserved Function



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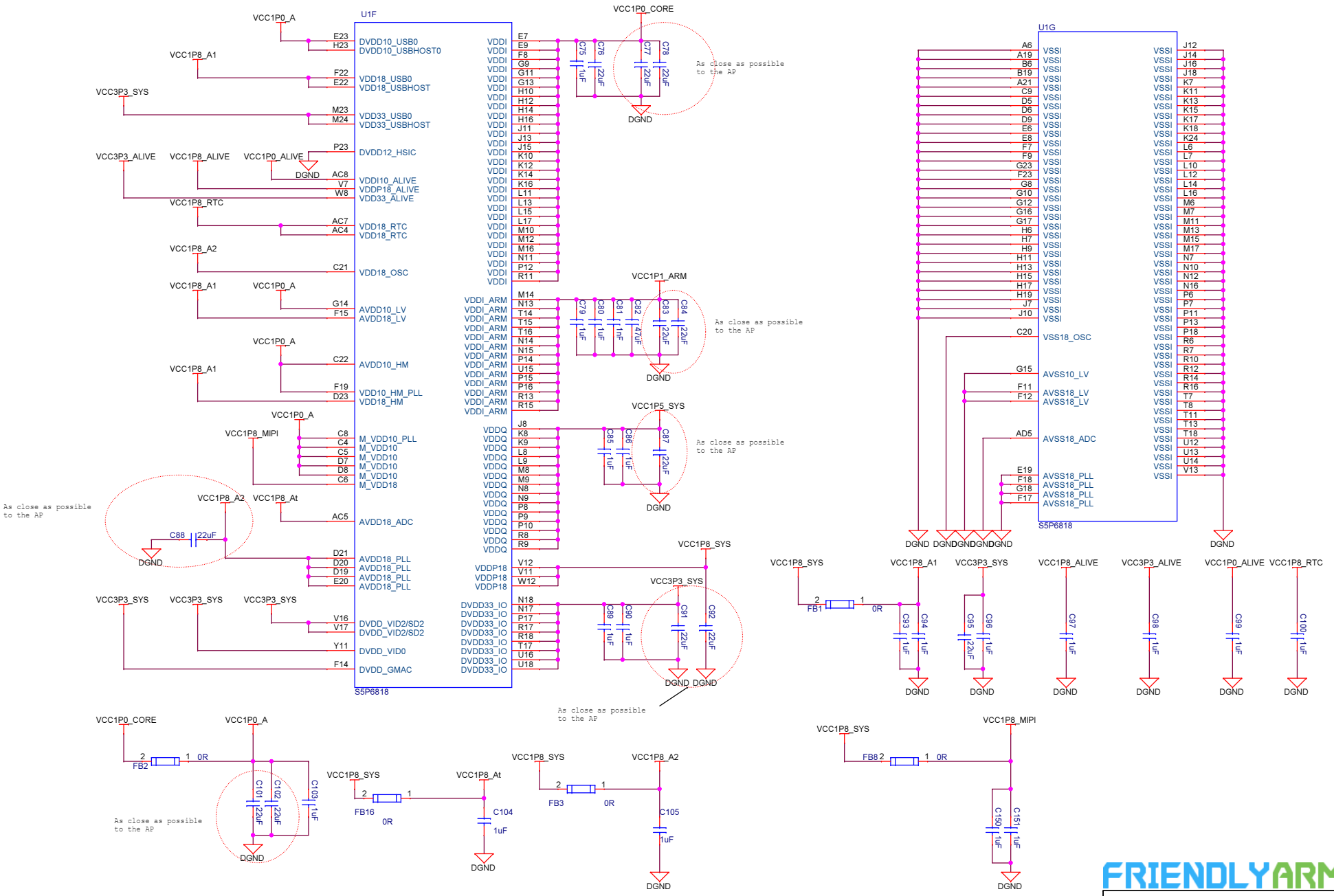
AP Power


U1F
S5P6818
VCC1P0_A
VCC1P8_A1
VCC3P3_SYS
VCC3P3_ALIVE
VCC1P8_ALIVE
VCC1P0_ALIVE
VCC1P8_RTC
VCC1P8_A2
VCC1P8_MIPI
VCC1P8_At
VCC1P0_CORE
VCC1P1_ARM
VCC1P5_SYS
VCC1P8_SYS
VCC3P3_SYS
DGND
As close as possible to the AP
U1G
S5P6818
VCC1P8_A1
VCC3P3_SYS
VCC1P8_ALIVE
VCC3P3_ALIVE
VCC1P0_ALIVE
VCC1P8_RTC
VCC1P0_CORE
VCC1P0_A
VCC1P8_SYS
VCC1P8_A2
VCC1P8_At
VCC1P8_MIPI
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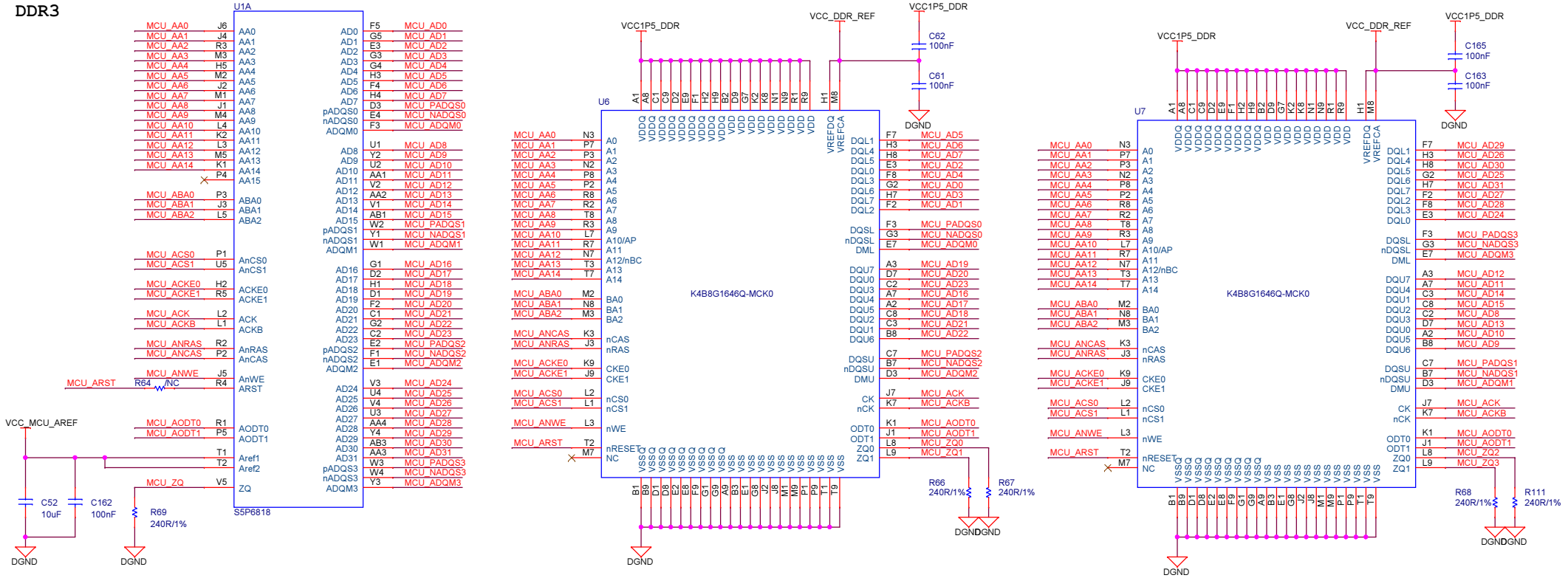
DDR3



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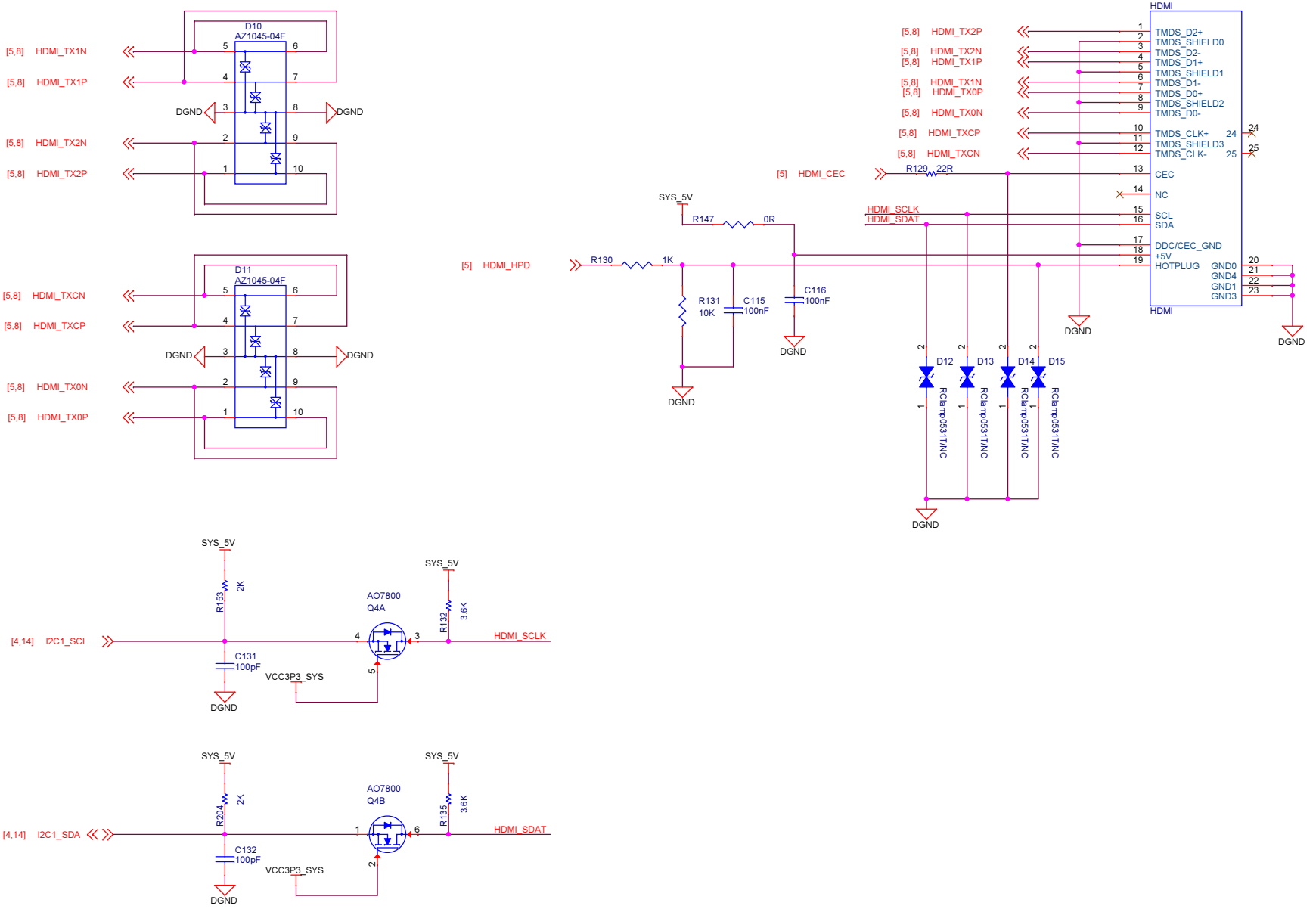
HDMI



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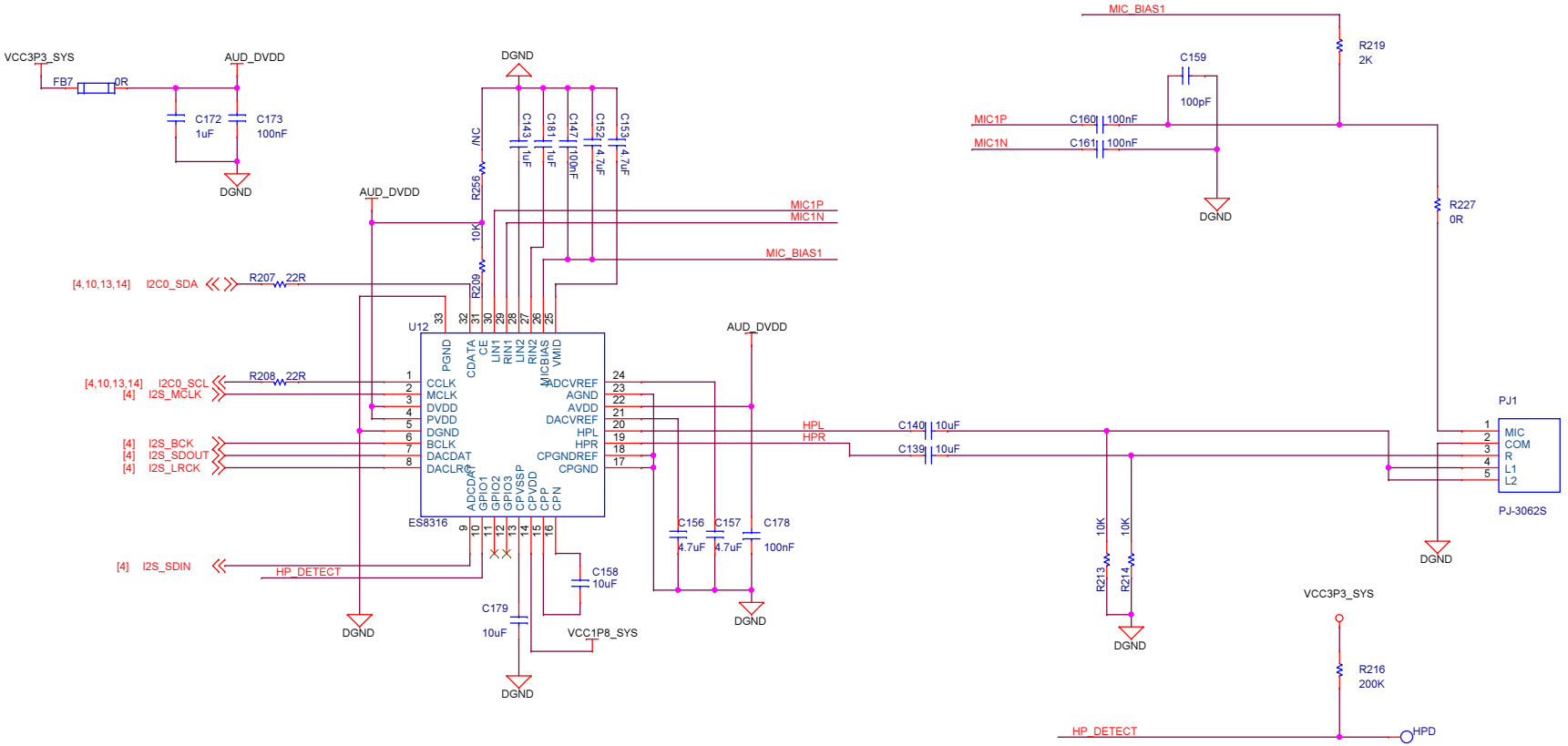
Audio



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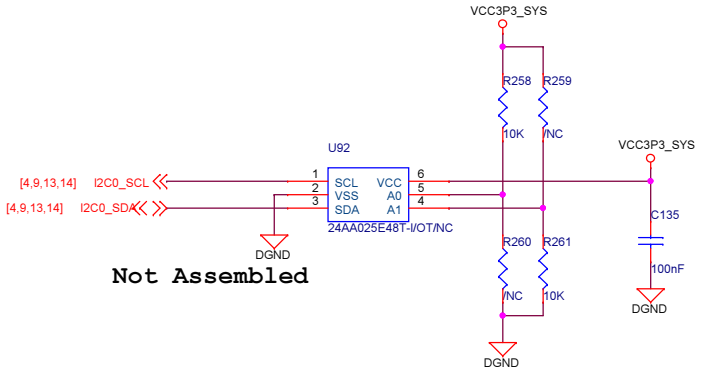
EEPROM with MAC Address



Not Assembled

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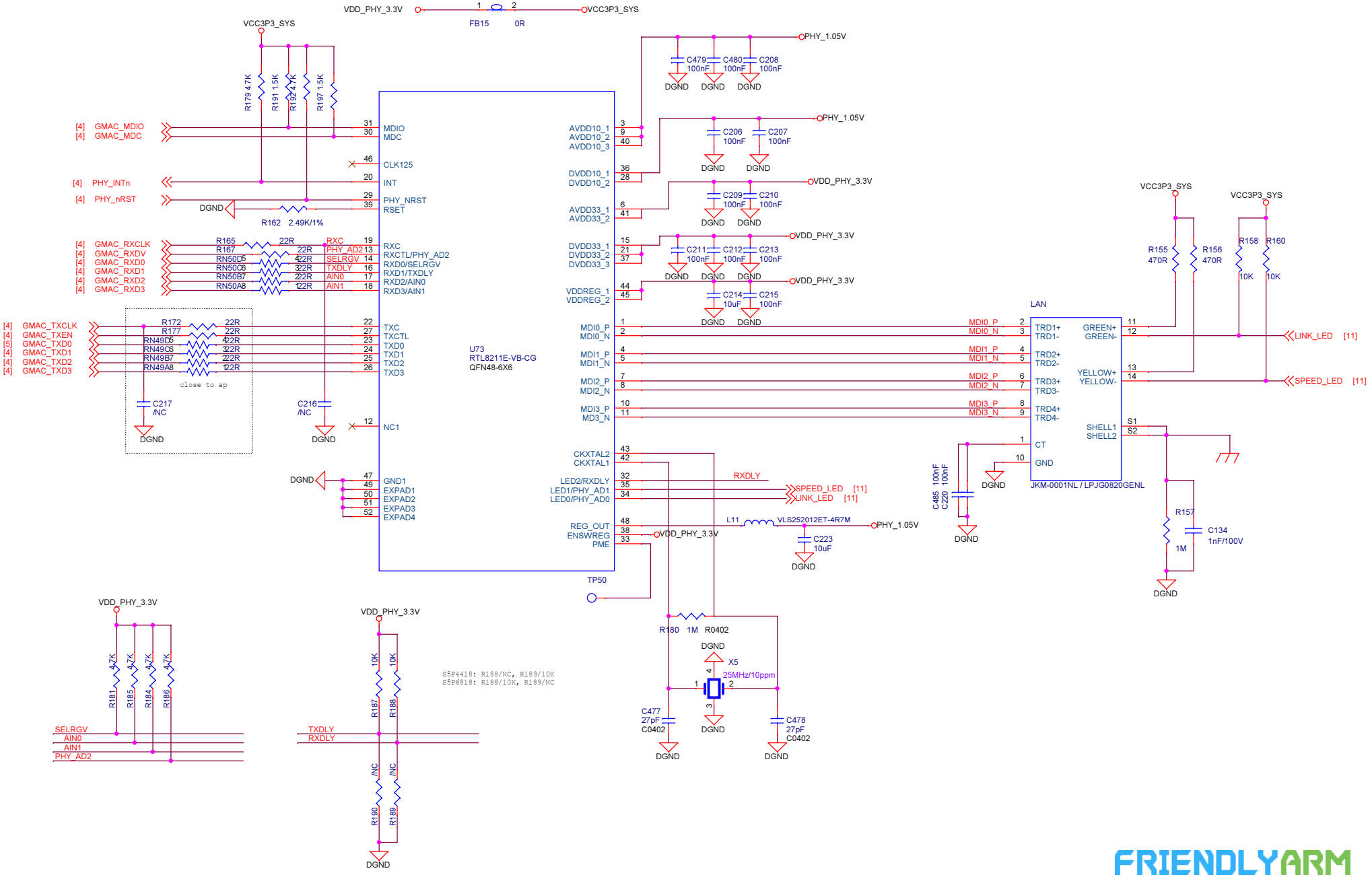
10/100/1000M Ethernet



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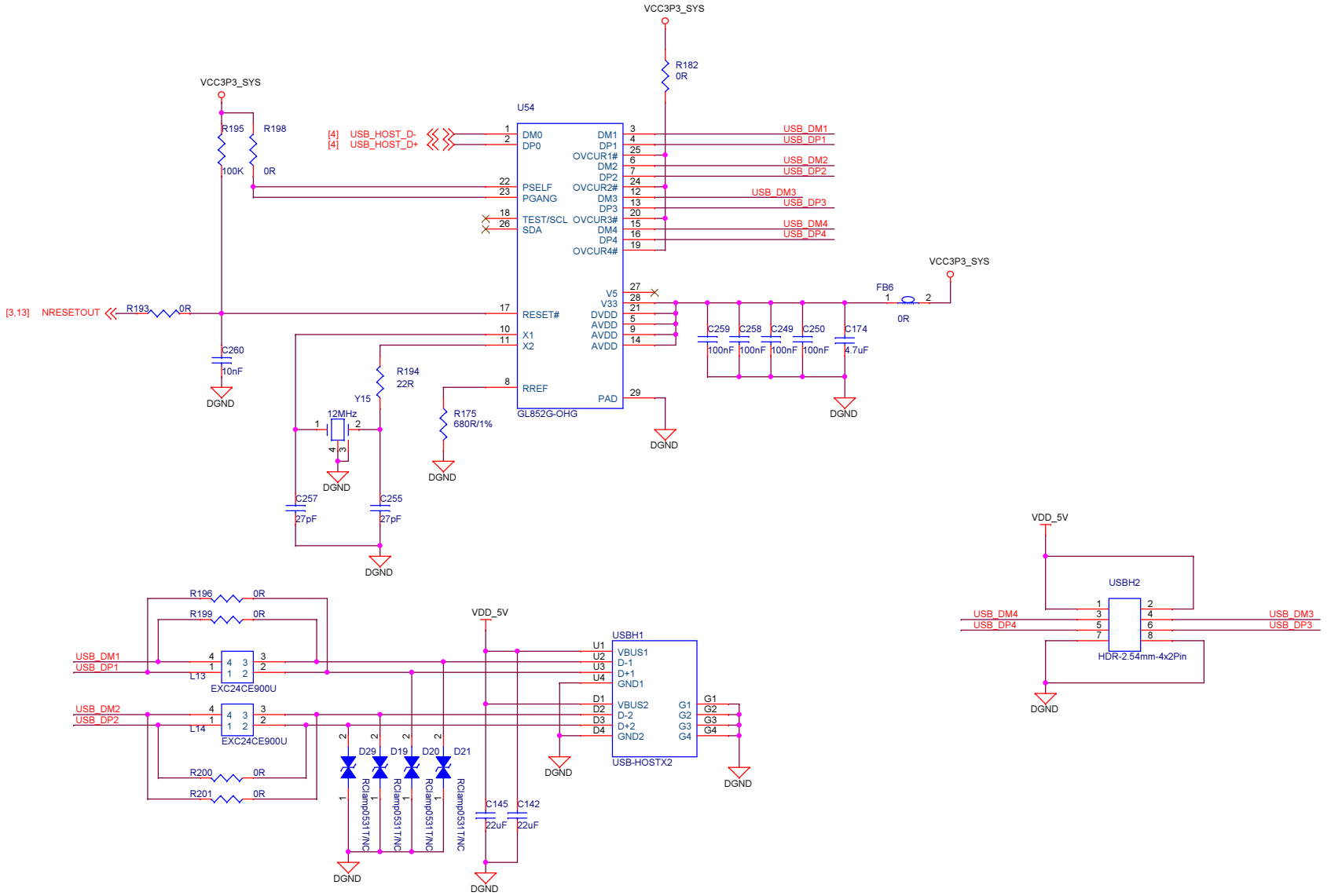
USB 2.0 Host



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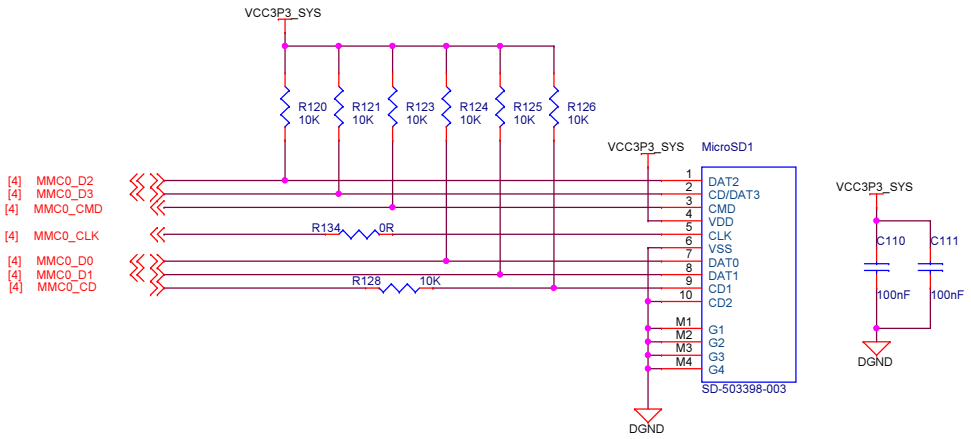
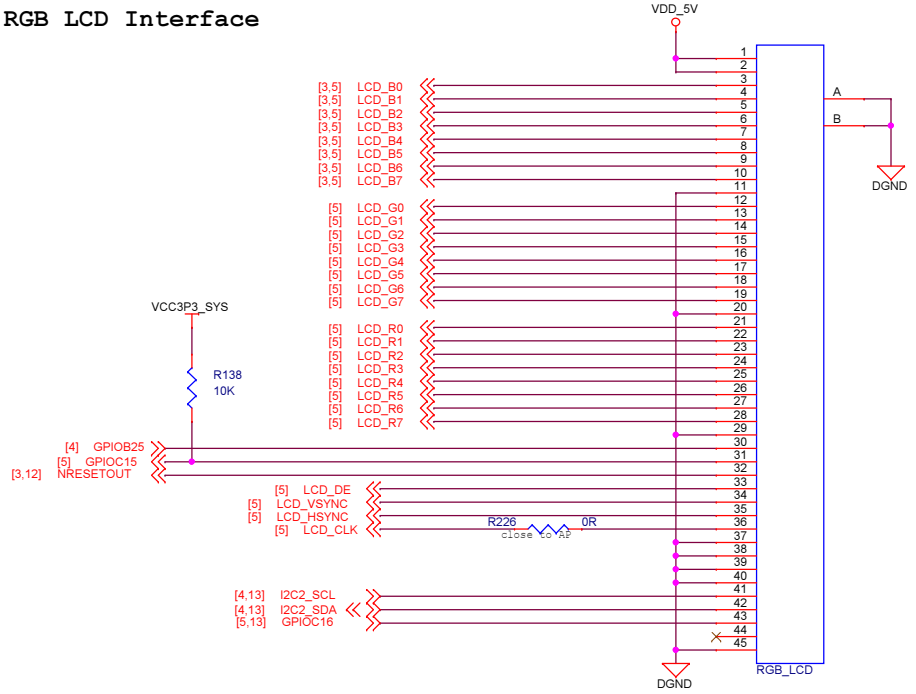
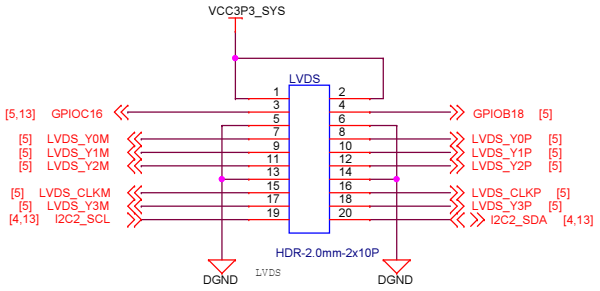
SDCARD



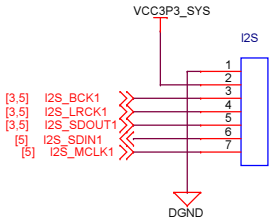
RGB LCD Interface



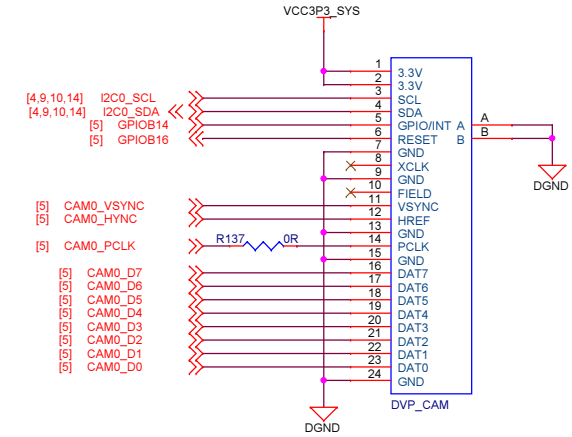
LVDS



I2S Interface



Camera Interface



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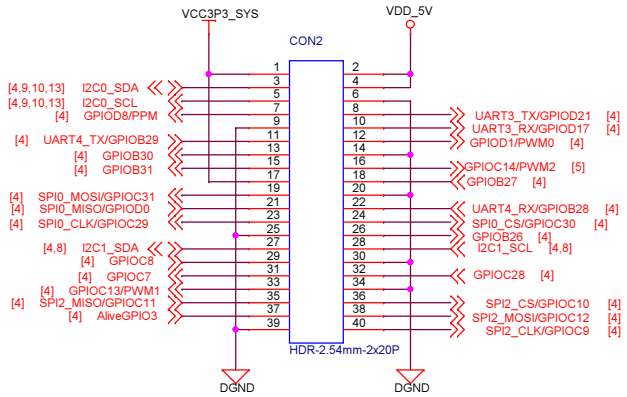
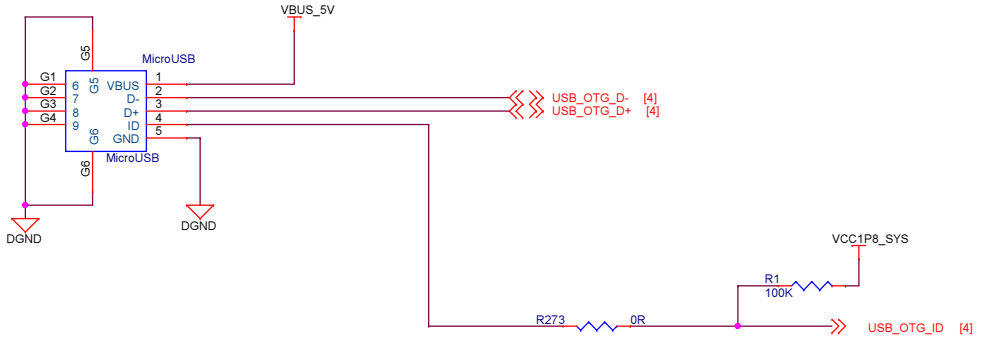
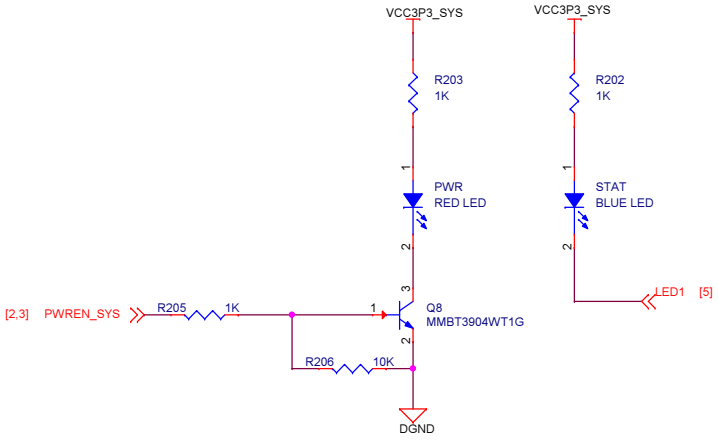
2.54mm Header

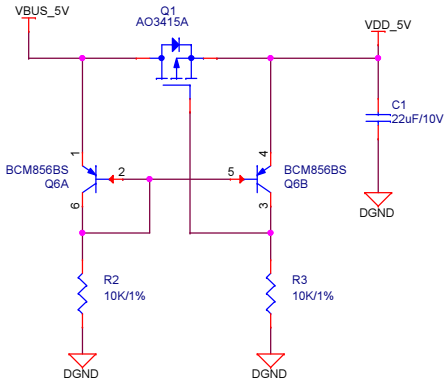


MicroUSB

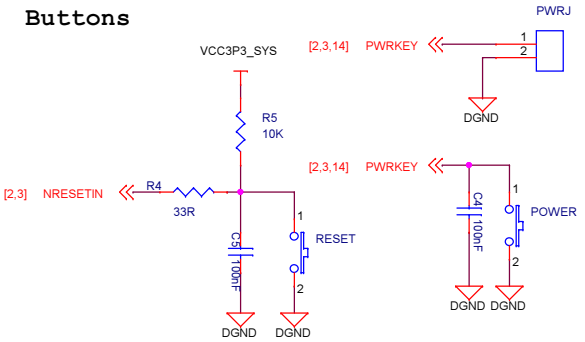


LEDs

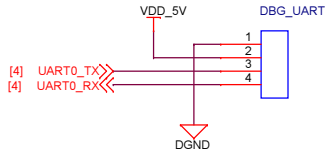




Buttons



Debug UART



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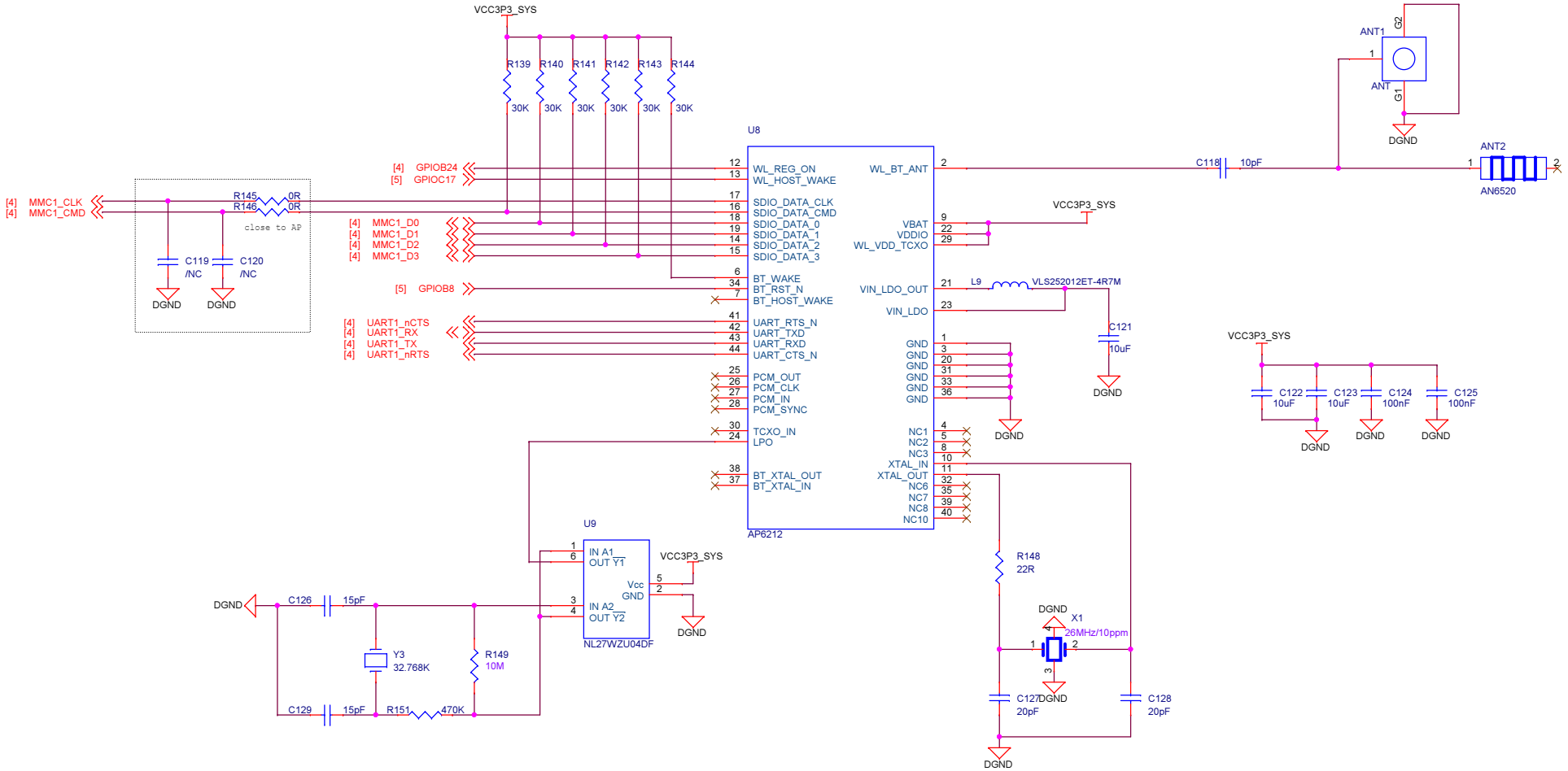
Wi-Fi/Bluetooth



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