Change with James:

- Replace RTC capacitor with battery since charging capacitor would take sometimes, as well as the voltage are only usable in a certain range.

Charging with 4.2- 0.7 = 3.5 volt is still higher than the voltage the RTC can handle which is 3V.

- Utilized Voltage Divider on 2nd Inverter node and V\_Latch node to determine NMOS resistance value for proper current.

Change by Bao:

- Reposition the side rail so it fit on the breadboard - Multiple of 2.54mm spacing

- Replace test pins with continuous header holes

- Resize and rearrange printed letters so they would print nicely

- Add letters for test pins

- Rewire signal rail and power rail for easy installation

- Move LED from bottom layer to front

Suggested change from Bao:

- Remove 1nF capacitor due protect the RTC. Current spike is allowed for the RTC INT => can blow the RTC

- The board when overrided to stay awake, the latch will pulled to GND. The shutdown pin is only have 1 resistor and 1 diode away from a feather pin.

This will cause over current sink from the feather pin if it tries to sent shutdown signal.