

## M251/M252 Series BSP Directory

Directory Introduction for 32-bit NuMicro® Family

### Directory Information

<b>Document</b>	Driver reference manual and revision history.
<b>Library</b>	Driver header and source files.
<b>SampleCode</b>	Driver sample code.

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## 1 Document Information

<b>CMSIS.html</b>	Document of CMSIS version 5.1.1.
<b>NuMicro M251_252 Series CMSIS BSP Revision History.pdf</b>	This document shows the revision history of M251/M252 BSP.
<b>NuMicro M251_252 Series CMSIS BSP Driver Reference.chm</b>	This document describes the usage of drivers in M251/M252 BSP.

## 2 Library Information

<b>CMSIS</b>	Cortex® Microcontroller Software Interface Standard (CMSIS) V5.1.1 definitions by Arm® Corp.
<b>Device</b>	CMSIS compliant device header file.
<b>SmartcardLib</b>	Smartcard library binary and header file.
<b>StdDriver</b>	All peripheral driver header and source files.

### 3 Sample Code Information

<b>CardReader</b>	USB CCID Smartcard Reader sample code
<b>Hard_Fault_Sample</b>	<p>Show hard fault information when hard fault happened.</p> <p>The hard fault handler show some information included program counter, which is the address where the processor was executing when the hard fault occur. The listing file (or map file) can show what function and instruction that was.</p> <p>It also shows the Link Register (LR), which contains the return address of the last function call. It can show the status where CPU comes from to get to this point.</p>
<b>ISP</b>	Sample code for Nuvoton NuMicro ISP Programming Tool.
<b>Semihost</b>	Show how to print and get character through IDE console window.
<b>StdDriver</b>	Sample code to demonstrate the usage of M251/M252 MCU peripheral driver APIs.
<b>Template</b>	A project template for M251/M252.
<b>XOM</b>	Demonstrate how to create XOM library and use it.

## 4 \SampleCode\ISP

ISP_DFU	Sample ISP firmware communicated with DFU (Device Firmware Upgrade) tool through a USB DFU interface.
ISP_HID	Sample ISP firmware communicated with ISP tool through a USB HID interface.
ISP_I2C	Sample ISP firmware communicated with ISP tool through an I <sup>2</sup> C interface.
ISP_RS485	Sample ISP firmware communicated with ISP tool through a RS485 interface.
ISP_SPI	Sample ISP firmware communicated with ISP tool through a SPI interface.
ISP_UART	Sample ISP firmware communicated with ISP tool through a UART interface.

## 5 \SampleCode\StdDriver

<b>ACMP_ComapreDAC</b>	Demonstrate ACMP comparison by comparing ACMP0_P0 input and DAC voltage and shows the result on UART console.
<b>ACMP_ComapreVBG</b>	Demonstrate ACMP comparison by comparing ACMP0_P0 input and VBG voltage and shows the result on UART console.
<b>ACMP_Wakeup</b>	Use ACMP to wake up system from Power-down mode while comparator output changes.
<b>ACMP_WindowCompare</b>	Show how to monitor ACMP input with window compare function.
<b>ACMP_WindowLatch</b>	Demonstrate how to use ACMP window latch mode.
<b>BPWM_Capture</b>	Use BPWM0 channel 0 (PA.0) to capture the BPWM1 channel 0 (PE.13) waveform.
<b>BPWM_DoubleBuffer</b>	Change duty cycle and period of output waveform by BPWM double buffer function.
<b>BPWM_OutputWaveform</b>	Demonstrate how to use BPWM counter output waveform.
<b>BPWM_SwitchDuty</b>	Change duty cycle of output waveform by configured period.
<b>BPWM_SyncStart</b>	Demonstrate how to use BPWM counter synchronous start function.
<b>CLK_ClockDetector</b>	Demonstrate the usage of clock fail detector and clock frequency range detector function.
<b>CRC_CCITT</b>	Implement CRC in CRC-CCITT mode and get the CRC checksum result.
<b>CRC_CRC8</b>	Implement CRC in CRC-8 mode and get the CRC checksum result.
<b>CRC_CRC32</b>	Implement CRC in CRC-32 mode and get the CRC checksum result.

<b>DAC_ExtPinTrigger</b>	Demonstrate external pin trigger DAC convert sine wave outputs.
<b>DAC_PDMA_TimerTrigger</b>	Show Timer trigger DAC to fetch data with PDMA and convert sine wave outputs.
<b>DAC_SoftwareTrigger</b>	Demonstrate software trigger DAC to convert sine wave outputs.
<b>DAC_TimerTrigger</b>	Demonstrate Timer trigger DAC to convert sine wave outputs.
<b>EADC_Accumulate</b>	Demonstrate how to get accumulate conversion result.
<b>EADC_ADINT_Trigger</b>	Use ADINT interrupt to trigger the EADC conversion.
<b>EADC_Average</b>	Demonstrate how to get average conversion result.
<b>EADC_BandGap</b>	Convert band-gap (Sample module 16) and print conversion result.
<b>EADC_OffsetCancel</b>	Demonstrate how to modify final EADC conversion result by offset cancellation.
<b>EADC_PDMA_PWM_Trigger</b>	Demonstrate how to trigger EADC by PWM and transfer conversion data by PDMA.
<b>EADC_Pending_Priority</b>	Demonstrate how to trigger multiple sample modules and got conversion results in order of priority.
<b>EADC_PWM_Trigger</b>	Demonstrate how to trigger EADC by PWM.
<b>EADC_ResultMonitor</b>	Monitor the conversion result of channel 2 by the digital compare function.
<b>EADC_SWTRG_Trigger</b>	Trigger EADC by writing EADC_SWTRG register.
<b>EADC_TempSensor</b>	Convert temperature sensor (Sample module 17) and print conversion result.
<b>EADC_Timer_Trigger</b>	Show how to trigger EADC by Timer.
<b>EADC_VBat</b>	Convert VBAT/4 (Sample module 18) and print conversion result.

<b>EBI_NOR</b>	Configure EBI interface to access NOR Flash connects on EBI interface.
<b>EBI_SRAM</b>	Configure EBI interface to access SRAM connects on EBI interface.
<b>FMC_CRC32</b>	Demonstrate how to use FMC CRC32 ISP command to calculate the CRC32 checksum of APROM and LDROM.
<b>FMC_ExeInSRAM</b>	Implement a code and execute in SRAM to program embedded Flash.
<b>FMC_IAP</b>	Demonstrate FMC IAP boot mode and show how to use vector remap function. LDROM image was embedded in APROM image and be programmed to LDROM Flash at run-time. This sample also shows how to branch between APROM and LDROM.
<b>FMC_MultiBoot</b>	Implement a multi-boot system to boot from different applications in APROM or LDROM by VECMAP.
<b>FMC_MultiWordProgram</b>	Show FMC multi-word program ISP command to program APROM 0x18000~0x20000 area.
<b>FMC_ReadAllOne</b>	Demonstrate how to use FMC Read-All-One ISP command to verify APROM or LDROM pages are all 0xFFFFFFFF or not.
<b>FMC_RW</b>	Show FMC read Flash IDs, erase, read, and write functions.
<b>FMC_XOM</b>	This sample code shows how to configure and setup an XOM region then perform XOM function.
<b>GPIO_EINTAndDebounce</b>	Show the usage of GPIO external interrupt function and de-bounce function.
<b>GPIO_INT</b>	Show the usage of GPIO interrupt function.
<b>GPIO_OutputInput</b>	Show how to set GPIO pin mode and use pin data input and output control.
<b>GPIO_PowerDown</b>	Show how to wake up system from Power-down mode by GPIO interrupt.



<b>I2C_EEPROM</b>	Read and write EEPROM via I <sup>2</sup> C interface.
<b>I2C_GCMode_Master</b>	Demonstrate how a master uses I <sup>2</sup> C address 0x0 to write data to I <sup>2</sup> C slave. This sample code needs to work with I2C_GCMode_Slave.
<b>I2C_GCMode_Slave</b>	Demonstrate how to receive master data in GC (General Call) mode. This sample code needs to work with I2C_GCMode_Master.
<b>I2C_Loopback</b>	Demonstrate how a master accesses slave.
<b>I2C_Master</b>	An I <sup>2</sup> C master mode demo code. This sample code needs to work with I2C_Slave sample code.
<b>I2C_MultiBytes_Master</b>	Demonstrate how to use multi-bytes API to access slave. This sample code needs to work with I2C_Slave.
<b>I2C_PDMA_TRX</b>	Demonstrate I <sup>2</sup> C PDMA mode, which need to connect I <sup>2</sup> C0 (master) and I <sup>2</sup> C1 (slave).
<b>I2C_SingleByte_Master</b>	Demonstrate how to use single byte API to access slave. This sample code needs to work with I2C_Slave.
<b>I2C_Slave</b>	An I <sup>2</sup> C slave mode demo code.
<b>I2C_Wakeup_Slave</b>	Demonstrate how to set I <sup>2</sup> C to wake up MCU from Power-down mode. This sample code needs to work with I2C_Master.
<b>OPA_Control</b>	Show how to control OPA.
<b>PDMA_BasicMode</b>	Use PDMA channel 2 to transfer data from memory to memory.
<b>PDMA_ScatterGather</b>	Use PDMA channel 4 to transfer data from memory to memory by scatter-gather mode.
<b>PDMA_ScatterGather_PingPongBuffer</b>	Use PDMA to implement Ping-Pong buffer by scatter-gather mode (memory to memory).
<b>PSIO_1Wire</b>	Use PSIO to access MAXIM DS18B20 digital thermometer.

<b>PSIO_DM512</b>	Use PSIO to implement DM512 protocol.
<b>PSIO_HDQ</b>	Use PSIO to access TI BQ2028 EEPROM.
<b>PSIO_IR</b>	Use PSIO to implement NEC IR protocol.
<b>PSIO_LED</b>	Use PSIO to control Worldsemi WS2812 LED.
<b>PSIO_Microwire</b>	Use PSIO to access Atmel T93C46D EEPROM.
<b>PSIO_PS2_Device</b>	Use PSIO to implement PS/2 device.
<b>PSIO_PS2_Host</b>	Use PSIO to implement PS/2 host.
<b>PSIO_Wiegand</b>	Use PSIO to access HZ1050 RFID reader.
<b>PWM_Brake</b>	Demonstrate how to use PWM brake function.
<b>PWM_Capture</b>	Capture the PWM1 channel 0 waveform by PWM1 channel 2.
<b>PWM_DeadTime</b>	Demonstrate how to use PWM dead-time insertion function.
<b>PWM_DoubleBuffer</b>	Change duty cycle and period of output waveform by PWM double buffer function.
<b>PWM_OutputWaveform</b>	Demonstrate how to use PWM output waveform.
<b>PWM_PDMA_Capture</b>	Capture the PWM1 channel 0 waveform by PWM1 channel 2, and use PDMA to transfer captured data.
<b>PWM_SwitchDuty</b>	Change duty cycle of PWM output waveform by configured period.
<b>PWM_SyncStart</b>	Demonstrate how to use PWM counter synchronous start function.
<b>QSPI_DualMode_Flash</b>	Access SPI Flash using QSPI dual mode.
<b>QSPI_QuadMode_Flash</b>	Access SPI Flash using QSPI quad mode.
<b>QSPI_Slave3Wire</b>	Configure QSPI0 as Slave 3 wire mode and demonstrate how to communicate with an off-chip SPI Master device with FIFO mode. This sample code needs

	to work with SPI_MasterFIFOmode sample code.
<b>RTC_Alarm_Test</b>	Demonstrate the RTC alarm function. It sets an alarm 10 seconds after execution.
<b>RTC_Alarm_Wakeup</b>	Use RTC alarm interrupt event to wake up system.
<b>RTC_Spare_Access</b>	Show how to access RTC spare registers in supported chip.
<b>RTC_Static_Tamper</b>	Show how to use RTC static tamper function in supported chip.
<b>RTC_Time_Display</b>	Demonstrate the RTC function and displays current time to the UART console.
<b>SC_ReadATR</b>	Read the smartcard ATR from Smartcard interface.
<b>SC_ReadSimPhoneBook</b>	Demonstrate how to read phone book information in the SIM card.
<b>SC_Timer</b>	Demonstrate how to use SC embedded timer.
<b>SCUART_TxRx</b>	Demonstrate Smartcard UART mode by connecting PB.4 and PB.5 pins.
<b>SPI_Flash</b>	Access SPI Flash through SPI interface.
<b>SPI_HalfDuplex</b>	Demonstrate SPI half-duplex mode. Configure SPI0 as master mode and SPI1 as slave mode. Both SPI0 and SPI1 are half-duplex mode.
<b>SPI_Loopback</b>	A SPI read/write demo connecting SPI0 MISO and MOSI pins.
<b>SPI_MasterFIFOmode</b>	Configure SPI0 as master mode and demonstrate how to communicate with an off-chip SPI slave device with FIFO mode. This sample code needs to work with SPI_SlaveFIFOmode sample code.
<b>SPI_PDMA_LoopTest</b>	Demonstrate SPI data transfer with PDMA. SPI0 will be configured as slave mode and QSPI0 will be configured as master mode. Both Tx PDMA function and Rx PDMA function will be enabled.

<b>SPI_SlaveFIFOmode</b>	Configure SPI0 as slave mode and demonstrate how to communicate with an off-chip SPI master device with FIFO mode. This sample code needs to work with SPI_MasterFIFOmode sample code.
<b>SPII2S_Master</b>	Configure SPI0 as I <sup>2</sup> S master mode and demonstrate how I <sup>2</sup> S works in master mode. This sample code needs to work with SPII2S_Slave sample code.
<b>SPII2S_PDMA_Codec</b>	An I <sup>2</sup> S demo with PDMA function connected with audio codec.
<b>SPII2S_PDMA_Play</b>	An I <sup>2</sup> S demo for playing data and demonstrating how I <sup>2</sup> S works with PDMA.
<b>SPII2S_PDMA_PlayRecord</b>	An I <sup>2</sup> S demo for playing and recording data with PDMA function.
<b>SPII2S_PDMA_Record</b>	An I <sup>2</sup> S demo for recording data and demonstrating how I <sup>2</sup> S works with PDMA.
<b>SPII2S_Slave</b>	Configure SPI0 as I <sup>2</sup> S slave mode and demonstrate how I <sup>2</sup> S works in slave mode. This sample code needs to work with SPII2S_Master sample code.
<b>SYS_BODWakeup</b>	Demonstrate how to wake up system from Power-down mode by brown-out detector interrupt.
<b>SYS_DPDMODE_Wakeup</b>	Demonstrate how to wake up system from Deep Power-down mode by Wake-up pin (PA.0), Wake-up Timer, RTC Tick, RTC Alarm, or RTC Tamper 0.
<b>SYS_PLLClockOutput</b>	Change system clock to different PLL frequency and output system clock from CLK0 pin.
<b>SYS_TrimHIRC</b>	Demonstrate how to use LXT to trim HIRC.
<b>SYS_TrimMIRC</b>	Demonstrate how to use Timer to trim MIRC.
<b>TIMER_ACMPTrigger</b>	Use ACMP to trigger Timer reset mode.
<b>TIMER_CaptureCounter</b>	Show how to use the Timer2 capture function to capture Timer2 counter value.

<b>TIMER_Delay</b>	Demonstrate the usage of TIMER_Delay( ) API to generate a 1 second delay.
<b>TIMER_EventCounter</b>	Use pin PB.4 to demonstrates Timer event counter function.
<b>TIMER_FreeCountingMode</b>	Use the Timer pin PA.11 to demonstrate Timer free counting mode function, and display the measured input frequency to UART console.
<b>TIMER_InterTimerTriggerMode</b>	Use the Timer pin PB.5 to demonstrate inter-timer trigger mode function, and display the measured input frequency to UART console.
<b>TIMER_Periodic</b>	Use the Timer periodic mode to generate Timer interrupt every 1 second.
<b>TIMER_PeriodicINT</b>	Implement Timer counting in periodic mode.
<b>TIMER_PWM_ChangeDuty</b>	Change duty cycle and period of output waveform in PWM down count type.
<b>TIMER_PWM_OutputWaveform</b>	Demonstrate output different duty waveform in Timer0~3 PWM.
<b>TIMER_TimeoutWakeup</b>	Use Timer0 periodic time-out interrupt event to wake up system.
<b>TIMER_ToggleOut</b>	Demonstrate the Timer0 toggle out function on pin PB.5.
<b>UART_AutoBaudRate</b>	Show how to use auto baud rate detection function.
<b>UART_AutoFlow</b>	Transmit and receive data using auto flow control.
<b>UART_IrDA</b>	Transmit and receive UART data in UART IrDA mode.
<b>UART_LIN</b>	Demonstrate how to send data to LIN bus.
<b>UART_PDMA</b>	Demonstrate UART transmit and receive function with PDMA.
<b>UART_RS485</b>	Transmit and receive data in UART RS485 mode.
<b>UART_SingleWire</b>	Transmit and receive data in UART single-wire mode.

<b>UART_TxRxFunction</b>	Transmit and receive data from PC terminal through RS232 interface.
<b>UART_Wakeup</b>	Show how to wake up system from Power-down mode by UART interrupt.
<b>USBD_Audio_Codec</b>	Demonstrate how to implement a USB audio class device.
<b>USBD_HID_Keyboard</b>	Demonstrate how to implement a USB keyboard device. This sample code supports to use GPIO to simulate key input.
<b>USBD_HID_Mouse</b>	Simulate a USB mouse and draws circle on the screen.
<b>USBD_HID_MouseKeyboard</b>	Simulate an USB HID mouse and HID keyboard. Mouse draws circle on the screen and Keyboard uses GPIO to simulate key input.
<b>USBD_HID_RemoteWakeup</b>	Simulate a HID mouse supports USB suspend and remote wakeup.
<b>USBD_HID_Transfer</b>	Demonstrate how to transfer data between a USB device and PC through a USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
<b>USBD_HID_Transfer_And_Keyboard</b>	Demonstrate how to implement a composite device of HID transfer and keyboard. Transfer data between USB device and PC through USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
<b>USBD_HID_Transfer_And_MSC</b>	Demonstrate how to implement a composite device of HID transfer and mass storage. Transfer data between USB device and PC through USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
<b>USBD_HID_Transfer_CTRL</b>	Use USB host core driver and HID driver. It shows how to submit HID class request and how to read data from control pipe. A windows tool is also included in this sample code to connect with a USB device.

<b>USBD_Mass_Storage_CDROM</b>	Demonstrate the emulation of USB mass storage device, CD-ROM.
<b>USBD_Mass_Storage_Flash</b>	Use internal flash as backend storage media to simulate a USB pen drive.
<b>USBD_Mass_Storage_SRAM</b>	Use internal SRAM as backend storage media to simulate a USB pen drive.
<b>USBD_Micro_Printer</b>	Demonstrate how to implement a USB micro printer device.
<b>USBD_Printer_And_HID_Transfer</b>	Demonstrate how to implement a composite device of USB micro printer and HID transfer. Transfer data between USB device and PC through USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
<b>USBD_VCOM_And_HID_Keyboard</b>	Demonstrate how to implement a composite device of VCOM and HID keyboard.
<b>USBD_VCOM_And_HID_Transfer</b>	Demonstrate how to implement a composite device of VCOM and HID transfer. Transfer data between USB device and PC through USB HID interface. A windows tool is also included in this sample code to connect with a USB device.
<b>USBD_VCOM_And_Mass_Storage</b>	Demonstrate how to implement a composite device of VCOM and mass storage.
<b>USBD_VCOM_DualPort</b>	Demonstrate how to implement a USB dual virtual COM port device.
<b>USBD_VCOM_SerialEmulator</b>	Demonstrate how to implement a USB virtual COM port device.
<b>USCI_I2C_EEPROM</b>	Show how to use USCI_I2C interface to access EEPROM.
<b>USCI_I2C_Lookback</b>	Show an I <sup>2</sup> C master how to access 7-bit address slave via loopback of 2 USCI ports.
<b>USCI_I2C_Loopback_10bit</b>	Show an I <sup>2</sup> C master how to access 10-bit address slave



	via loopback of 2 USCI ports.
<b>USCI_I2C_Master</b>	Show an I <sup>2</sup> C master how to access 7-bit address slave. This sample code needs to work with USCI_I2C_Slave sample code.
<b>USCI_I2C_Master_10bit</b>	Show an I <sup>2</sup> C master how to access 10-bit address slave. This sample code needs to work with USCI_I2C_Slave_10bit sample code.
<b>USCI_I2C_Monitor</b>	Use USCI_I2C to monitor and log I2C bus traffic.
<b>USCI_I2C_MultiBytes_Master</b>	Use UI2C multiple-byte functions to read and write data to slave. Need to work with the USCI_I2C_Slave sample code.
<b>USCI_I2C_SingleByte_Master</b>	Use UI2C single-byte functions to read and write data to slave. Need to work with the USCI_I2C_Slave sample code.
<b>USCI_I2C_Slave</b>	Show an I <sup>2</sup> C 7-bit address slave how to receive data from master.
<b>USCI_I2C_Slave_10bit</b>	Show an I <sup>2</sup> C 10-bit address slave how to receive data from master. This sample code needs to work with USCI_I2C_Master_10bit sample code.
<b>USCI_I2C_Wakeup_Slave</b>	Demonstrate how to set I <sup>2</sup> C to wake up MCU from Power-down mode. This sample code needs to work with USCI_I2C_Master sample code.
<b>USCI_SPI_Loopback</b>	Implement USCI_SPI0 master loop back transfer. This sample code needs to connect USCI_SPI0_MISO pin and USCI_SPI0_MOSI pin together. It will compare the received data with transmitted data.
<b>USCI_SPI_MasterMode</b>	Configure USCI_SPI0 as master mode and demonstrate how to communicate with an off-chip SPI Slave device. Needs to work with USCI_SPI_SlaveMode sample code.
<b>USCI_SPI_PDMA_LoopTest</b>	Demonstrate SPI data transfer with PDMA. USCI_SPI0 will be configured as master mode and USCI_SPI1 will be configured as slave mode. Both Tx PDMA function and Rx PDMA function will be enabled.



<b>USCI_SPI_SlaveMode</b>	Configure USCI_SPI0 as slave mode and demonstrate how to communicate with an off-chip SPI master device. This sample code needs to work with USCI_SPI_MasterMode sample code.
<b>USCI_UART_AutoBaudRate</b>	Show how to use auto baud rate detection function.
<b>USCI_UART_Autoflow_Master</b>	Transmit and receive data with auto flow control. This sample code needs to work with USCI_UART_Autoflow_Slave sample code.
<b>USCI_UART_Autoflow_Slave</b>	Transmit and receive data with auto flow control. This sample code needs to work with USCI_UART_Autoflow_Master sample code.
<b>USCI_UART_PDMA</b>	This is a USCI_UART PDMA demo and need to connect USCI_UART Tx and Rx.
<b>USCI_UART_RS485_Master</b>	Transmit and receive data in RS485 mode. This sample code needs to work with USCI_UART_RS485_Slave sample code.
<b>USCI_UART_RS485_Slave</b>	Transmit and receive data in RS485 mode. This sample code needs to work with USCI_UART_RS485_Master sample code.
<b>USCI_UART_TxRxFunction</b>	Transmit and receive data from PC terminal through RS232 interface.
<b>USCI_UART_Wakeup</b>	Show how to wake up system from Power-down mode by USCI interrupt in UART mode.
<b>WDT_TimeoutWakeupAndReset</b>	Implement WDT time-out interrupt event to wake up system and generate time-out reset system event while WDT time-out reset delay period expired.
<b>WWDT_CompareINT</b>	Show how to reload the WWDT counter value.

## **6 \SampleCode\XOM**

<b>XOMLib</b>	Demonstrate how to create XOM library.
<b>XOMLibDemo</b>	Demonstrate how to use XOMLib.

## 7 Sample Code Compatibility List

Category	C	D	E	G
<div>Part Number</div> <div>Sample Code</div>	M251EC2AE M251FC2AE M251ZC2AE M252EC2AE M252FC2AE M252ZC2AE	M251LC2AE M251LD2AE M251SC2AE M251SD2AE M251ZD2AE M252LC2AE M252LD2AE M252SC2AE M252SD2AE M252ZD2AE	M251KE3AE M251LE3AE M251SE3AE M252KE3AE M252LE3AE M252SE3AE	M251KG6AE M251LG6AE M251SG6AE M252KG6AE M252LG6AE M252SG6AE
ACMP_ComapreDAC	-	-	-	√
ACMP_ComapreVBG	-	√	√	√
ACMP_Wakeup	-	√	√	√
ACMP_WindowCompare	-	√	√	√
ACMP_WindowLatch	-	√	√	√
BPWM_Capture	-	√	√	√
BPWM_DoubleBuffer	-	√	√	√
BPWM_OutputWaveform	-	√	√	√
BPWM_SwitchDuty	-	√	√	√
BPWM_SyncStart	-	√	√	√
CLK_ClockDetector <sup>1</sup>	√	√	√	√
CRC_CCITT	√	√	√	√
CRC_CRC8	√	√	√	√

<sup>1</sup> CLK\_ClockDetector requires HXT.

Category	C	D	E	G
CRC_CRC32	√	√	√	√
DAC_ExtPinTrigger	-	-	-	√
DAC_PDMA_TimerTrigger	-	-	-	√
DAC_SoftwareTrigger	-	-	-	√
DAC_TimerTrigger	-	-	-	√
EADC_Accumulate	√	√	√	√
EADC_ADINT_Trigger	√	√	√	√
EADC_Average	√	√	√	√
EADC_BandGap	√	√	√	√
EADC_OffsetCancel	√	√	√	√
EADC_PDMA_PWM_Trigger	√	√	√	√
EADC_Pending_Priority	√	√	√	√
EADC_PWM_Trigger	√	√	√	√
EADC_ResultMonitor	√	√	√	√
EADC_SWTRG_Trigger	√	√	√	√
EADC_TempSensor	√	√	√	√
EADC_Timer_Trigger	√	√	√	√
EADC_VBat	-	-	√	√
EBI_NOR	-	-	√	√
EBI_SRAM	-	-	√	√
FMC_CRC32	√	√	√	√
FMC_ExecInSRAM	√	√	√	√
FMC_IAP	√	√	√	√
FMC_MultiBoot	√	√	√	√

Category	C	D	E	G
FMC_MultiWordProgram	√	√	√	√
FMC_ReadAllOne	√	√	√	√
FMC_RW	√	√	√	√
FMC_XOM	√	√	√	√
GPIO_EINTAndDebounce	√	√	√	√
GPIO_INT	√	√	√	√
GPIO_OutputInput	√	√	√	√
GPIO_PowerDown	√	√	√	√
I2C_EEPROM	√	√	√	√
I2C_GCMode_Master	√	√	√	√
I2C_GCMode_Slave	√	√	√	√
I2C_Loopback	√	√	√	√
I2C_Master	√	√	√	√
I2C_MultiBytes_Master	√	√	√	√
I2C_PDMA_TRX	√	√	√	√
I2C_SingleByte_Master	√	√	√	√
I2C_Slave	√	√	√	√
I2C_Wakeup_Slave	√	√	√	√
OPA_Control	-	-	-	√
PDMA_BasicMode	√	√	√	√
PDMA_ScatterGather	√	√	√	√
PDMA_ScatterGather_PingPongBuffer	√	√	√	√
PSIO_1Wire	-	√	√	√
PSIO_DM512	-	√	√	√

Category	C	D	E	G
PSIO_HDQ	-	√	√	√
PSIO_IR	-	√	√	√
PSIO_LED	-	√	√	√
PSIO_Microwire	-	√	√	√
PSIO_PS2_Device	-	√	√	√
PSIO_PS2_Host	-	√	√	√
PSIO_Wiegand	-	√	√	√
PWM_Brake	√	√	√	√
PWM_Capture	√	√	√	√
PWM_DeadTime	√	√	√	√
PWM_DoubleBuffer	√	√	√	√
PWM_OutputWaveform	√	√	√	√
PWM_PDMA_Capture	√	√	√	√
PWM_SwitchDuty	√	√	√	√
PWM_SyncStart	√	√	√	√
QSPI_DualMode_Flash	√	√	√	√
QSPI_QuadMode_Flash	√	√	√	√
QSPI_Slave3Wire	√	√	√	√
RTC_Alarm_Test	√	√	√	√
RTC_Alarm_Wakeup	√	√	√	√
RTC_Spare_Access	-	√	√	√
RTC_Static_Tamper	-	√	√	√
RTC_Time_Display	√	√	√	√
SC_ReadATR	√	√	√	√

Category	C	D	E	G
SC_ReadSimPhoneBook	√	√	√	√
SC_Timer	√	√	√	√
SCUART_TxRx	√	√	√	√
SPI_Flash	-	√	√	√
SPI_HalfDuplex	-	√	√	√
SPI_Loopback	-	√	√	√
SPI_MasterFIFOmode	-	√	√	√
SPI_PDMA_LoopTest	-	√	√	√
SPI_SlaveFIFOmode	-	√	√	√
SPII2S_Master	-	√	√	√
SPII2S_PDMA_Codec	-	√	√	√
SPII2S_PDMA_Play	-	√	√	√
SPII2S_PDMA_PlayRecord	-	√	√	√
SPII2S_PDMA_Record	-	√	√	√
SPII2S_Slave	-	√	√	√
SYS_BODWakeup	√	√	√	√
SYS_DPDMODE_Wakeup <sup>2</sup>	√	√	√	√
SYS_PLLClockOutput	-	√	√	√
SYS_TrimHIRC	√	√	√	√
SYS_TrimMIRC	√	√	√	√
TIMER_ACMPTrigger	-	√	√	√
TIMER_CaptureCounter	√	√	√	√

<sup>2</sup> SYS\_DPDMODE\_Wakeup does not support tamper pin wakeup function in Category-C parts.

Category	C	D	E	G
TIMER_Delay	√	√	√	√
TIMER_EventCounter	√	√	√	√
TIMER_FreeCountingMode	√	√	√	√
TIMER_InterTimerTriggerMode	√	√	√	√
TIMER_Periodic	√	√	√	√
TIMER_PeriodicINT	√	√	√	√
TIMER_PWM_ChangeDuty	√	√	√	√
TIMER_PWM_OutputWaveform	√	√	√	√
TIMER_TimeoutWakeup	√	√	√	√
TIMER_ToggleOut	√	√	√	√
UART_AutoBaudRate	√	√	√	√
UART_AutoFlow	√	√	√	√
UART_IrDA	√	√	√	√
UART_LIN	√	√	√	√
UART_PDMA	√	√	√	√
UART_RS485	√	√	√	√
UART_SingleWire	√	√	√	√
UART_TxRxFunction	√	√	√	√
UART_Wakeup	√	√	√	√
USBD_Audio_Codec <sup>34</sup>	-	√	√	√

<sup>3</sup> Only M252 series support USB D samples.

<sup>4</sup> USBD\_Audio\_Codec requires I<sup>2</sup>S.



Category	C	D	E	G
USBD_CCID <sup>5</sup>	-	√	√	√
USBD_HID_Keyboard	√	√	√	√
USBD_HID_Mouse	√	√	√	√
USBD_HID_MouseKeyboard	√	√	√	√
USBD_HID_RemoteWakeup	√	√	√	√
USBD_HID_Transfer	√	√	√	√
USBD_HID_Transfer_And_Keyboard	√	√	√	√
USBD_HID_Transfer_And_MSC <sup>6</sup>	-	√	√	√
USBD_HID_Transfer_CTRL	√	√	√	√
USBD_Mass_Storage_CDROM	√	√	√	√
USBD_Mass_Storage_Flash	-	√	√	√
USBD_Mass_Storage_SRAM	-	-	-	√
USBD_Micro_Printer	√	√	√	√
USBD_Printer_And_HID_Transfer	√	√	√	√
USBD_VCOM_And_HID_Keyboard	√	√	√	√
USBD_VCOM_And_HID_Transfer	√	√	√	√
USBD_VCOM_And_Mass_Storage	-	√	√	√
USBD_VCOM_DualPort	√	√	√	√
USBD_VCOM_SerialEmulator	√	√	√	√
USCI_I2C_EEPROM	√	√	√	√

<sup>5</sup> Limited by flash size.

<sup>6</sup> Mass storage function of USB samples requires enough storage size for distinct file systems of operation systems.

Category	C	D	E	G
USCI_I2C_Lookback <sup>7</sup>	-	√	√	√
USCI_I2C_Loopback_10bit	-	√	√	√
USCI_I2C_Master	√	√	√	√
USCI_I2C_Master_10bit	√	√	√	√
USCI_I2C_Monitor	√	√	√	√
USCI_I2C_MultiBytes_Master	√	√	√	√
USCI_I2C_SingleByte_Master	√	√	√	√
USCI_I2C_Slave	√	√	√	√
USCI_I2C_Slave_10bit	√	√	√	√
USCI_I2C_Wakeup_Slave	√	√	√	√
USCI_SPI_Loopback	√	√	√	√
USCI_SPI_MasterMode	√	√	√	√
USCI_SPI_PDMA_LoopTest	-	√	√	√
USCI_SPI_SlaveMode	√	√	√	√
USCI_UART_AutoBaudRate	√	√	√	√
USCI_UART_AutoFlow_Master	√	√	√	√
USCI_UART_AutoFlow_Slave	√	√	√	√
USCI_UART_PDMA	√	√	√	√
USCI_UART_RS485_Master	√	√	√	√
USCI_UART_RS485_Slave	√	√	√	√
USCI_UART_TxRxFunction	√	√	√	√

<sup>7</sup> USCI\_I2C\_Lookback and USCI\_I2C\_Loopback\_10bit require two USCI\_I2C ports.

Category	C	D	E	G
USCI_UART_Wakeup <sup>8</sup>	√	√	√	√
WDT_TimeoutWakeupAndReset	√	√	√	√
WWDT_CompareINT	√	√	√	√

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<sup>8</sup> USCI\_UART\_Wakeup does not support nCTS wakeup function in Category-C parts.

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