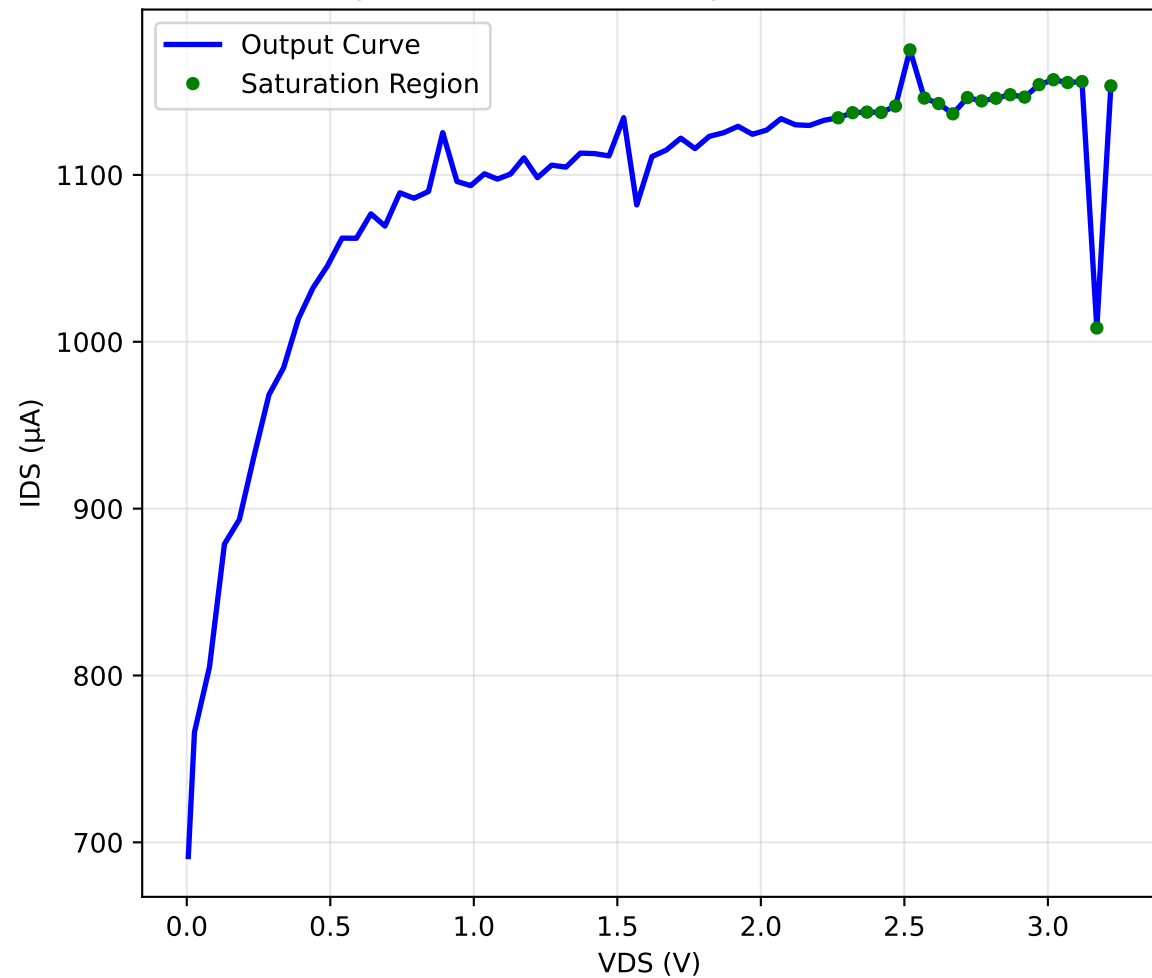


Output Conductance Analysis (Gate = 1.0V)



Output Conductance vs VDS

