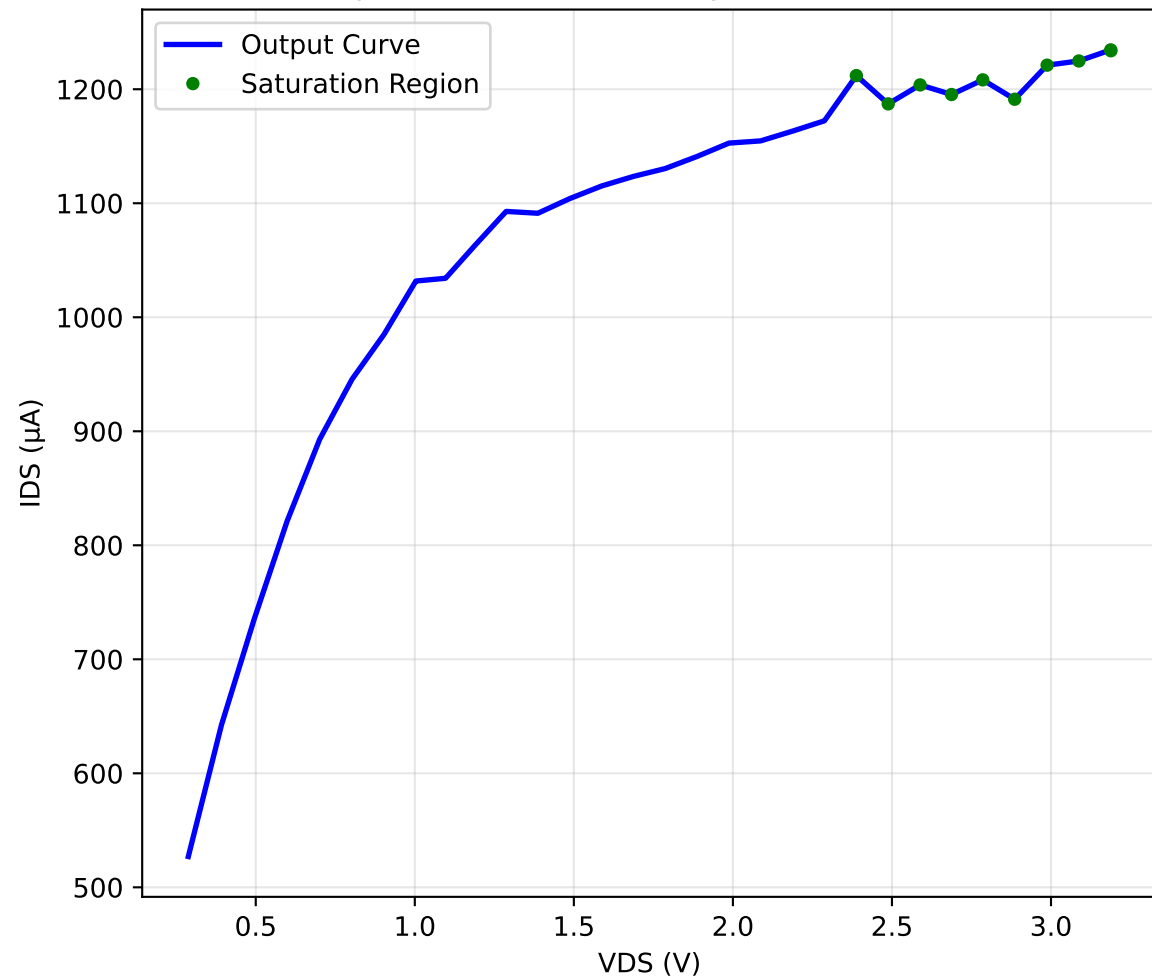


Output Conductance Analysis (Gate = 0.5V)



Output Conductance vs VDS

