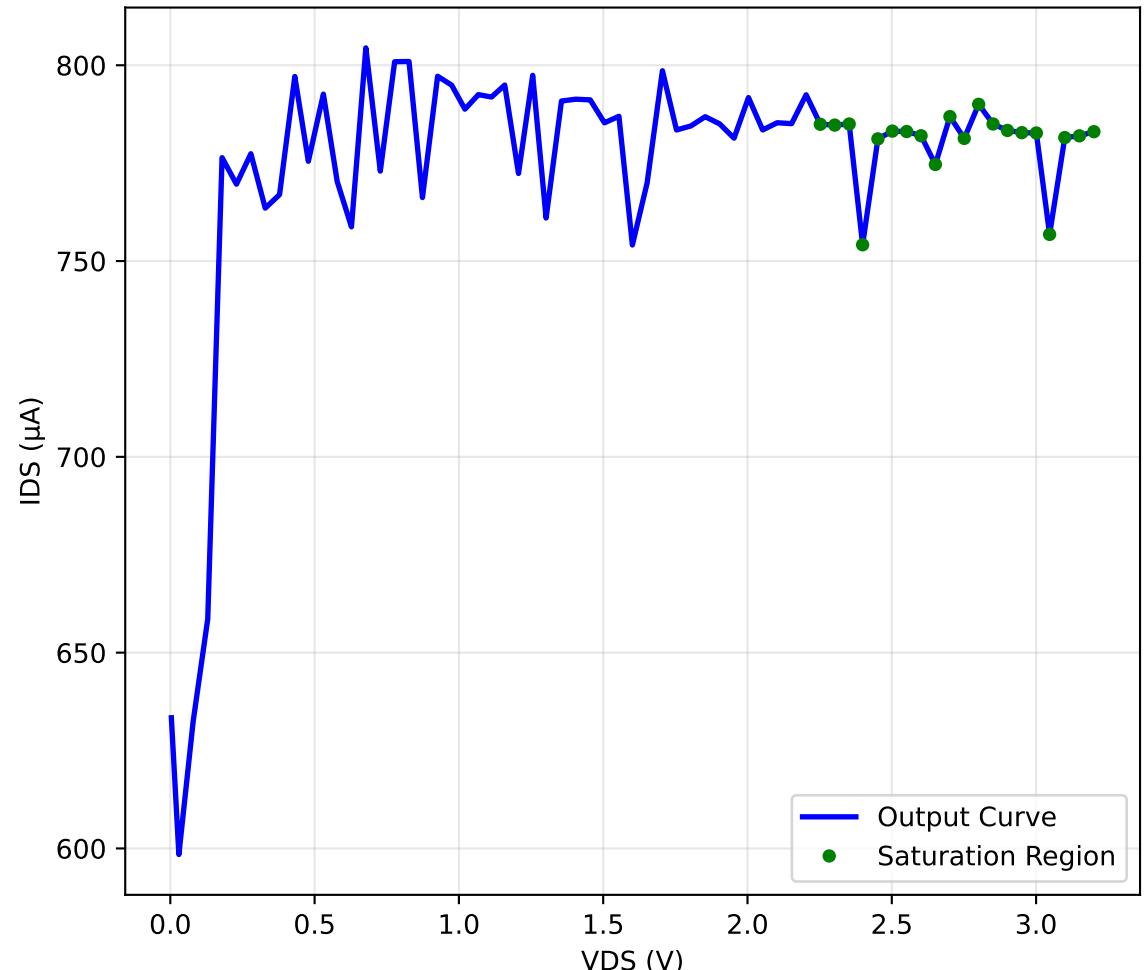


Output Conductance Analysis (Gate = 2.0V)



Output Conductance vs VDS

