



POWER

UART

CORE\_RST

RST\_BOOT\_PA2

FLASH

JTAG



open source  
hardware

OSHW  
CN000018

HFP-LINK

SPI TFT

CLK\_CFG

EN  
0  
1  
2

CORE\_SEL

CORE\_LED

STARRY SKY

Pi

RISC-V

designed by XH\_Timmo



EasyEDA

KEY-1

KEY-2

40  
39







