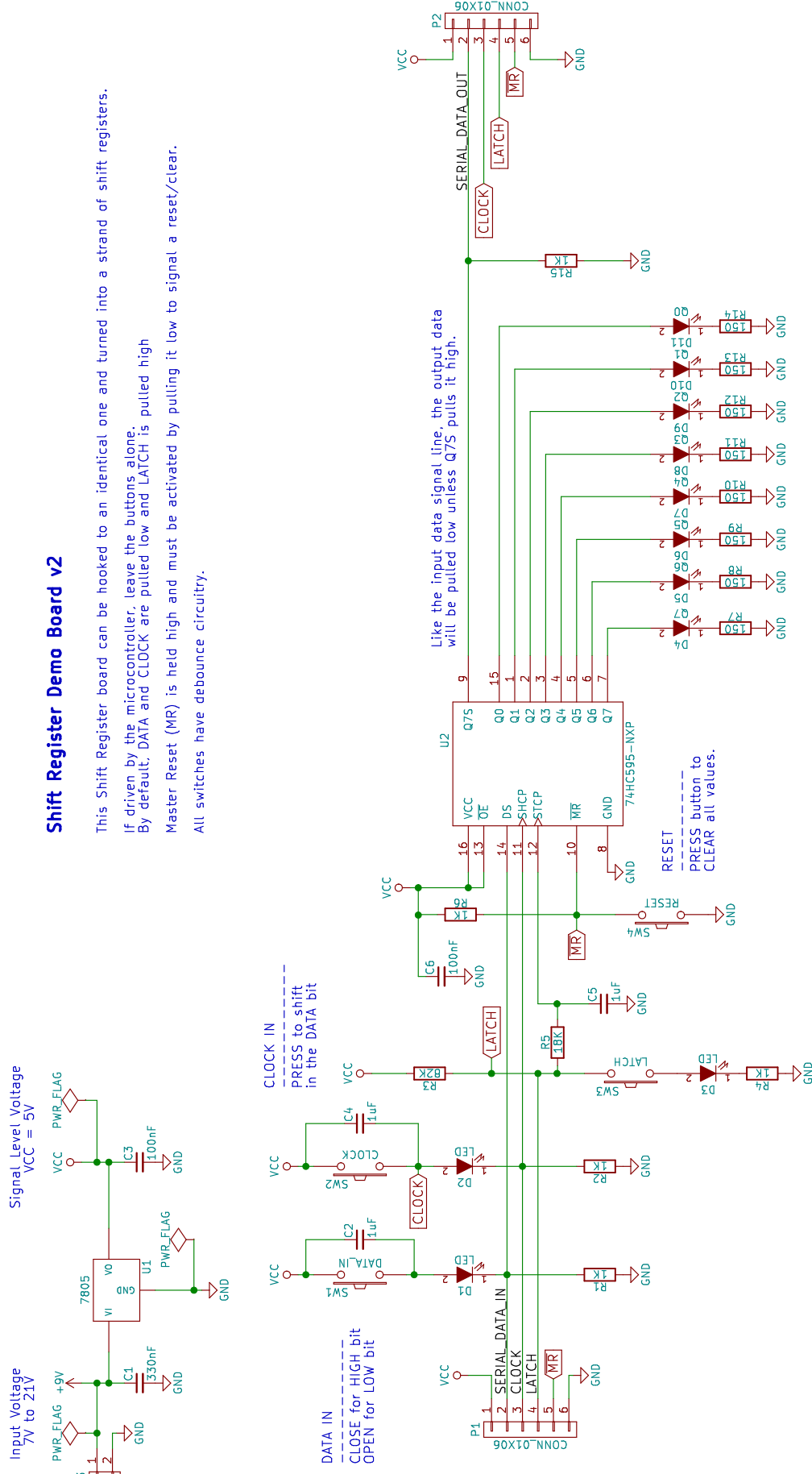


Shift Register Demo Board v2

This Shift Register board can be hooked to an identical one and turned into a strand of shift registers. If driven by the microcontroller, leave the buttons alone. By default, DATA and CLOCK are pulled low and LATCH is pulled high. Master Reset (MR) is held high and must be activated by pulling it low to signal a reset/clear. All switches have debounce circuitry.



LATCH

Press and hold the LATCH low while using DATA and CLOCK to shift in the desired bits.

Outputs will not be visible until LATCH is pulled high.

This debounce circuit is partly an experiment so v3 will have minimum necessary circuitry.

Company: OSH Park
Version 1 by Laen
Version 2 additions by Jenner
Released under the CERN Open Hardware License v1.2

Sheet: /
File: shift-register-demo-v2.sch

Title: Shift Register Demo

Size: A4	Date: 2010	Rev: v2
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