

LOW-COST STEREO VISION SYSTEM FOR AUTONOMOUS MOBILE  
ROBOTS

A Thesis

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Master of Science in Computer Science

by

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## ABSTRACT

Low-Cost Stereo Vision System for Autonomous Mobile Robots

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Something, something, robots. that

## ACKNOWLEDGMENTS

I would like to especially thank my parents and family for their love and support.

## TABLE OF CONTENTS

List of Tables	vii
List of Figures	viii
1 Introduction	1
2 Background	2
3 Related Works	3
4 Implementation	5
5 Experiments and Results	6
6 Conclusions	7
7 Future Work	8
Bibliography	9

## LIST OF TABLES

## LIST OF FIGURES



## CHAPTER 1

### Introduction

Introducing ...

## CHAPTER 2

### Background

Back to the grounding.

## CHAPTER 3

### Related Works

There are several different ways to implement a stereo vision system. Many stereo vision systems are implemented on field-programmable gate arrays (FPGAs). FPGAs allow for parallelization when processing images. Systems that use FPGAs generally can achieve a high frames per second on a decent or good image quality, but most of these systems are expensive.

FPGA Design and Implementation of a Real-Time Stereo Vision System [6] uses an Altera Stratix IV GX DE4 FPGA board to process the right and left images that come from the cameras that were attached to it. [6] uses the Sum of Absolute Differences (SAD) algorithm to compute distances. This system allows for real time speeds up to 15 frames per second at an image resolution of 1280x1024. However, the Altera Stratix IV GX DE4 FPGA board costs over \$4000, [1] which makes the system impractical for non-high budget projects.

Improved Real-time Correlation-based FPGA Stereo Vision System [5] uses a Xilinx Virtex-5 board to process images. [5] uses a correlation-based algorithm, which is based on the Census Transform, to obtain the depth in images. The algorithm is fast, but there are some inherent weaknesses to it. This system can run at 70 frames per second for images at a resolution of 512x512. Unfortunately, the Xilinx Virtex-5 board costs more than \$1000, [2] which is still quite expensive.

Low-Cost Stereo Vision on an FPGA [7] uses a Xilinx Spartan-3 XC3S2000

board. [7] uses the Census Transform algorithm for image processing. This allows images with a resolution of 320x240 to be processed at 150 frames per second. The total hardware for the low-cost prototype used in [7] costs just over \$1000, which is a bit too pricy for a lot of projects.

An Embedded Stereo Vision Module For Industrial Vehicles Automation [4] uses a Xilinx Spartan-3A-DSP FGPA board. [4] uses an Extended Kalman Filter (EKF) based visual simultaneous localization and mapping (SLAM) algorithm. The accuracy of this system directly varied with speed and distance of detected object. The Xilinx Spartan-3A-DSP FGPA board is around \$600, [3] which is fairly expensive still.

## CHAPTER 4

### Implementation

Architectural stuff

## CHAPTER 5

### Experiments and Results

#### Experiments and Results

## CHAPTER 6

### Conclusions

Concluded.

## CHAPTER 7

### Future Work

In the Future!



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