DECEMBER 1983-REVISED MARCH 1988

- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

description

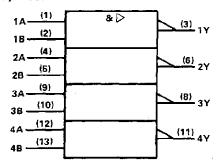
These devices contain four independent 2-input NAND buffer gates.

The SN5437, SN54LS37 and SN54S37 are characterized for operation over the full military range of $-55\,^{\circ}\text{C}$ to $125\,^{\circ}\text{C}$. The SN7437, SN74LS37 and SN74S37 are characterized for operation from $0\,^{\circ}\text{C}$ to $70\,^{\circ}\text{C}$.

FUNCTION TABLE (each gate)

INP	UTS	OUTPUT
A	₿	Y
Н	Н	L
∟	×	н
x	L	н

logic symbol†



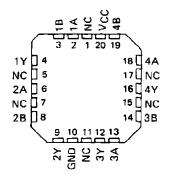
[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

SN5437, SN54LS37, SN54S37... J OR W PACKAGE SN7437... N PACKAGE SN74LS37, SN74S37... D OR N PACKAGE (TOP VIEW)

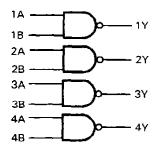
1A []1 1B []2 1Y []3 2A []4 2B []5	14 VCC 13 4B 12 4A 11 4Y 10 3B
28 5 2Y 6 GND 7	9 34 8 37

SN54LS37, SN54S37...FK PACKAGE (TOP VIEW)



NC - No internal connection

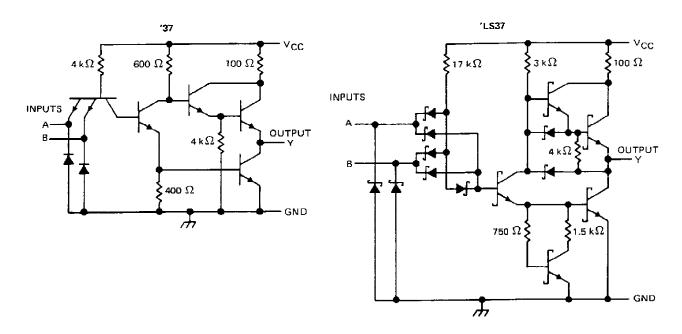
logic diagram

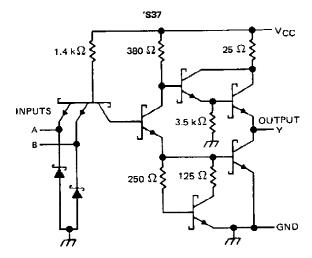


positive logic

 $Y = \overline{A \cdot B} \text{ or } Y = \overline{A} + \overline{B}$

schematics (each gate)





Resistor values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note	1)	
Input voltage: '37, 'S37		5.5 V
'LS37	,	7 V
Operating free-air temperature:	\$N54'	. –55°C to 125°C
	SN74'	0°C to 70°C
Storage temperature range		. -65° C to 150° C

NOTE 1: Voltage values are with respect to network ground terminal.



recommended operating conditions

			SN5437	,		SN7437		UNIT
ĺ		MIN	NOM	MAX	MIN	MOM	MAX	CNII
VCC	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage		-	8.0			0.8	V
ТОН	High-level output current			- 1.2			- 1.2	mΑ
loL	Low-level output current			48			48	mΑ
TA	Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER	TEST CONDITIONS †			SN5437			SN7437			
PARAMETER		TEST COMBITTOMS			TYP#	MAX	MIN	TYP \$	MAX	UNIT
VIK	V _{CC} ≈ MIN,	I _I = - 12 mA		<u> </u>		1.5			- 1.5	٧
νон	V _{CC} = MIN,	V _{IL} = 0.8 V,	I _{OH} = - 1.2 mA	2.4	3.3		2.4	3.3		V
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	IOL = 48 mA		0.2	0.4		0.2	0.4	V
11	V _{CC} = MAX,	V ₁ = 6.5 V				1			1	mA
Iн	V _{CC} = MAX,	V = 2.4 V				40		_	40	μА
ΊΙL	VCC = MAX,	V _I = 0.4 V				- 1.6			- 1.6	mA
I _{OS} §	V _{CC} = MAX			20		- 70	- 18		- 70	mA
Гссн	V _{CC} ≈ MAX,	V ₁ = 0 V			9	15.5		9	15.5	mΑ
ICCL	$V_{CC} = MAX$,	V ₁ = 4.5 V			34	54		34	54	mΑ

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, V_{CC} = 5 V, T_A = 25°C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	DITIONS	MIN	TYP	MAX	UNIT
tPLH	A or B	×	$R_1 = 133 \Omega_r$	C: - 45 p.F		13	22	กร
†PHL	Aora	,	nL - 133 12,	CL = 45 pF		8	15	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_{\Delta} = 25^{\circ}\text{C}$. § Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

SN54LS37, SN74LS37 QUADRUPLE 2-INPUT POSITIVE-NAND BUFFERS

recommended operating conditions

		S	SN54LS37 SN74LS			N74LS	37	UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2	-		$\overline{}$
VIL	Low-level input voltage			0.7			8.0	V
Гон	High-level output current			-1.2			- 1.2	mA
loL	Low-level output current			12		-	24	mA
TA	Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDIT	TIONS T	8	N54LS	37	5	N74LS	37	
FARANIETER	ANAMEIEN				TYP‡	MAX	MIN	TYP#	MAX	UNIT
v _{IK}	V _{CC} = MIN,	I _I = -18 mA				- 1.5	_		- 1.5	V
Voн	V _{CC} = MIN,	V _{IL} = MAX,	I _{OH} = - 1.2 mA	2.5	3.4		2.7	3.4		V
V	V _{CC} = MIN,	V _{IH} = 2 V,	I _{OL} = 12 mA		0.25	0.4		0.25	0.4	V
V _{OL}	VCC = MIN.	V _{JH} = 2 V,	10L = 24 mA					0.35	0.5	\
<u> 1</u>	V _{CC} = MAX,	V _I = 7 V		1 -		0.1			0.1	mA
ЧН	V _{CC} = MAX,	V ₁ = 2.7 V				20			20	μΑ
ΗL	V _{CC} = MAX.	V _I = 0.4 V		_		-0.4			- 0.4	mA
IOS \$	V _{CC} = MAX			- 30		130	- 30		- 130	mA
Іссн	V _{CC} = MAX,	V ₁ = 0 V		1	0.9	2		0.9	2	mA
ICCL	V _{CC} = MAX,	V _I = 4.5 V		_ _	6	12		6	12	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

switching characteristics, VCC = 5 V, TA = 25°C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	DITIONS	MIN TYP	MAX	UNIT
tPLH .	A or B	~	$R_1 = 667 \Omega$,	C ₁ = 45 pF	12	24	ns
^t PHL	40.0		RL = 667 Ω,	CL - 40 pr	12	24	กร

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$. § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

recommended operating conditions

		SN54S3	7	:	SN74S3	7	
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH High-level input voltage	2			2			V
VIL Low-level input voltage			8.0			8.0	V
IOH High-level output current			– 3			- 3	mA
IOL Low-level output current			60			60	mA
TA Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

	+			SN54S3	7					
PARAMETER		TEST CONDITIONS T		MIN	TYP‡	MAX	MIN	TYP ‡	MAX	UNIT
VIK	V _{CC} = MIN,	lı = — 18 mA			•	- 1.2			- 1.2	V
VoH	V _{CC} = MIN,	V _{IL} = 0.8 V,	I _{OH} = - 3 mA	2.5	3.4		2.7	3.4		V
VOL	V _{CC} = MIN,	V _{IH} = 2 V,	IQL = 60 mA			0.5			0.5	V
11	VCC = MAX.	V _I = 5.5 V				1			1	mΑ
ΊΗ	V _{CC} = MAX,	V ₁ = 2.7 V				0.1			0.1	mΑ
IIL	V _{CC} = MAX,	V _I = 0.5 V			-	- 4			- 4	mA
IOS §	V _{CC} = MAX			50		- 225	- 50		- 225	mA
ГССН		V ₁ - 0 V	·		20	36		20	36	mA
CCL	V _{CC} = MAX,	V _I = 4.5			46	80		46	80	mА

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. ‡ All typical values are at V_{CC} = 5 V, T_A = 25°C. § Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed 100 milliseconds.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM	TO	TEST CONDITIONS		MIN TYP	MAX	UNIT
	(INPUT)	(OUTPUT)					
tPLH			P 02 O	C ₁ = 50 pF	4	6.5	ns
tPHL	A or B		R _L = 93 Ω,	OF . 20 by	4	6.5	ns
[†] PLH	A OF B	· . [R _L = 93 Ω,	C ₁ = 150 pF	6		กร
t _{PHL}			a2 25'	C[- 150 bc	6		ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.

IMPORTANT NOTICE

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

Copyright © 1996, Texas Instruments Incorporated

IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1998, Texas Instruments Incorporated