INTEGRATED CIRCUITS

DATA SHEET

74ALS153Dual 4-input multiplexer

Product specification

1991 Feb 08

IC05 Data Handbook





Dual 4-input multiplexer

74ALS153

FEATURES

- Non-inverting outputs
- Common select outputs
- Separate enable for each section
- See 74ALS253 for 3-State version

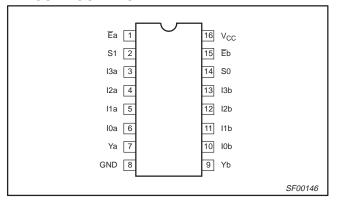
DESCRIPTION

The 74ALS153 has two identical 4–input multiplexer with 3–State outputs which selects two bits of data from four sources by using common select inputs (S0, S1). The two 4–input multiplexer circuits have individual active—Low enables (Ea, Eb) which can be used to strobe the outputs independently. Outputs (Ya, Yb) are forced Low when the corresponding enable is high.

The 74ALS153 is the logic implementation of a 2–pole, 4–position switch where the position of the switch is determined by the logic levels supplied to the common select inputs.

TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
74ALS153	7.0ns	6.5mA

PIN CONFIGURATION



ORDERING INFORMATION

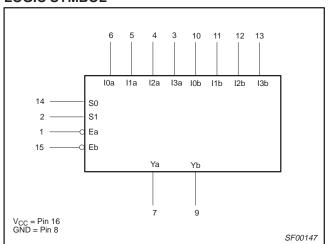
	ORDER CODE		
DESCRIPTION	COMMERCIAL RANGE V_{CC} = 5V $\pm 10\%$, T_{amb} = 0°C to ± 70 °C	DRAWING NUMBER	
16-pin plastic DIP	74ALS153N	SOT38-4	
16-pin plastic SO	74ALS153D	SOT109-1	
16-pin plastic SSOP Type II	74ALS153DB	SOT338-1	

INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

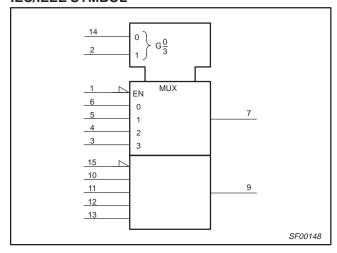
PINS	DESCRIPTION	74ALS (U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
loa – I3a	Port A data inputs	1.0/1.0	20μA/0.1mA
lob – I3b	Port B data inputs	1.0/1.0	20μA/0.1mA
S0, S1	Common select inputs	1.0/1.0	20μA/0.1mA
Ea	Port A enable input	1.0/1.0	20μA/0.1mA
Eb	Port B enable input	1.0/1.0	20μA/0.1mA
Ya, Yb	Data outputs	130/240	2.6mA/24mA

NOTE: One (1.0) ALS unit load is defined as: $20\mu A$ in the High state and 0.1mA in the Low state.

LOGIC SYMBOL



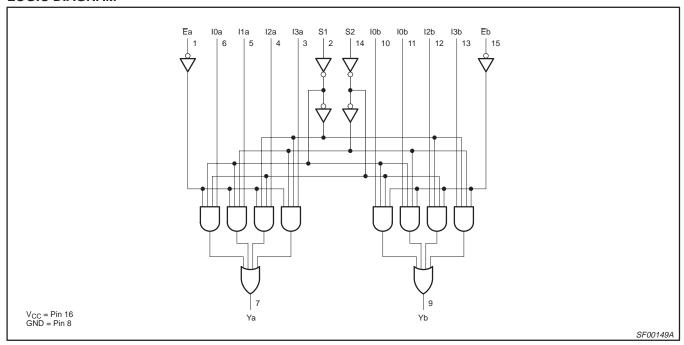
IEC/IEEE SYMBOL



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LOGIC DIAGRAM



FUNCTION TABLE

			INPUTS				OUTPUT
S0	S1	l0n	l1n	l2n	l3n	En	Yn
L	L	L	Х	Х	Х	L	L
L	L	Н	X	X	X	L	Н
Н	L	×	L	X	×	L	L
Н	L	×	Н	X	×	L	Н
L	Н	X	X	L	×	L	L
L	н	X	X	Н	×	L	Н
Н	н	X	X	X	L	L	L
н	Н	X	Х	Х	Н	L	Н

H = High voltage level L = Low voltage level X = Don't care

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ABSOLUTE MAXIMUM RATINGS

(Operation beyond the limit set forth in this table may impair the useful life of the device. Unless otherwise noted these limits are over the operating free-air temperature range.)

SYMBOL	PARAMETER	RATING	UNIT
V _{CC}	Supply voltage	-0.5 to +7.0	V
V _{IN}	Input voltage	-0.5 to +7.0	V
I _{IN}	Input current	−30 to +5	mA
V _{OUT}	Voltage applied to output in high output state	–0.5 to V _{CC}	V
lout	Current applied to output in Low output state	48	mA
T _{amb}	Operating free-air temperature range	0 to +70	°C
T _{stg}	Storage temperature range	−65 to +150	°C

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER			UNIT	
STWIBOL	FARAWETER	MIN	NOM	MAX	UNIT
V _{CC}	Supply voltage	4.5	5.0	5.5	V
V _{IH}	High-level input voltage	2.0			V
V _{IL}	Low-level input voltage			0.8	V
I _{Ik}	Input clamp current			-18	mA
I _{OH}	High-level output current			-2.6	mA
I _{OL}	Low-level output current			24	mA
T _{amb}	Operating free-air temperature range	0	·	+70	°C

DC ELECTRICAL CHARACTERISTICS

(Over recommended operating free-air temperature range unless otherwise noted.)

SYMBOL	PARAMETER	TEST CONDITION	ONE1		LIMITS		UNIT
STWIBUL	PARAMETER	TEST CONDITION	ONS.	MIN	TYP ²	MAX	UNIT
V	Lligh level output voltege	$V_{CC} = \pm 10\%, V_{IL} = MAX,$	$I_{OH} = -0.4$ mA	V _{CC} – 2			V
V _{OH}	High-level output voltage	V _{IH} = MIN	I _{OH} = MAX	2.4	3.2		V
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Low lovel output voltogo	V _{CC} = MIN, V _{IL} = MAX,	I _{OL} = 12mA		0.25	0.40	V
V _{OL}	Low-level output voltage	V _{IH} = MIN	I _{OL} = 24mA		0.35	0.50	V
V _{IK}	Input clamp voltage	$V_{CC} = MIN, I_I = I_{IK}$			-0.73	-1.5	V
II	Input current at minimum input voltage	V _{CC} = MAX, V _I = 7.0V				0.1	mA
I _{IH}	High-level input current	$V_{CC} = MAX, V_I = 2.7V$				20	μΑ
I _{IL}	Low-level input current	$V_{CC} = MAX, V_I = 0.4V$				-0.1	mA
I _O	Output current ³	$V_{CC} = MAX, V_O = 2.25V$		-30		-112	mA
I _{CC}	Supply current (total)	V _{CC} = MAX			6.5	12	mA

NOTES:

- 1. For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
- All typical values are at V_{CC} = 5V, T_{amb} = 25°C.
 The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.

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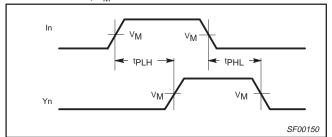
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AC ELECTRICAL CHARACTERISTICS

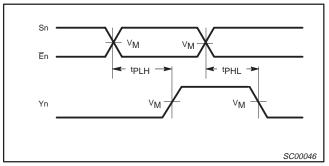
			LIM	UNIT	
SYMBOL	PARAMETER	TEST CONDITION	T _{amb} = 0°C V _{CC} = +5. C _L = 50pF,		
			MIN	MAX	
t _{PLH} t _{PHL}	Propagation delay In to Yn	Waveform 1	4.0 4.0	12.0 12.0	ns
t _{PLH} t _{PHL}	Propagation delay Sn to Yn	Waveform 2	5.0 7.0	15.0 16.0	ns
t _{PLH}	Propagation delay En to Yn	Waveform 2	3.0 5.0	10.0 12.0	ns

AC WAVEFORMS

For all waveforms, $V_M = 1.3V$.

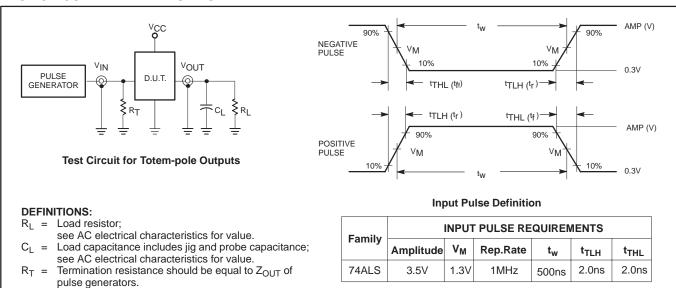


Waveform 1. Propagation Delay for Data to Output



Waveform 2. Propagation Delay for Select or Enable to Output

TEST CIRCUIT AND WAVEFORMS



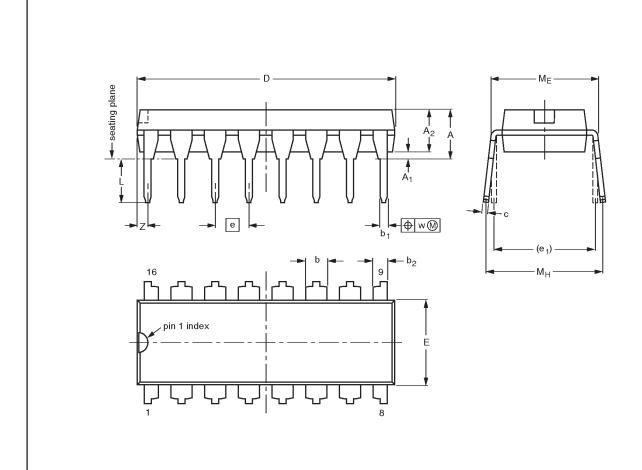
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DIP16: plastic dual in-line package; 16 leads (300 mil)

SOT38-4



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	b ₂	C	D ⁽¹⁾	E ⁽¹⁾	е	e ₁	L	ME	Мн	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.30	0.53 0.38	1.25 0.85	0.36 0.23	19.50 18.55	6.48 6.20	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	0.76
inches	0.17	0.020	0.13	0.068 0.051	0.021 0.015	0.049 0.033	0.014 0.009	0.77 0.73	0.26 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.030

scale

10 mm

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE	
SOT38-4						-92-11-17 95-01-14	

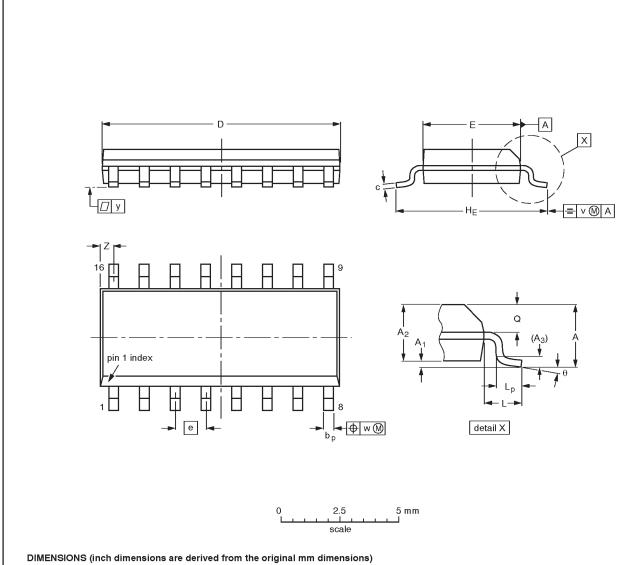
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SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1



							_											
UNIT	. A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽¹⁾	e	HE	L	Lp	Ø	v	w	у	Z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	10.0 9.8	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inche	s 0.069	0.0098 0.0039		0.01		0.0098 0.0075	0.39 0.38	0.16 0.15	0.050	0.24 0.23	0.041	0.039 0.016		0.01	0.01	0.004	0.028 0.012	0°

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT109-1	076E07S	MS-012AC				91-08-13 95-01-23

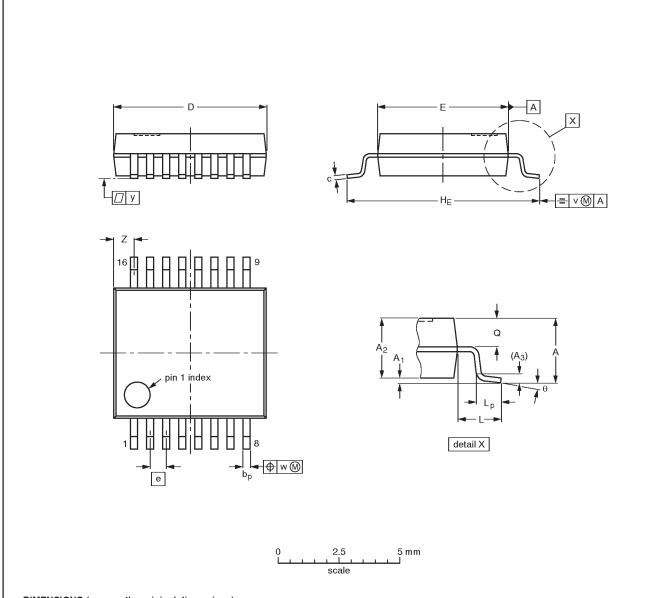
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SSOP16: plastic shrink small outline package; 16 leads; body width 5.3 mm

SOT338-1



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	рb	С	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	Ø	v	w	у	Z ⁽¹⁾	θ
mm	2.0	0.21 0.05	1.80 1.65	0.25	0.38 0.25	0.20 0.09	6.4 6.0	5.4 5.2	0.65	7.9 7.6	1.25	1.03 0.63	0.9 0.7	0.2	0.13	0.1	1.00 0.55	8° 0°

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE	
SOT338-1		MO-150AC				94-01-14 95-02-04	

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DEFINITIONS						
Data Sheet Identification	Product Status	Definition				
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