

CA MCQ TEST 1

Due Nov 16 at 12:59pm **Points** 50 **Questions** 50
Available after Nov 16 at 10:20am **Time Limit** 90 Minutes

Instructions

CA - MCQ TEST 1 - Choose the Best Answer from the following Options.

Each Question will carry 1 Mark.

Attempt History

	Attempt	Time	Score
LATEST	Attempt 1	49 minutes	45 out of 50

Score for this quiz: **45** out of 50

Submitted Nov 16 at 11:15am

This attempt took 49 minutes.

Correct!

Question 1

1 / 1 pts

1. The _____ format is usually used to store data .

- ☒ BCD
- ☐ Octal
- ☐ Hecadecimal
- ☐ Decimal

Question 2**1 / 1 pts**

2. The ALU makes use of _____ to store the intermediate results

Correct!☒ Accumulators☐ Heap☐ Stack☐ Registers**Question 3****1 / 1 pts**

3. The I/O interface required to connect the I/O device to the bus consists of _____

Correct!☐ Address decoder and registers☒ Both a and b☐ Control circuits☐ Only b**Question 4****1 / 1 pts**

4. _____ is generally used to increase the apparent size of physical memory .

Correct!

- ☐ Hard-disk
- ☒ Virtual memory
- ☐ Secondary memory
- ☐ Disks

Question 5**1 / 1 pts**

5. Which of the register/s of the processor is/are connected to Memory Bus ?

Correct!

- ☒ MAR
- ☐ PC
- ☐ Both a and b
- ☐ IR

Question 6**1 / 1 pts**

6. _____ is used to store data in registers .

- ☐ none of these
- ☐ JK flip flop

Correct!☐ RS flip flop☒ D flip flop**Question 7****1 / 1 pts**

7. The clock rate of the processor can be improved by

Correct!☒ All of the above☐ Reducing the amount of processing done in one step☐ Improving the IC technology of the logic circuits☐ By using overclocking method**Question 8****1 / 1 pts**

8. When Performing a looping operation, the instruction gets stored in the _____ .

Correct!☒ Cache☐ System Heap☐ System stack☐ Registers

Question 9**1 / 1 pts**

9. The average number of steps taken to execute the set of instructions can be made to be less than one by following _____ .

☐ Pipe-lining☐ ISA☐ Sequential☒ Super-scaling**Correct!****Question 10****1 / 1 pts**

10. In Breakpoint mode of operation,

☐ The program is interrupted after each detection☐ The program will not be stopped and the errors are sorted out after the complete program is scanned☒ The program is altered only at specific points☐ There is no effect on the program, i.e the program is executed without rectification of errors**Correct!**

Question 11**1 / 1 pts**

11. How is a privilege exception dealt ?

☐ The Program is stopped and removed from the queue

☐

The system switches the mode and starts the execution of a new process

☐ The system switches mode and runs the debugger

☒

The program is altered and the system switches into supervisor mode and restarts the program execution

Correct!**Question 12****1 / 1 pts**

12. Synchronous means _____

☐ At variable time

☒ At same time

☐ At irregular intervals

☐ None of these

Correct!**Question 13****1 / 1 pts**

13. When CPU is executing a Program that is part of the Operating System, it is said to be in

- ☐ Interrupt mode
- ☐ Half mode
- ☐ Simplex mode
- ☒ System mode

Correct!

Question 14

1 / 1 pts

14. Erasing EPROM programming is accomplished by using ultraviolet light that belongs to the UVC range and has a frequency of

_____.

- ☒ 253.7
- ☐ 254.9
- ☐ 253.2
- ☐ None of the above

Correct!

Question 15

0 / 1 pts

15. Cache memory is intended to provide memory access

Correct Answer

You Answered

☐ Slow☐ Very Slow☐ Fastest☒ Fast**Question 16****1 / 1 pts**

16. Interrupts are provided primarily as a way to

☐ Improve processor control☒ Improve processor utilization☐ Improve processor execution☐ Improve processor speed

Correct!

Question 17**1 / 1 pts**

17. Processor is often referred to

☐ Hardware☐ I/O Modules☐ System Bus

Correct!

- ☒ Central Processing Unit

Question 18**1 / 1 pts**

18. Data moved between computer components through

☐ I/O Processor

☐ I/O Devices

☐ I/O Buffers

Correct!

☒ I/O Modules

Question 19**1 / 1 pts**

19. User-visible registers enable the machine or programmer to

☒ Minimize Main Memory usage

☐ Minimize Control

☐ Minimize Compiler

☐ Minimize Register

Correct!**Question 20****1 / 1 pts**

Correct!

20. I/O modules performs the requested action on

- ☐ I/O devices
- ☒ Programmed I/O
- ☐ Direct Memory Access (DMA)
- ☐ Interrupt driven I/O

Question 21**1 / 1 pts**

21. Direct memory access is more efficient to move

- ☐ Less Data
- ☒ Large Data
- ☐ Low Data
- ☐ Largest Data

Correct!**Question 22****0 / 1 pts**

22. The processor fetches an instruction from

- ☐ Keyboard

You Answered

☐ Monitor☒ CPU

Correct Answer

☐ Memory**Question 23****1 / 1 pts**

23. Performance $X = 1 / \text{Execution Time}$ x the given relation shows that

☐ None☒ Performance is increased when execution time is decreased☐ Performance is increased when execution time is increased☐ Performance is decreased when execution time is decreased**Correct!****Question 24****1 / 1 pts**

24. The valid and unimpeachable measurement of performance of any computer is

☒ Execution time☐ Clock rate☐ Instruction set**Correct!**

☐ Delay time

Question 25**1 / 1 pts**

25. The total amount of work done during execution, in a given time is referred to as

☐ Response time

☒ Through put

☐ Execution time

☐ Delay time

Correct!**Question 26****1 / 1 pts**

26. Computer A having clock cycle time of 250 ps and cycle per instruction of 2.0 for some programs, and computer B having clock cycle time of 500 ps and a cycle per instruction of 1.2 for the same program. Which one is faster for this program

☐ Computer B

☐ Both will have same time

☒ Computer A

☐ None of the above

Correct!

Question 27**1 / 1 pts**

27. When the PC having Clock rate of 2 and the CPU clock cycle for a program is 4 then Execution time of this computer for a program will be

☐ 1.75☐ 1☒ 2☐ 1.5**Correct!****Question 28****1 / 1 pts**

28. The time between the start and the end of program execution is known as

☐ both a and b☒ Execution time☐ Response time☐ Delay time**Correct!****Question 29****1 / 1 pts**

29. A first goal of compiler writer

Correct!

☒ Correctness

☐ Callee saving

☐ Data dependence

☐ Fast performance

Question 30

1 / 1 pts

30. In 32-bit addressing mode, the address field is either 1 byte or

☐ 3 bytes

☐ 2 bytes

☐ 5 bytes

Correct!

☒ 4 bytes

Question 31

1 / 1 pts

31. One of the complex jobs of the compiler writer, is to figure-out what instruction sequence will be used best for each segment, named as

☐ Jumps

Correct!

- ☐ Local optimizations
- ☒ Simplify trade-offs among alternatives
- ☐ Stack height reduction

Question 32**1 / 1 pts**

32. One that is used to allocate local variables is

- ☐ Queue
- ☐ Registers
- ☐ Banks
- ☒ Stack

Correct!**Question 33****1 / 1 pts**

33. Vector architectures are operated on vectors of

- ☐ Registers
- ☐ Memory
- ☒ Data
- ☐ Graph coloring

Correct!

Question 34**1 / 1 pts**

34. Compilers usually chooses which procedure calls has to be expanded inline before knowing the size of the procedure, that is being called, the stated problem is known as

Correct!

- ☒ Phase-ordering problem
- ☐ All above
- ☐ Callee saving
- ☐ Caller saving

Question 35**1 / 1 pts**

35. Specified telling that what addressing mode will be used for accessing the operand, is called

Correct!

- ☐ Packed decimal
- ☐ Binary-coded decimal
- ☒ Address specified
- ☐ Unpacking

Question 36**0 / 1 pts**

36. The developed program's performance, is affected by

☐ Operating system

☐ Compiler

☒ Algorithm

☐ All above

ou Answered

orrect Answer

Question 37

1 / 1 pts

37. The modern supercomputers and the minicomputers, accessed by the network are called

☐ Micro Computer

☒ Servers

☐ Laptops

☐ Desktop Computers

Correct!

Question 38

1 / 1 pts

38. Every letter of the computer is referred to as

☒ Bit

Correct!

☐ Alphabet☐ Nibble☐ Number**Question 39****0 / 1 pts**

39. The High level language, referred as human language, is converted into the computer instructions via

Correct Answer**You Answered**☐ Interpreter☒ Compiler☐ Hardware system☐ Software system**Unanswered****Question 40****0 / 1 pts**

40. Microprocessors, that are present in home appliances are considered as the best example of

Correct Answer☐ Embedded Computers☐ Mini Computers☐ Servers

☐ Desktop Computers

Question 41**1 / 1 pts**

41. A+B in assembly language will be written as

Correct!

☒ add A,B

☐ addition AB

☐ add AB

☐ addition A,B

Question 42**1 / 1 pts**

42. CPU provides enabling signals through

Correct!

☒ control bus

☐ data bus

☐ address bus

☐ ordinary bus

Question 43**1 / 1 pts**

43. In a computer, set of electrical paths which is used to transfer data is called

☐ computer clock

☐ monitors

☒ bus

☐ ports

Correct!

Question 44

1 / 1 pts

44. Computer address bus is

☐ multidirectional

☒ unidirectional

☐ circular

☐ bidirectional

Correct!

Question 45

1 / 1 pts

45. Computer bus with 64 lines can carry

☐ 8bits

Correct!☒ 64bits☐ 16bits☐ 32bits**Question 46****1 / 1 pts**

46. Use of computer buses to connect different components of computer is known as

☐ layout of computer components☒ bus interconnection☐ bus topology☐ computer structure**Correct!****Question 47****1 / 1 pts**

47. Network topology in which you connect each node to the network along a single piece of network cable is called

☐ star topology☐ mesh topology☒ bus topology**Correct!**

☐ ring topology

Question 48**1 / 1 pts**

48. Computer bus which moves data between the central processor and memory is called

☐ CPU bus

☒ data bus

☐ I/O bus

☐ processor bus

Correct!**Question 49****1 / 1 pts**

49. Type of computer bus which connects the CPU to a memory on the system board is

☐ width bus

☐ word bus

☐ expansion bus

☒ system bus

Correct!

Question 50**1 / 1 pts**

50. Example of computer logic operation is

☐ OR

☐ NOR

☒ all of these

☐ AND

Correct!**Quiz Score: 45 out of 50**