

Orchid Technologies

CC1B Control board PCB Assembly

Build Instructions

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This document is presented to assist you in appraising the potential value of the CC1B CONTROL BOARD Build Instructions based upon the information which you have provided to us concerning your needs. Naturally, your requirements may change as business conditions and your operations change. Only you can determine the feasibility of the proposed CC1B CONTROL BOARD Build Instructions to answer your specific business needs. Consequently, while we believe that our CC1B CONTROL BOARD Build Instructions are sound, it cannot constitute express or implied warranties of merchantability and fitness for a particular purpose.

Revision History:

Revision	Date	Reason
1.0	August 6, 2018	Original

This document contains the assembly-time instructions for the proper build of CC1B CONTROL BOARD Revision A Printed Circuit Boards. These Instructions together with the following documents comprise the CC1B CONTROL BOARD Revision A Circuit Board Fabrication and Assembly data base.

Related Documentation:

1. SC071018 Revision A July 29, 2018 Schematic Data Base
2. CC1B_072918.xls Bill of Materials – Base Bill
3. CC1B CONTROL BOARD PCB, Revision A Data Base Assembly and Fabrication Artwork as released by Orchid Technologies using SC071018 Revision A schematic and the CC1B_072918.xls BOM data base.

The documents listed above are considered finished documents and will not be modified by Orchid Technologies Engineering and Consulting, Inc. until Phase 2 of the project. All modifications to the documents listed above will take the form of notes and appear in this document as revised.

Documentation Errata, Changes, Acceptable Procedures:

All component stuffing and board options reference CC1B_072918.xls. Please use CC1B_072918.xls, as the basic guideline for board stuffing options.

- 0. DO NOT POWER BOARDS**
1. Do Not Populate components with values “DNP”.
2. Do Not Populate components with values prefixed “DNP-“.
- 3. Completed assemblies shall be free of any and all flux residues.**

Known BOM Discrepancies and AVL issues:

BOM Updates:

Item No	Description
	None

AVL Issues:

Item No	Description
	None

Programmable Logic Instructions:

This describes the programmable device data files and their uses.

NONE

