25 AMP ASIC POWER SUPPLY

DATE 03-28-2018 REV 0- PRELIMINARY
REV 1- RES VALUE CHANGE SPI MODE PULL DOWN, 100 OHMS, SHTS 4-18
REV 1- ASIC DECOUPLE CAP PLACEMENT 'DNI' MODIFIED, BOTTOM LAYER CAPS

DATE 05-23-2018 REV 2- ADDED DECOUPLE CAP C417 AT U34 PIN 10 TO GND
REV 2- U33 PIN NUMBERING MATCHED TO DATA SHEET AND PCB DECAL

DATE 06-15-2018 REV 2- SCH-TO PCB VERIFICATION AND SYNC, REV1 TEST ERRATA

DATE 06-19-2018 REV 2- SHT18- REPLACE D76 & D77 WITH (1) BAV99-7-F
REV 2- SHT2- MOVE CAP SYMBOLS C377, C378, C379 CLOSE TO U33
REV 2- SHT1 - ADD R512 0.0 OHM RESISTOR TO FEEDBACK LOOP-POT



































