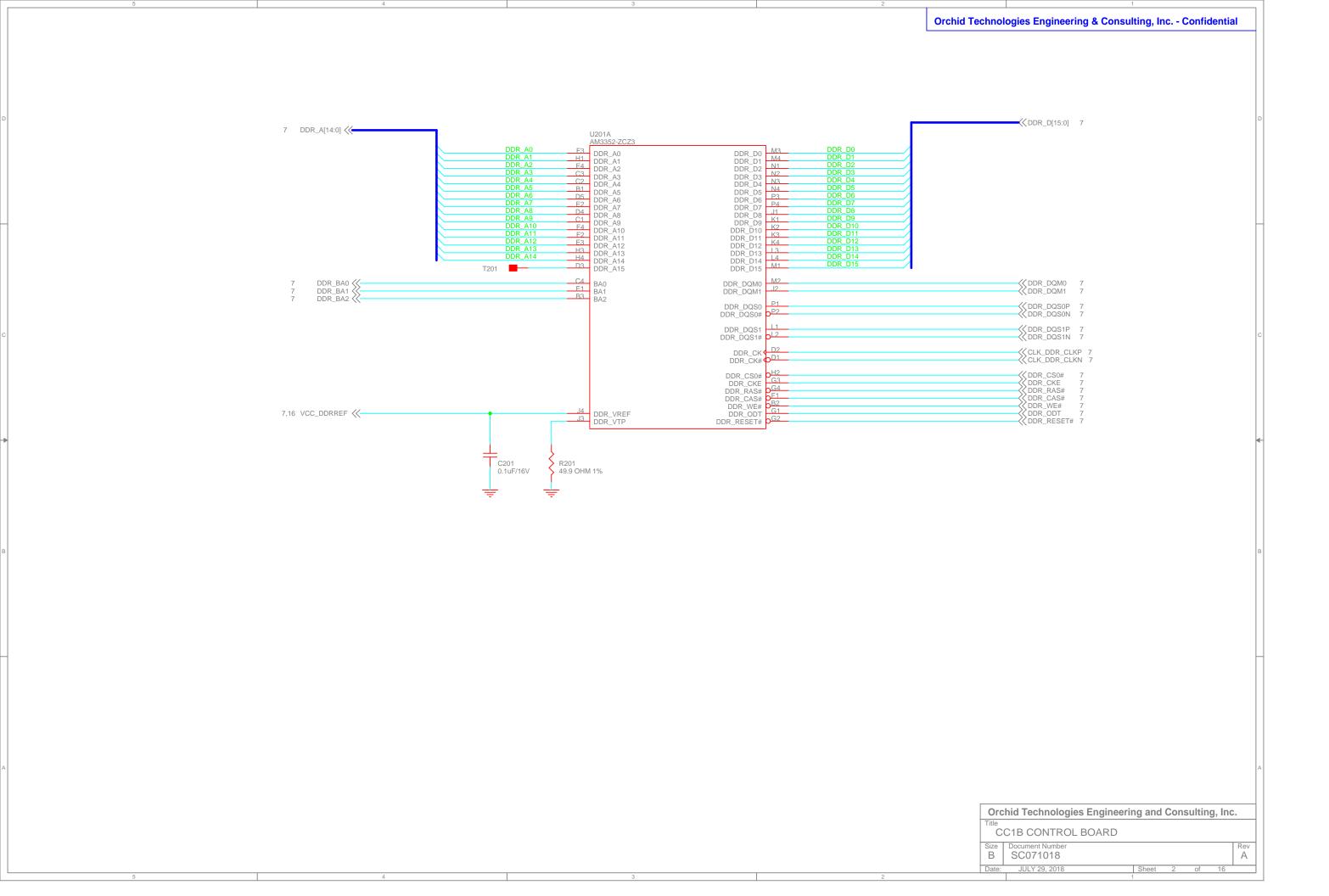
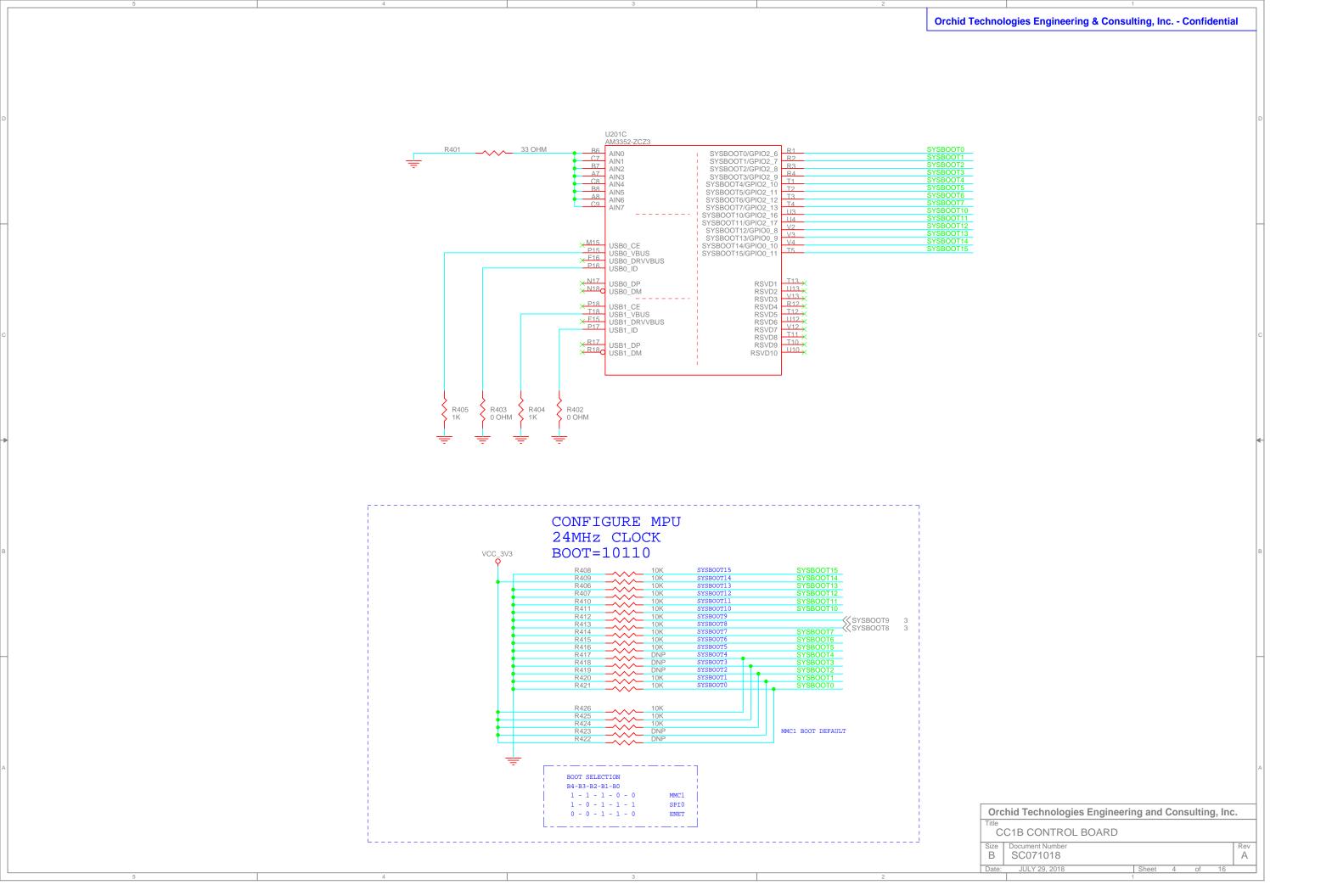
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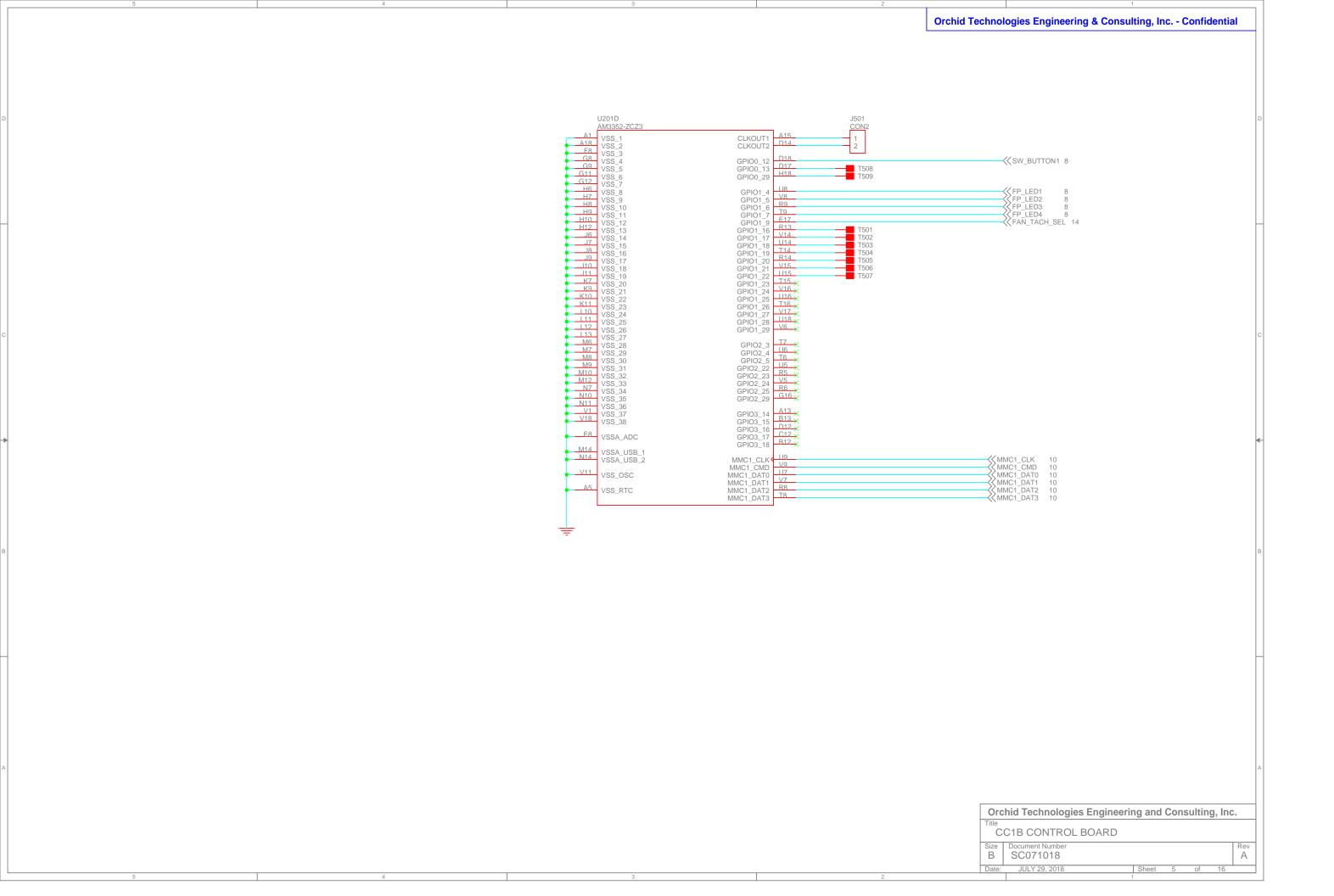
147 MAIN STREET MAYNARD, MA. 01754

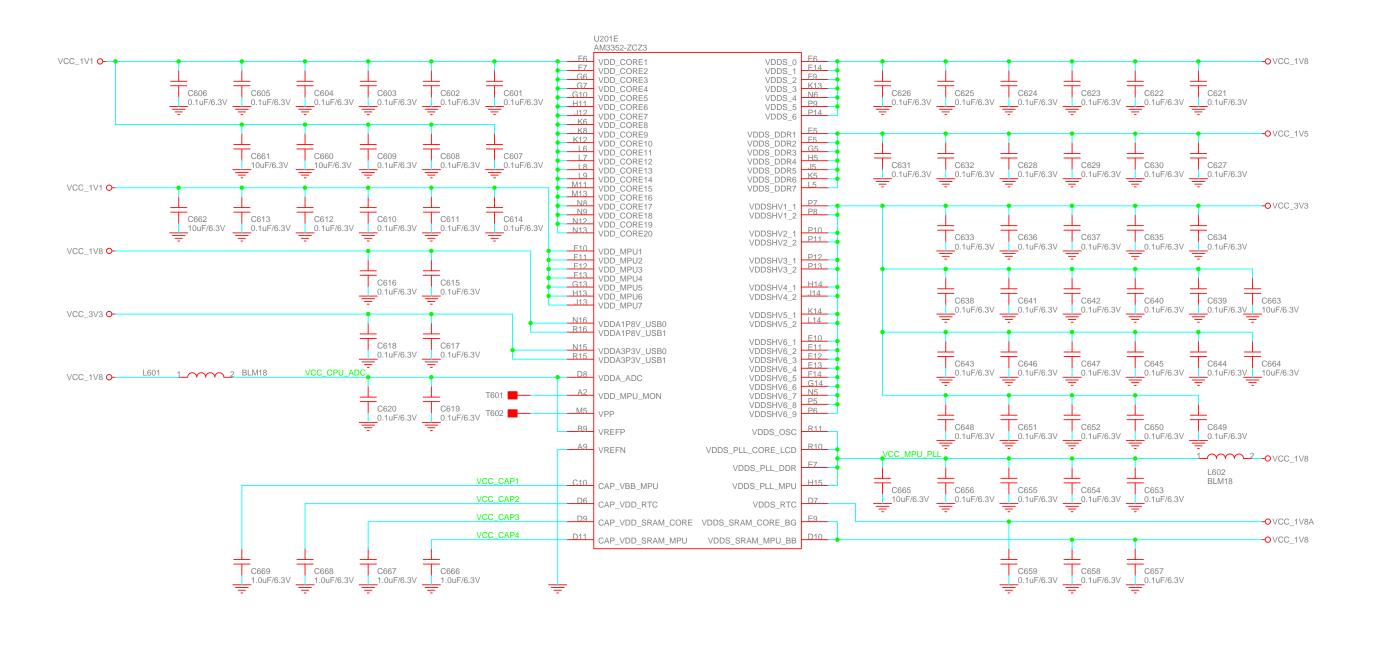
CC1B CONTROL BOARD REVISION: A



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12 UART1\_RXD \\\ UART1 TXD UART4\_TXD D16 UART1\_RXD UART4 RXD G18 UART2\_TXD 12 UART2\_TXD \\\
12 UART2\_RXD UART5\_TXD 12 UART5\_RXD 12 SYSBOOT8/UART5 TXD G17 UART2\_IAD SYSBOOT9/UART5\_RXD R7 TIMER4 R320 33 OHM 11 FAN\_F\_PWM CPU\_MII\_TXCLK 9 MII1\_TXCLK F17 TIMER5 CPU\_MII\_TXD0 9
CPU\_MII\_TXD1 9
CPU\_MII\_TXD2 9 MII1\_TXD0 E18 TIMER7 K16 14 FAN\_R\_TACH << 33 OHM 33 OHM 33 OHM MII1\_TXD1 MII1\_TXD2 K15 A17 A16 C15 B17 B16 SPI0\_CS0# SPI0\_CS1# SPI0\_D0 SPI0\_D1 10 SPIO\_SCLK SPIO SSO# R324 J18 MII1\_TXD3 CPU\_MII\_TXD3 9 T301 MII1\_RXCLK CPU\_MII\_RXCLK 9 SPI0\_MISO << CPU\_MII\_RXD0 9
CPU\_MII\_RXD1 9
CPU\_MII\_RXD2 9 SPI0\_MOSI MII1\_RXD0 MII1\_RXD1 MII1\_RXD2 C16 | 12C0\_SCL | 12C0\_SDA 13 13 CPU\_MII\_RXD3 9 MII1\_RXD3 0 OHM 0 OHM 0 OHM B150 PWRONRST#
A100 RESET\_OUT#
NMI# CPU\_MII\_TXEN 9
CPU\_MII\_COL 9
CPU\_MII\_CRS 9
CPU\_MII\_RXER 9 9,10,14 CPU\_PWRONRST# MII1\_TXEN H16 MII1\_COL MII1\_CRS 14 CPU\_NMI# 🔆 R309 0 OHM 1.8V R310 DNP 1.8V R311 0 OHM 1.8V R312 0 OHM 1.8V B50 C6 PMIC\_PWRONRST# PMIC\_POWER\_EN EXT\_WAKEUP RTC\_KALDO\_EN# MII1 RXER 14 RTC\_PWRONRST# <<-MII1\_RXDV CPU\_MII\_RXDV 9 M18 CPU\_MDC CPU\_MDIO MDIO\_CLK MDIO\_DATA A6 RTC\_XTALIN RTC\_XTALOUT 14 CLK\_CPU32K <<-R304 CPU\_JTAG\_TCK 13 CPU\_JTAG\_TMS 13 CPU\_JTAG\_TDI 13 CPU\_JTAG\_TDO 13 TCK TMS TDI V10 XTALIN XTALOUT \_\_\_\_\_\_\_\_33 OHM R305 CPU\_JTAG\_TRST# 13 TRST# ×C13 EMU2 CPU\_JTAG\_EMU0 13 CPU\_JTAG\_EMU1 13 EMU0 × D13 × A14 EMU3 EMU4 R301 0 OHM EMU1 TESTOUT R303 DNP Y301 ABM8G-24.000 C301 18pF/50V C302 18pF/50V Orchid Technologies Engineering and Consulting, Inc. CC1B CONTROL BOARD Size Document Number B SC071018 Rev Date: JULY 29, 2018







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