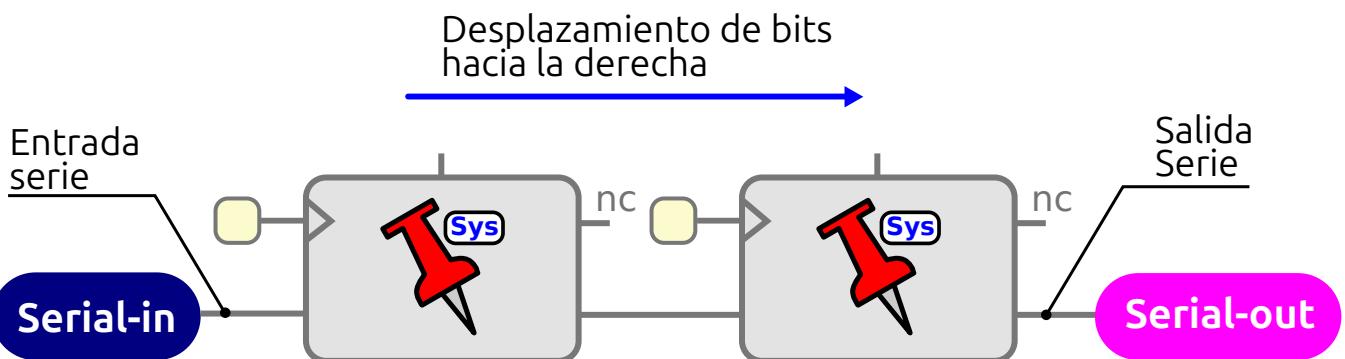
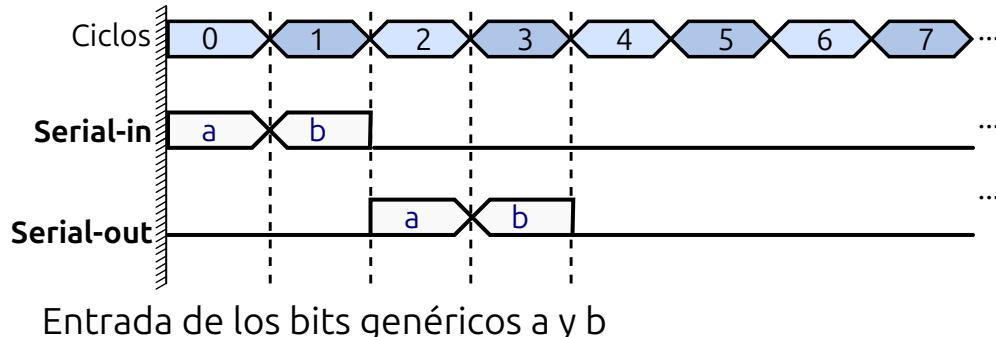


# Círculo en Icestudio



Fragmento de circuito

## Cronograma



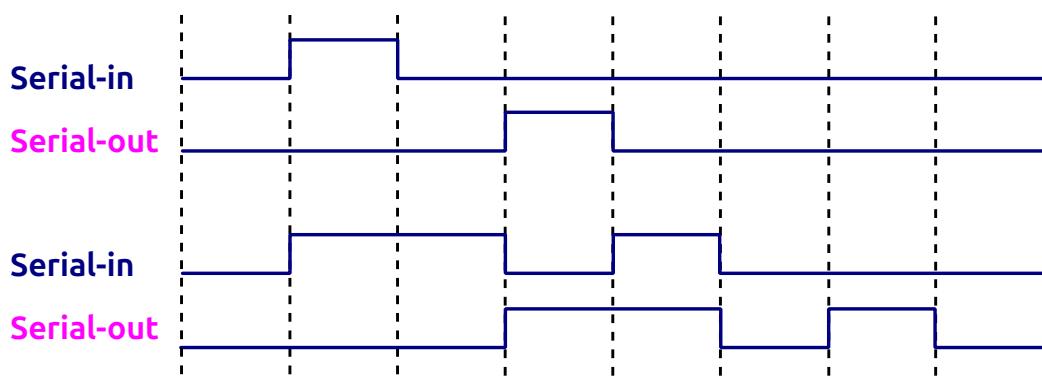
2 bits de almacenamiento

## Estado

Ciclo	Estado
0	0 0
1	a 0
2	b a
3	0 b
≥4	0 0

## Resultado

### Señales retrasadas 2 ciclos



038 shift-reg2 Registro de desplazamiento de 2 bits

TUTORIAL (Obijuan)