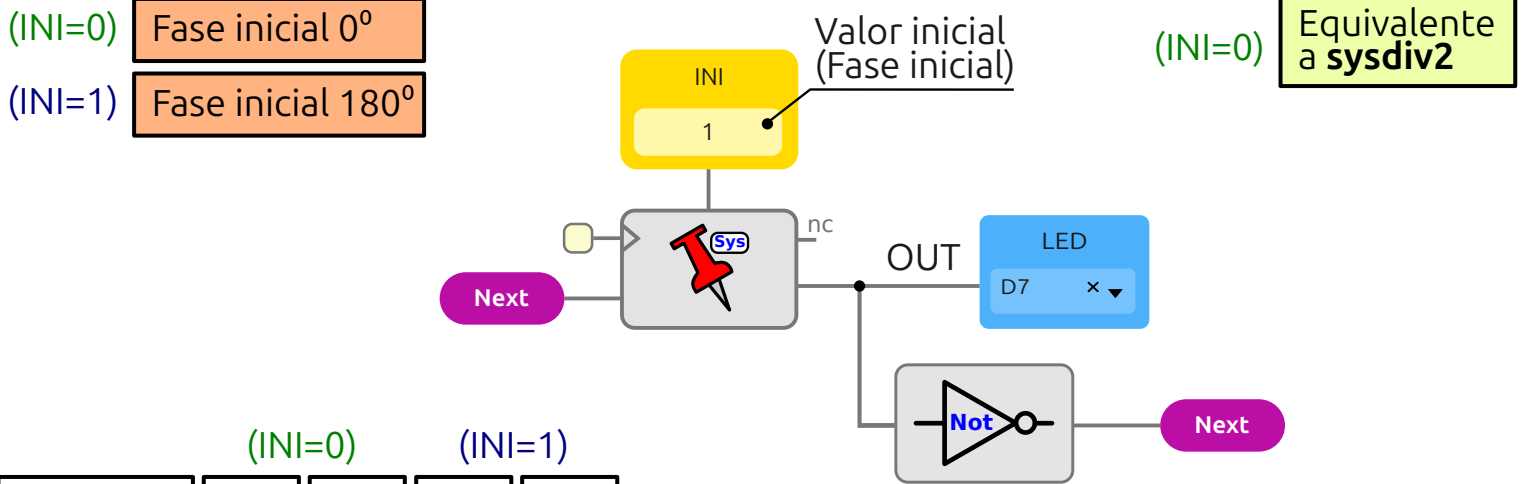
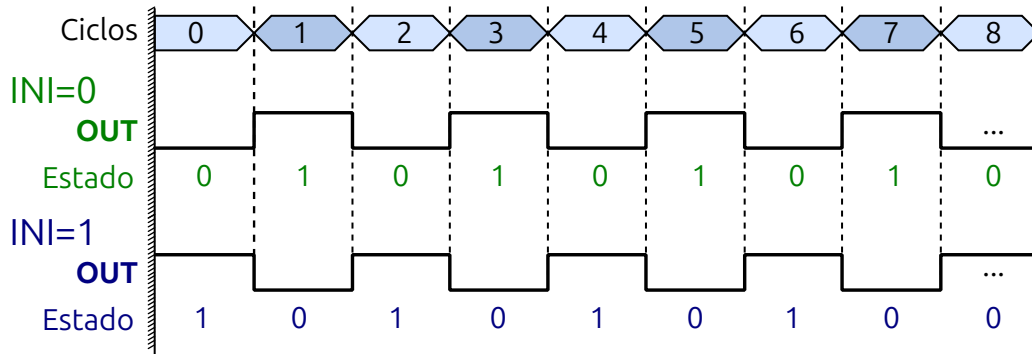


# Circuito en Icestudio



Recursos: LC: 3 IO: 2 LC: 4 IO: 2

## Cronograma



## Estado

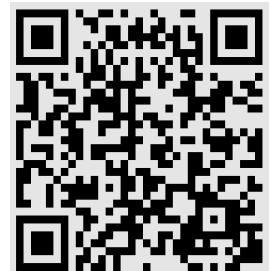
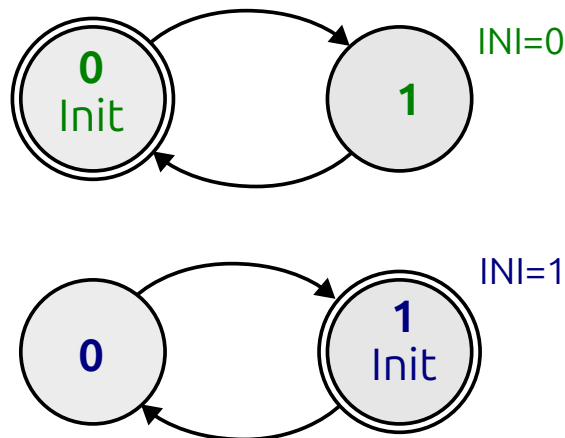
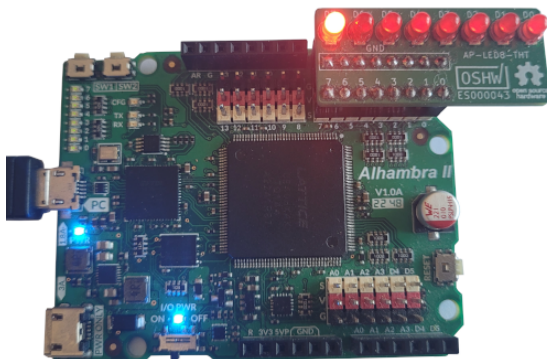
	(INI=0)	(INI=1)
Ciclo	Estado	Estado
0	0	1
1	1	0
2	0	1
3	1	0
	⋮	⋮

1 bit de almacenamiento

## Resultado

## Diagrama de estado

Efecto óptico:  
Un LED encendido



050 sysdiv2-ini Divisor sysclk entre 2, con valor inicial

## TUTORIAL (Obijuan)