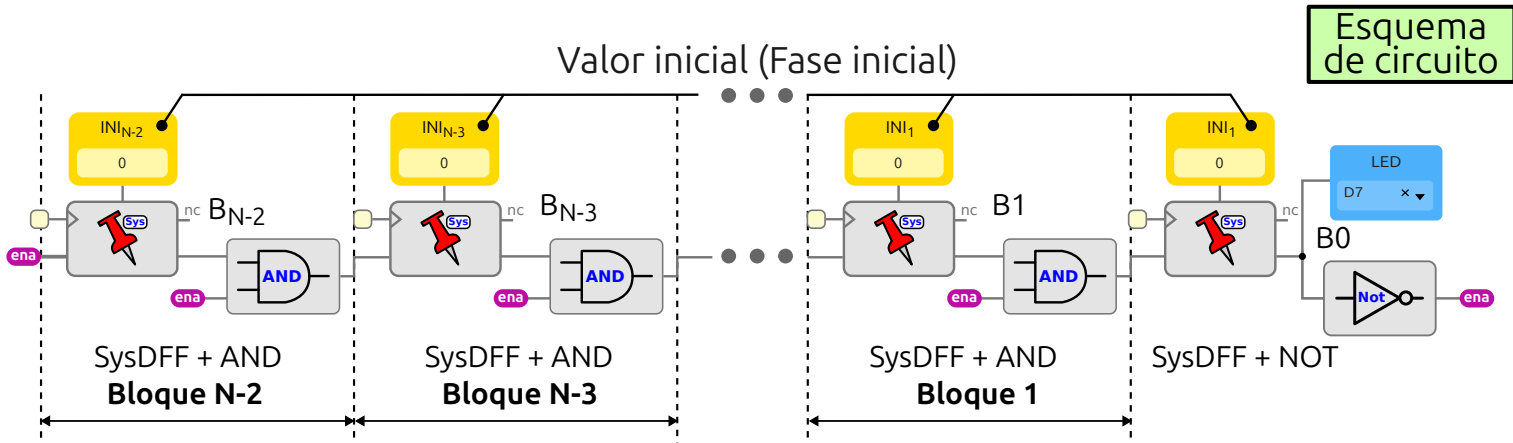


# Circuito en Icestudio



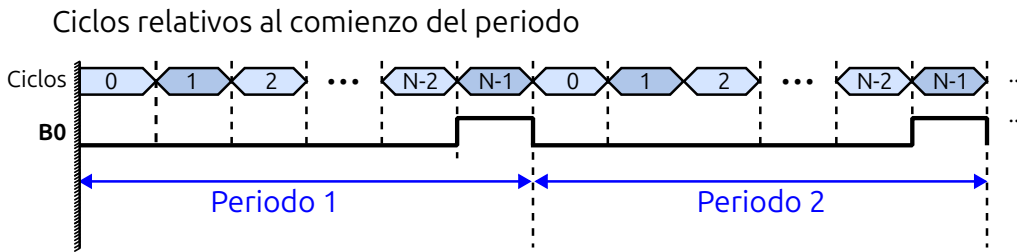
Recursos: LC: N+1 IO: N+1

Prescaler 1:n

N-1 puertas AND

N-1 Biestables

## Cronograma



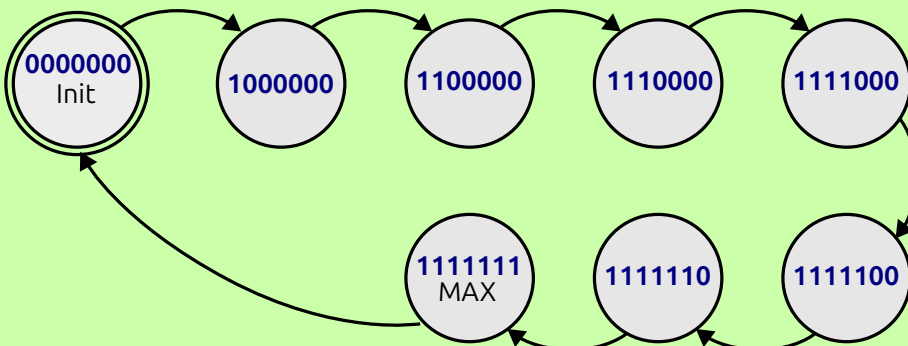
## Estado

Ciclo	B <sub>N-2</sub>	B <sub>N-3</sub>	...	B <sub>1</sub>	B <sub>0</sub>
0	0	0	...	0	0
1	1	0	...	0	0
2	1	1	...	0	0
...	...	...	...	...	...
N-2	1	1	...	1	0
N-1	1	1	...	1	1

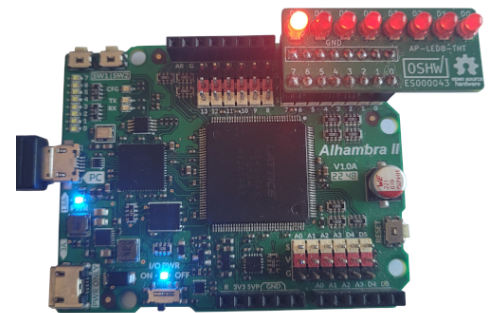
N-1 bits de almacenamiento

## Diagrama de estado

Caso de estudio N=8



Efecto óptico:  
Un LED encendido



070 unary-prescaler1:n

Divisor del reloj de sistema entre n

## TUTORIAL (Obijuan)

