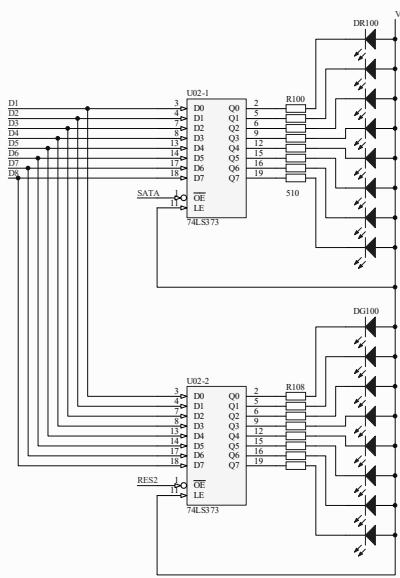
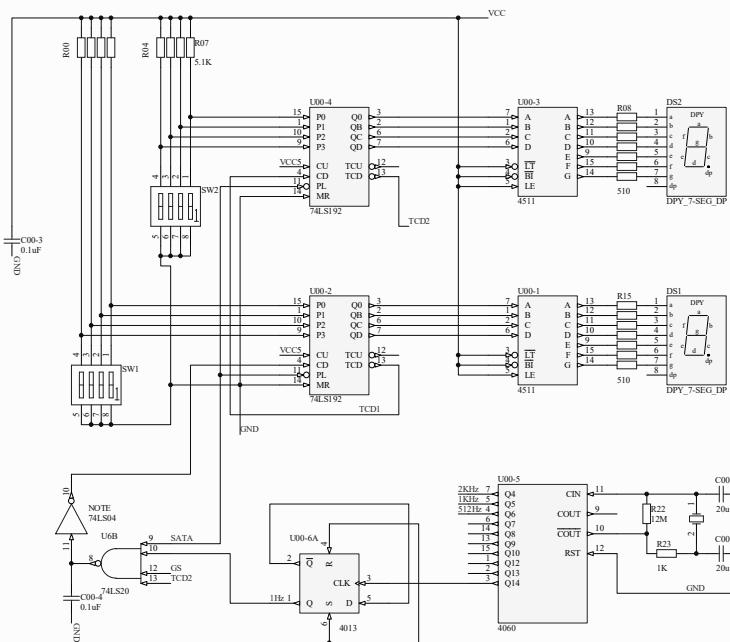


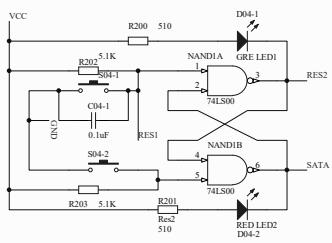
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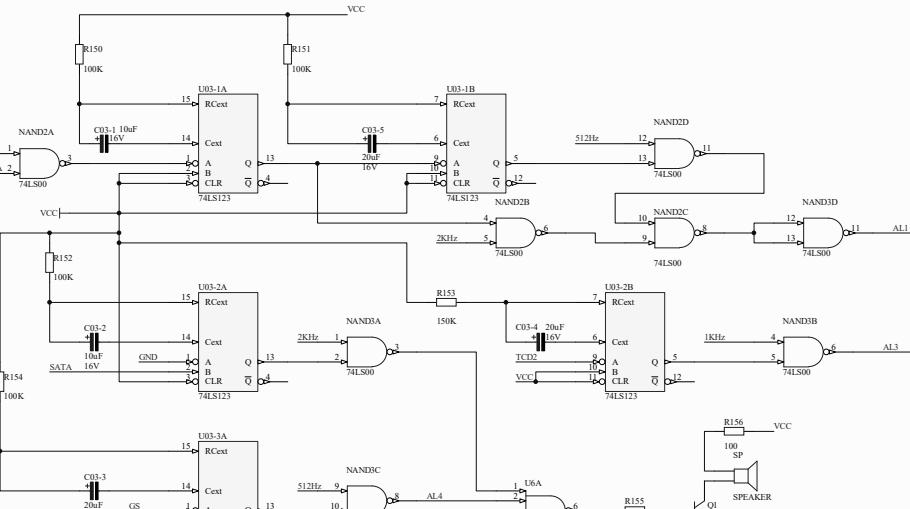
PART-02 抢答者指示电路图R100-149



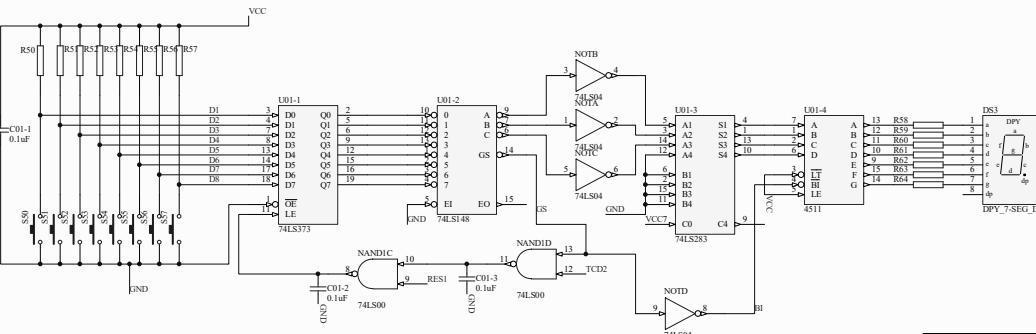
PART-00 可预置时间倒计时电路原理R00-49



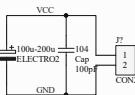
PART-04 各主持人控制电路R200-249



PART-03 各种警报音电路R150-199



PART-01 抢答输入、锁存、显示电路R50-99



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