



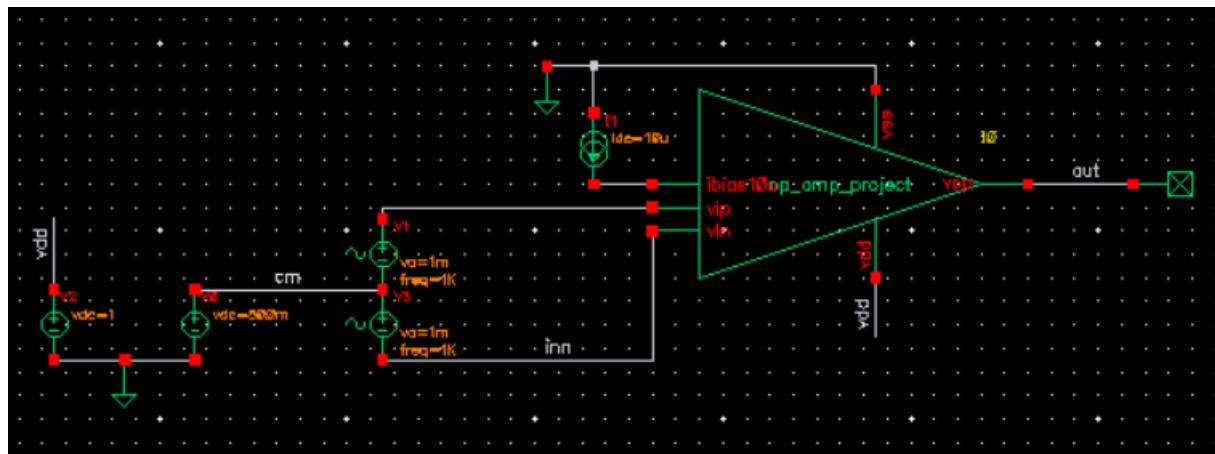
Τμήμα Ηλεκτρολόγων Μηχανικών
& Μηχανικών Υπολογιστών
**ΠΑΝΕΠΙΣΤΗΜΙΟ
ΠΕΛΟΠΟΝΝΗΣΟΥ**

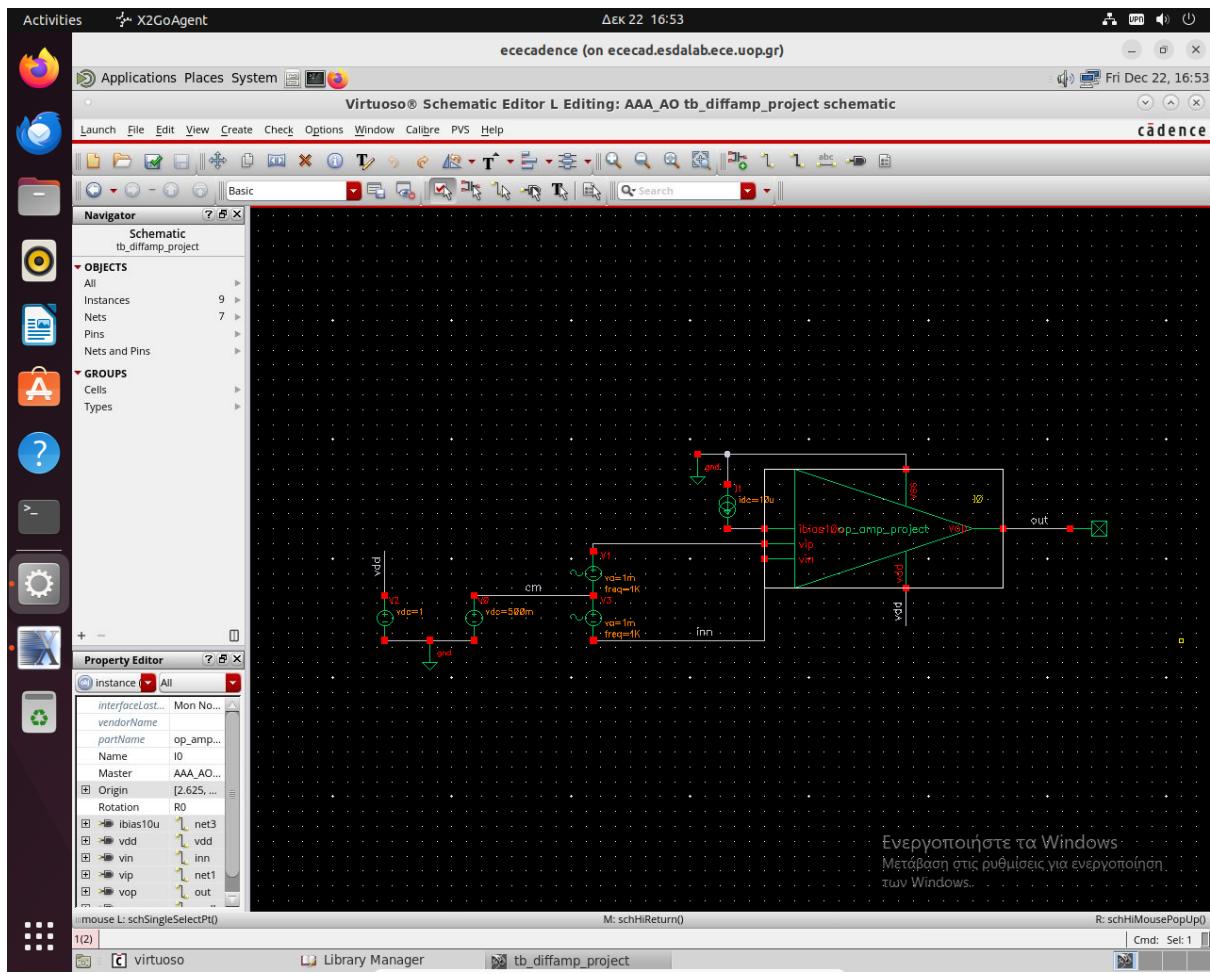
Operational Amplifier Design at Cadence

Avlonitis Konstantinos-Odysseas, Undergraduate Student

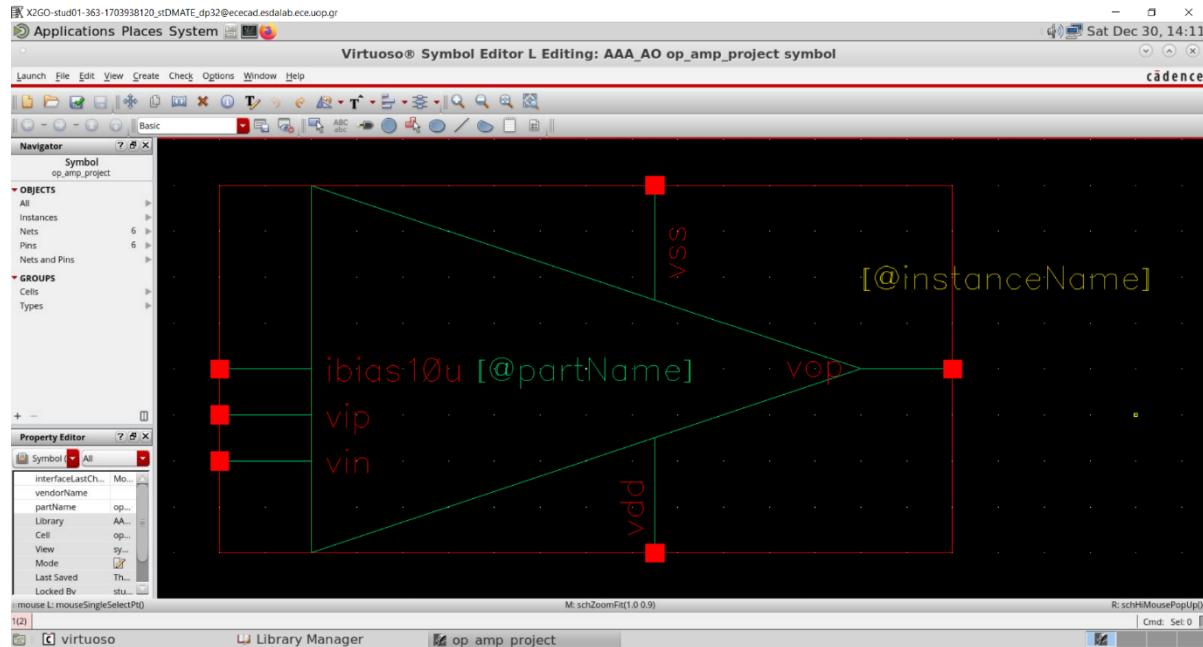
Department of Electrical and Computer Engineering, University of the Peloponnese

Testbench Schematic:

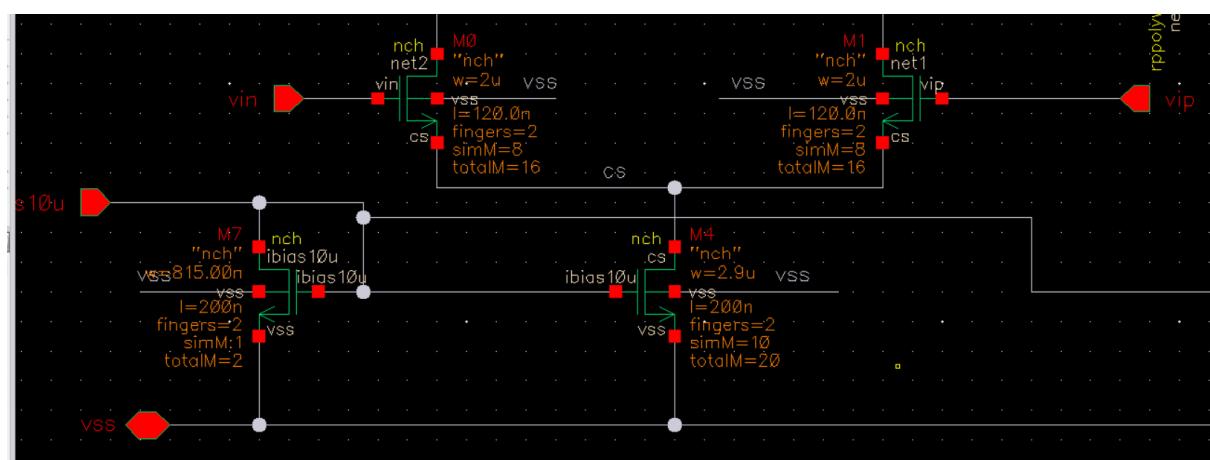
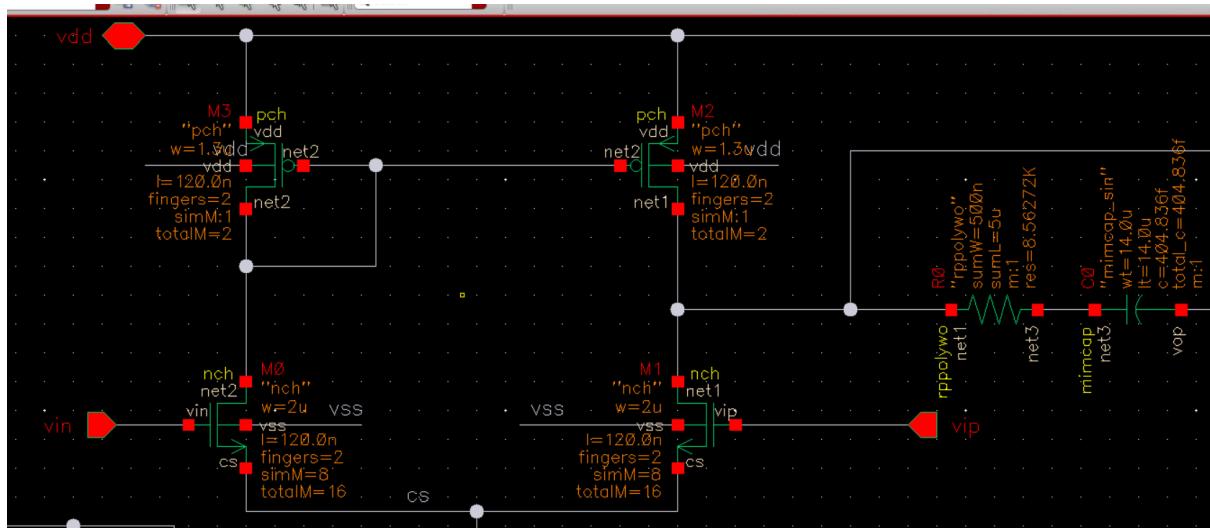
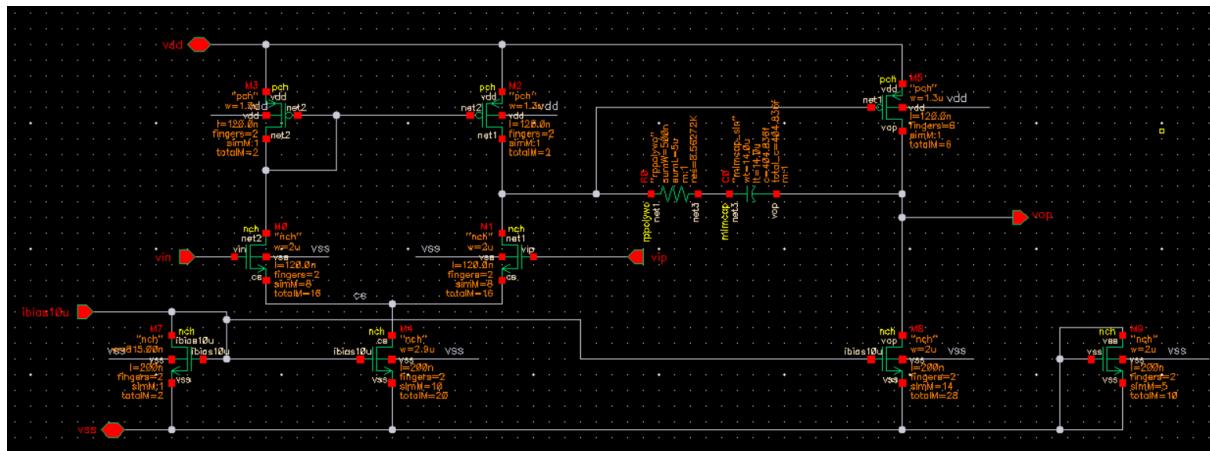


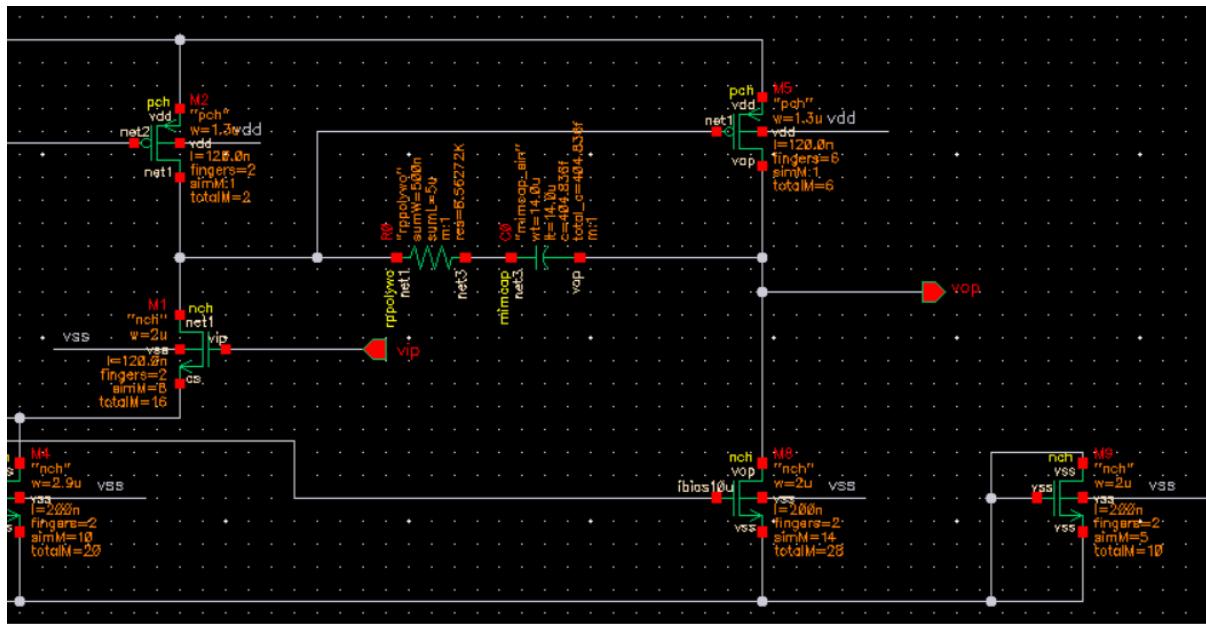


Symbol:

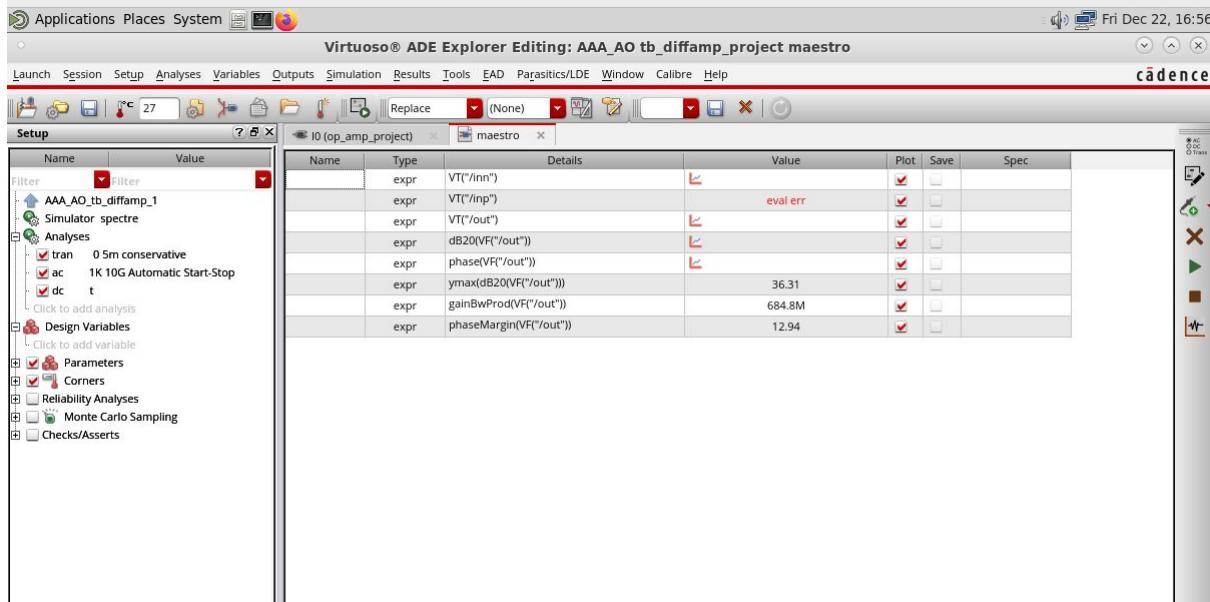
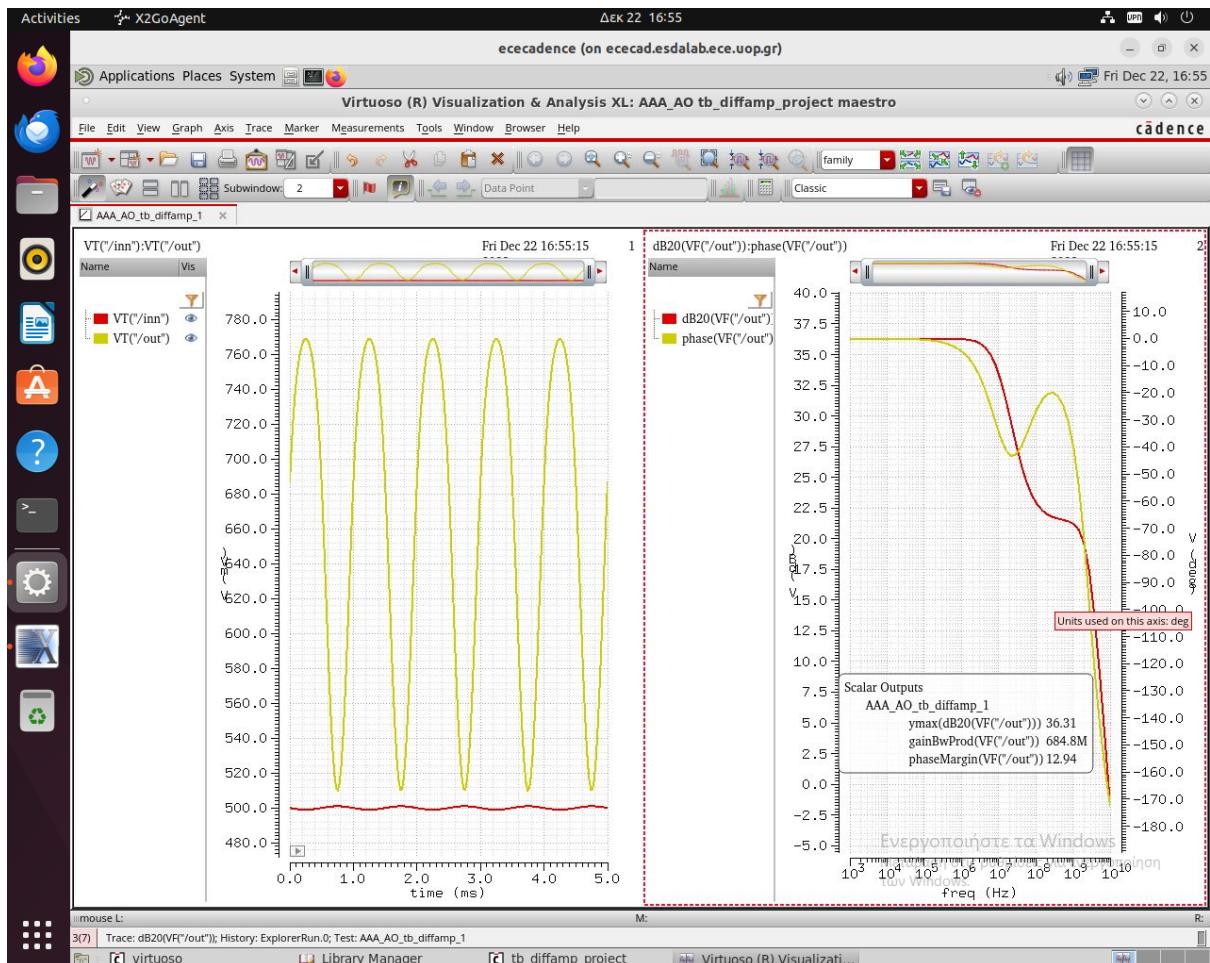


Schematics:

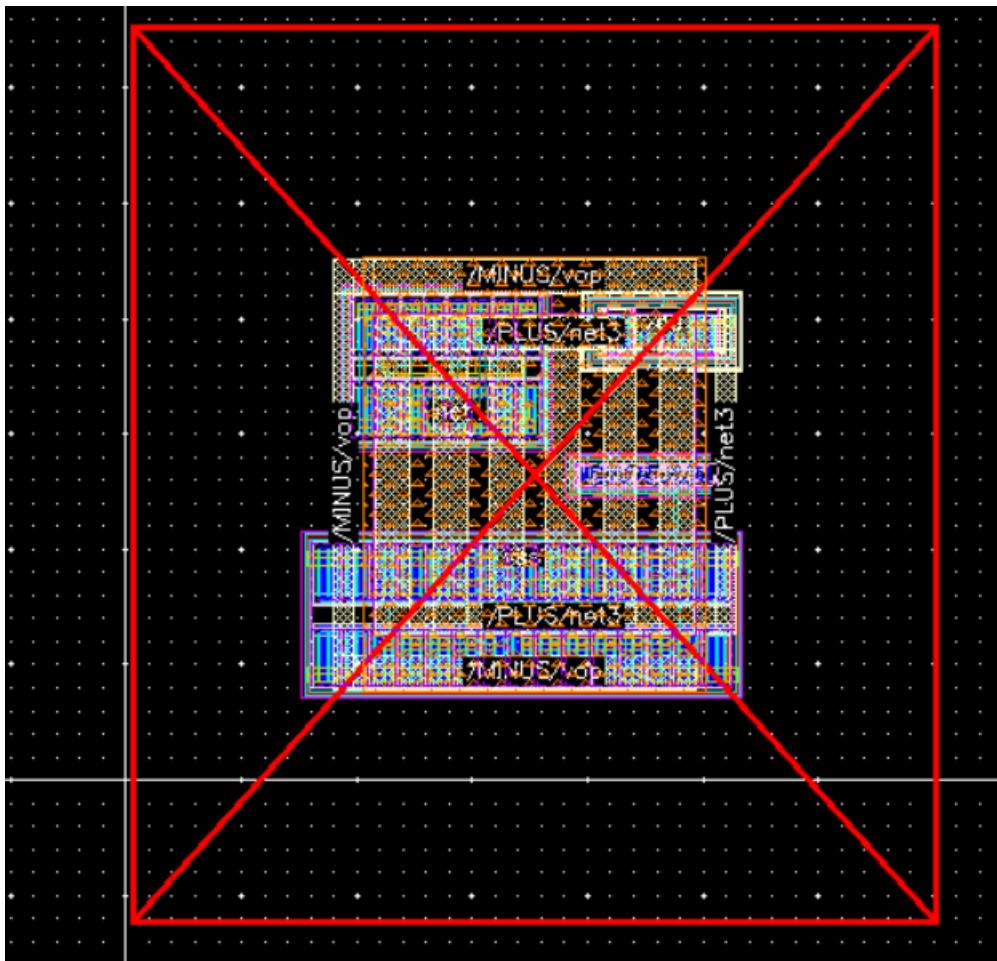




Maestro Simulation Schematic:

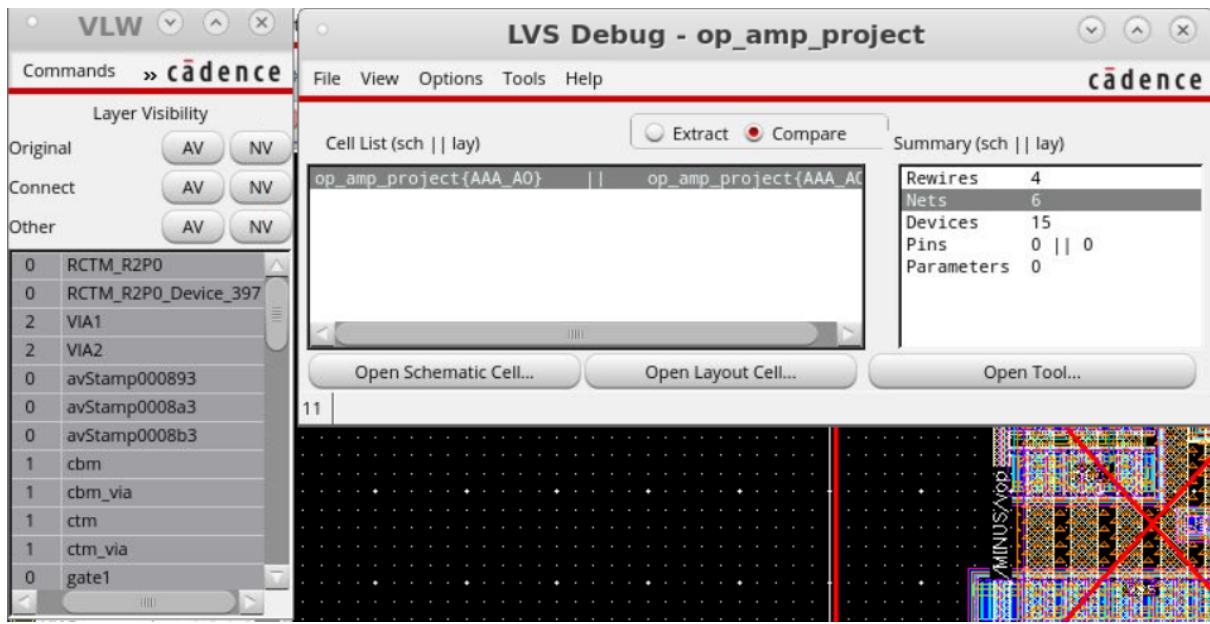


Layout:



LVS:





DRC:

