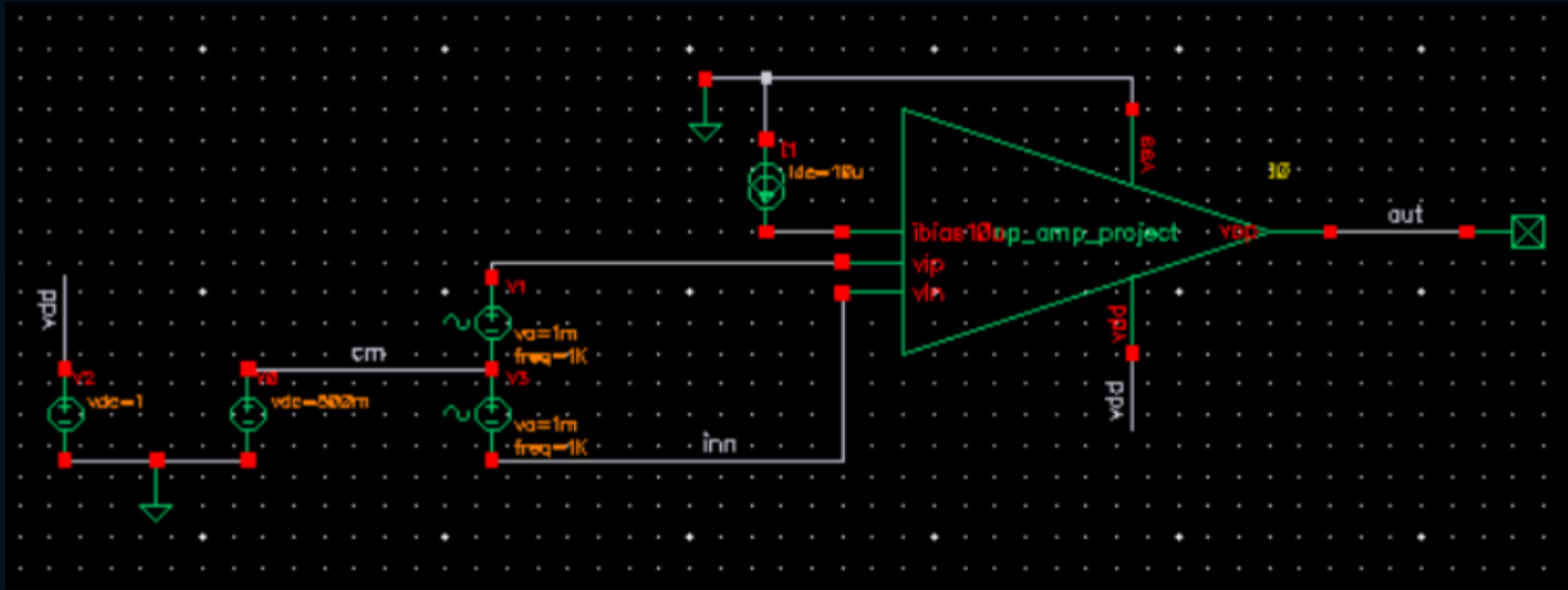


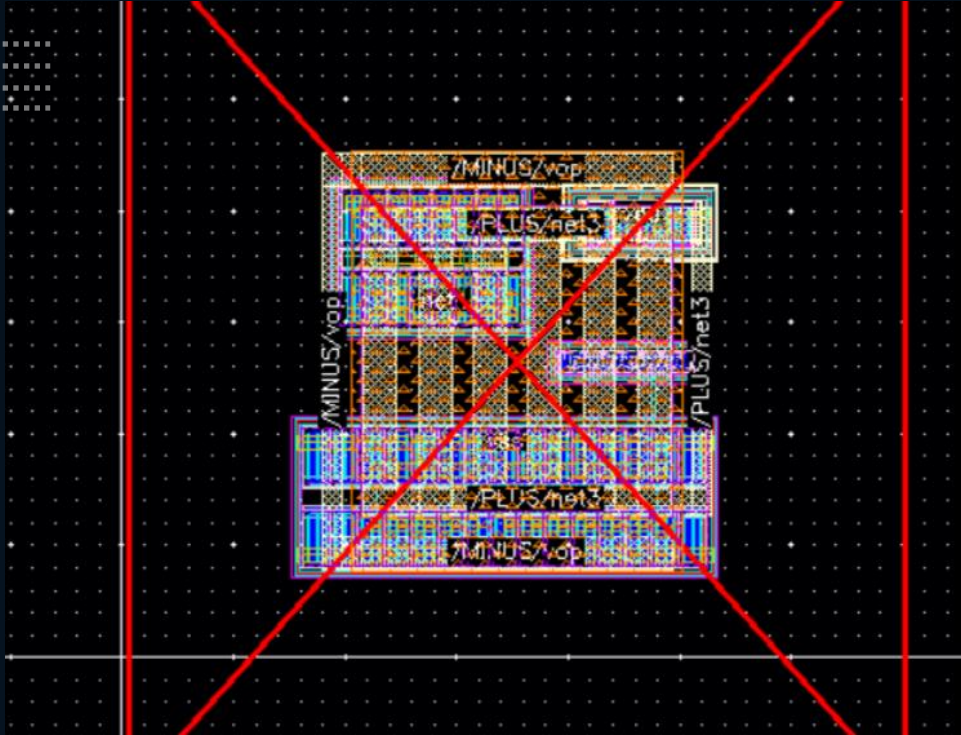
# Introducing my self

Avlonitis Konstantinos Odysseas

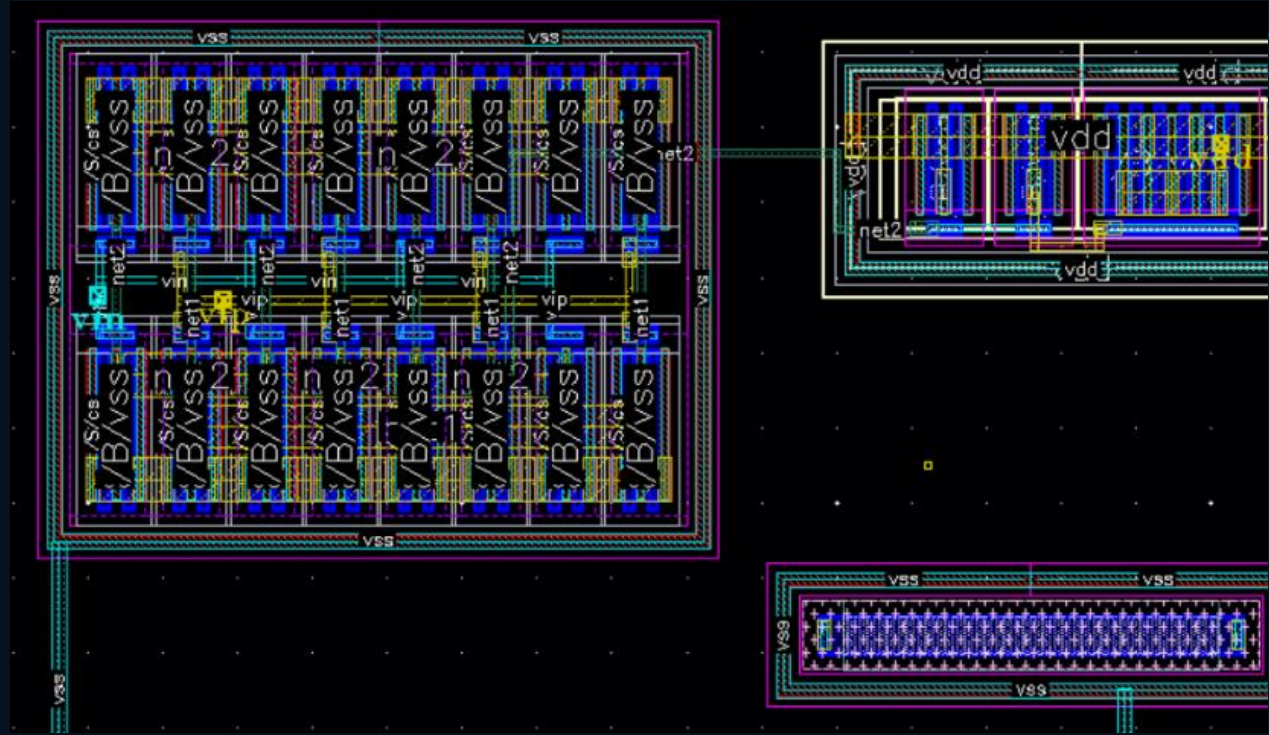
# Design Operational Amplifier At Cadence

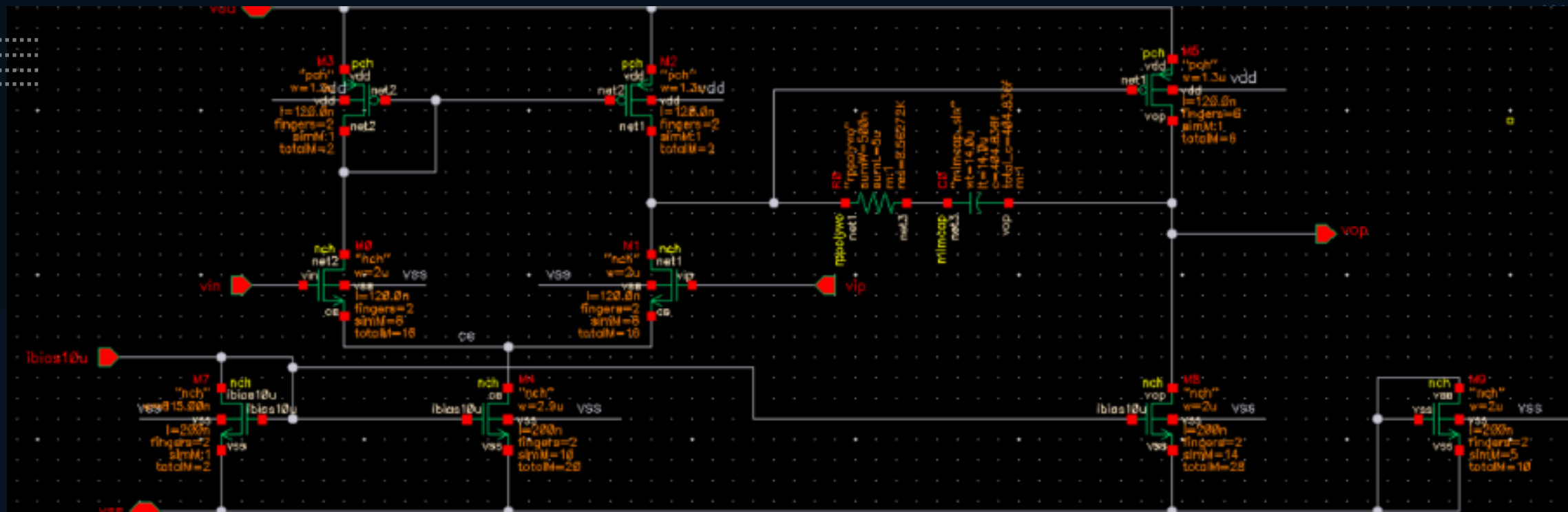


Testbench Schematic



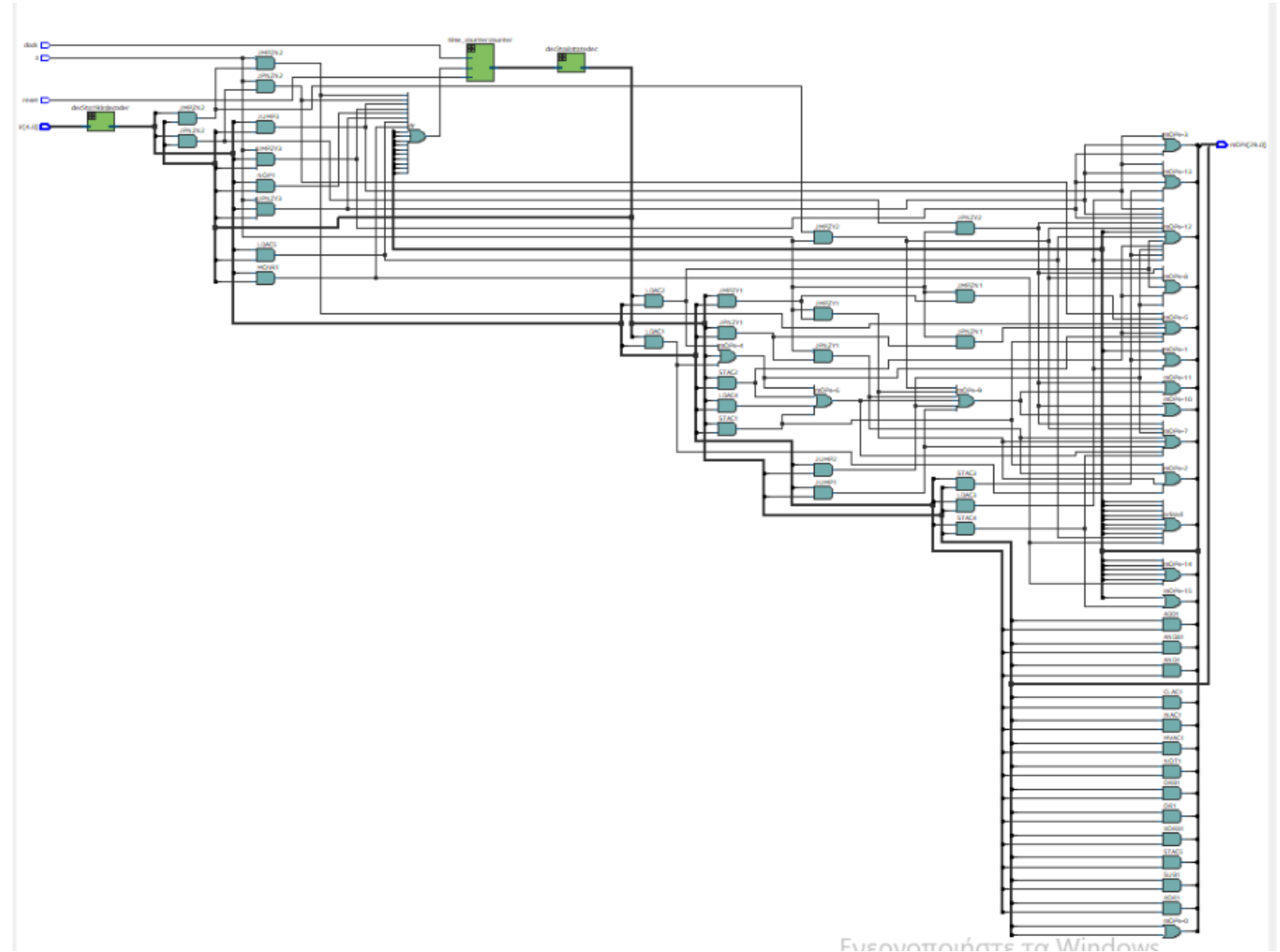
Layout



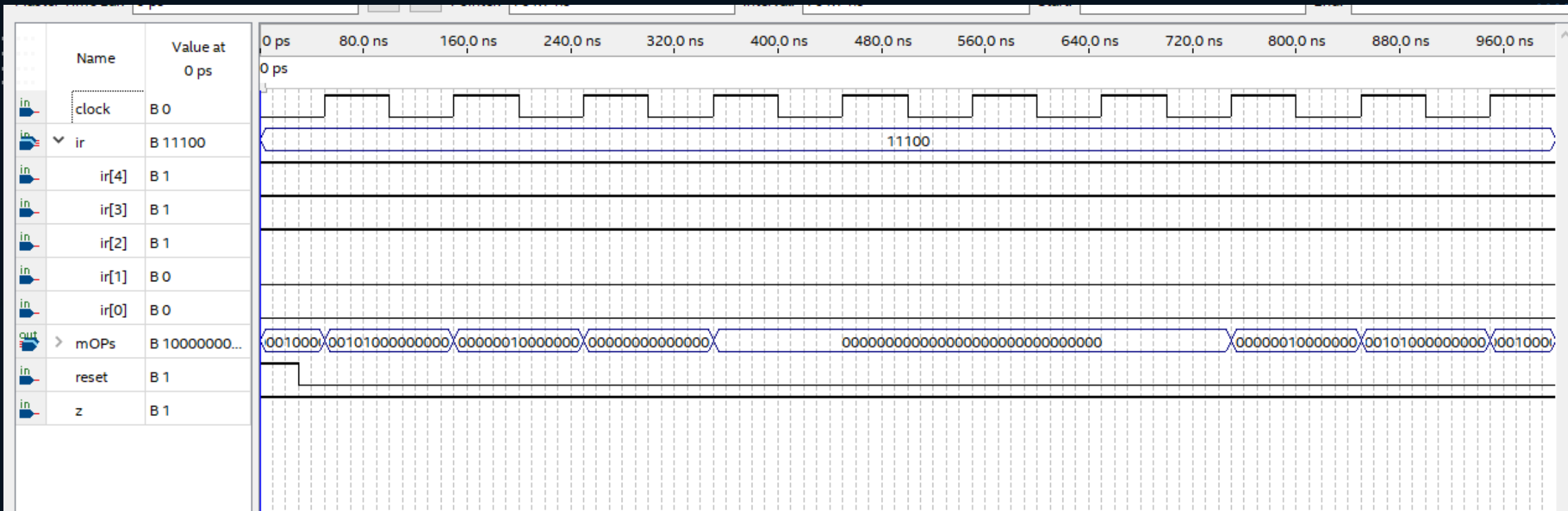


Schematics

# Simple CPU with an additional 8- bit B register at Quartus



Ενεργοποιήστε τα Windows



If B = 11100