表１，CONFIG\_ADDRESS register

| **bit** | **name** |
| --- | --- |
| 31 | Enable Bit |
| 30-24 | Reserved |
| 23-18 | Bus Number |
| 15-11 | Device Number |
| 10-8 | Function Number |
| 7-2 | Register offset |
| 1-0 | 0 |