

THE WESTERN DESIGN CENTER, INC.

W65CO2S Datasheet



Table 6-4 Operation, Operation Codes and Status Register

		in motor			1				1 100000	I TO A VIOLEN	THE WELL WITH	3.00	100000000000000000000000000000000000000												
Mnemomic	Operation # Immediate Data ~ NOT → AND ∨ OR * Exclusive OR	æ	(a,x)	a,×	a,y	(a)	A	#	-		w	¢z	(x,qz)	x'dz	Ą'dz	(dz)	y,(qz)	7	Pro	cesso *(r Stat Jser [us Red Defined	gister d 2	(P)	0
Mine		1	. 2	3	4	5	6,	7	8	9	10	11	12	13	14	15	16	N	٧	1	1	D	1	Z	С
ADC	A÷M+C→A	6D		7D	79	2.00		69	1.154		N 48	65	61	75	A francis	72	71	N	٧					Z	С
AND	A^M→A	2D		3D	39			29				25	21	35		32	31	N						Z	· .
ASL	C←76543210←0	0E		1E		4 470	0A	i da				06		16	. 469			N						Z	С
BBR0	Branch on bit 0 reset				The second			1 30		0F		L SA						1.39			i.		11.0		
BBR1	Branch on bit 1 reset							100		1F		1 (45)													
BBR2	Branch on bit 2 reset									2F			88					1.3				0 8.6%	4.0		
BBR3	Branch on bit 3 reset									3F	l est														gui.
BBR4	Branch on bit 4 reset									4F															
BBR5	Branch on bit 5 reset								1.39	5F												di jesti			287
BBR6	Branch on bit 6 reset									6F												10,000			gard -
BBR7	Branch on bit 7 reset									7F												107 (48)			
BBS0	Branch on bit 0 set								10.6	8F												61 .032			
BBS1	Branch on bit 1 set								1 44	9F											14. N				
BBS2	Branch on bit 2 set	All the second								AF															
BBS3	Branch on bit 3 set								Laa	BF.															
BBS4	Branch on bit 4 set								11	CF															- 10.
BBS5	Branch on bit 5 set									DF															
BBS6	Branch on bit 6 set									EF															
BBS7	Branch on bit 7 set									FF						1									
BCC	Branch C = 0					1				90		1000													
BCS	Branch if C=1									BO													I seed		
BEQ	Branch if Z=1									F0															
BIT	A^M	2C		3C				89				24		34				M ₇	Me		186 gg		Z		
BMI	Branch if N=0							1 10		30															are.
BNE	Branch if Z=0									D0													1.00		
BPL	Branch if N=0							i dei		10			1.00								i i ka				
BRA	Branch Always					100		E AV		80			1.0												
BRK	Break								1 - 5,0		00									100	1	0	1		
BVC	Branch if V=0									50												traggi kir	SHEET.		
BVS	Branch if V=1									70						,									
CLC	C → 0								18																0
CLD	0 → D					100			D8							20.1						0			
CLI	0 → 1								58														0		
CLV	0 → V								B8										0						
CMP	A-M	CD		DD	D9			C9		-		C5	C1	D5		D2	D1	N	J					7	C
CPX	X-M	EC						EO				E4				52		N				(Institute)		Z	C
CPY	Y-M	CC						CO				C4						N			16-4-100	300160		Z	C
DEC	Decrement	CE		DE			3A	00		line.		C6		D6				N						Z	Ü
DEX	X-1 → X						57		CA			33		00				N			100			Z	
DEY	Y-1 → Y						The same		88									N						Z	
EOR	$Y-1 \rightarrow Y$ A v M \rightarrow A	4D		5D	59			49	00			45	41	55		52	51					e orden	lenil.		ATE:
INC	Increments	EE.		FE	39		1A	43				#5 E6	71	F6		52	51	N					e tevil	Z	No.
INX				1 -					E8					, 0				N							
INY	$X+1 \rightarrow X$ $Y+1 \rightarrow Y$	1							C8									N			SIN			Z	
	The state of the s	-							00									N						Z	•



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	Operation # Immediate Data ~ NOT					îs.											_		Processor Status Register (P) *User Defined						
nic nic	^ AND v OR		×	,	>	_				i di			(x'dz)	x'dz	zp,y	(dz)	(zp),y			-		3	2	1	- 0
Mnemomic	xv Exclusive OR	a	(a,x)	a,x	a, y	(a)	<	#		Ŀ	, v	Zp		-				7	6	5	4	D	1	Z	-
Mne		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	N	V	1	1		-	Z	_
SR	Jump to Subroutine	20				100	. 10											N						Z	
DA	$M \rightarrow A$	AD		BD	B9			A9				A5	A1	B5		B2	B1	N	171.			2.0.0		Z	
DX	$M \rightarrow X$	AE			BE			A2		190		A6			B6			N	•			100 (40)		Z	
DY	$M \rightarrow Y$	AC		BC				A0		1		A4		B4				N						Z	
.sr	0 → 76543210 → C	4E		5E			4A					46		56				0	-						
IOP	No Operation								EA	1 1	3						15.	1		-		Balance No.		Z	
ORA	$A \lor M \rightarrow A$	0D		1D	19			09				05	01	15		12	11	N			4070.7			-	
PHA	$A \rightarrow Ms, S-1 \rightarrow S$									1 18	48							- Contraction							
PHP	$P \rightarrow Ms, S-1 \rightarrow S$									1 10	08							-							
РНХ	X → Ms, S-1 → S										DA							1							
PHY	Y → Ms, S-1 → S)							5A							1			100.0			Z	
PLA	S + 1→S, Ms → A										68							N			-		-	Z	1000
PLP	S + 1→S, Ms → P										28							N	V		1	D			
PLX	S + 1→S, Ms → X										FA		-					N						Z	
PLY	S+1→S, Ms → Y										7A							N		•				Z	
RMB0	Reset Memory Bit 0			N								07									7.				
RMB1	Reset Memory Bit 1						a de la composition della comp					17	in the same		-		-								
RMB2	Reset Memory Bit 2											27								1.				-	
RMB3	Reset Memory Bit 3		11.60									37													
RMB4	Reset Memory Bit 4									100	-	47		-	-	-		-				- 6.00g			
RMB5	Reset Memory Bit 5											57				12									
RMB6	Reset Memory Bit 6											67	1									i. Speci			
RMB7	Reset Memory Bit 7				1		-	-				77	-												
ROL	C←76543210 ← C	2E		3E			2A					26		36				N						2	Z
		6E		7E			6A					66		76				N				egion.	A SON	. 7	Z
ROR	C→76543210→C	0								-	40	-	-					N	\	/	. 1	. [)	1 7	Z
RTI	Return from Interrupt										60							١.				Owy	1,000	28	
RTS	Return from Subroutine	-	-		F9	+	+	E9	+	+		E5	E1	F5	1	F2	F1	N	1	/			, it does		Z
SBC	A - M - (~C) → A	ED		FD	La			-	38																
SEC	1 → C								F8														1		
SED	1 → D	-	132			-		+	78	-	1		-										.14	1	
SEI	1 → I	-	-			-			10			87	+					1						- 10	
SMB0	Set Memory Bit 0				-	-			-	-		97	-											0.5	
SMB1	Set Memory Bit 1			1 1		+					4	A7					+								
SMB2	Set Memory Bit 2									No. of Control	+	B7	-		+			+							
SMB3	Set Memory Bit 3									-		-	-		1			+							
SMB4	Set Memory Bit 4				-	-		-				C7	-		-			-	1000	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1					
SMB5	Set Memory Bit 5			the street		- 2						D7	-	-	+			+					-		77.7
SMB6	Set Memory Bit 6											E7	-			-		-							
SMB7	Set Memory Bit 7											F7	-	-			+	+		roes ha					
STA	$A \rightarrow M$	80		90	99							85	8	95	-	92	2 9	-				•			of a
STP	STOP (1→ PHI2)	1 1							D	В															