

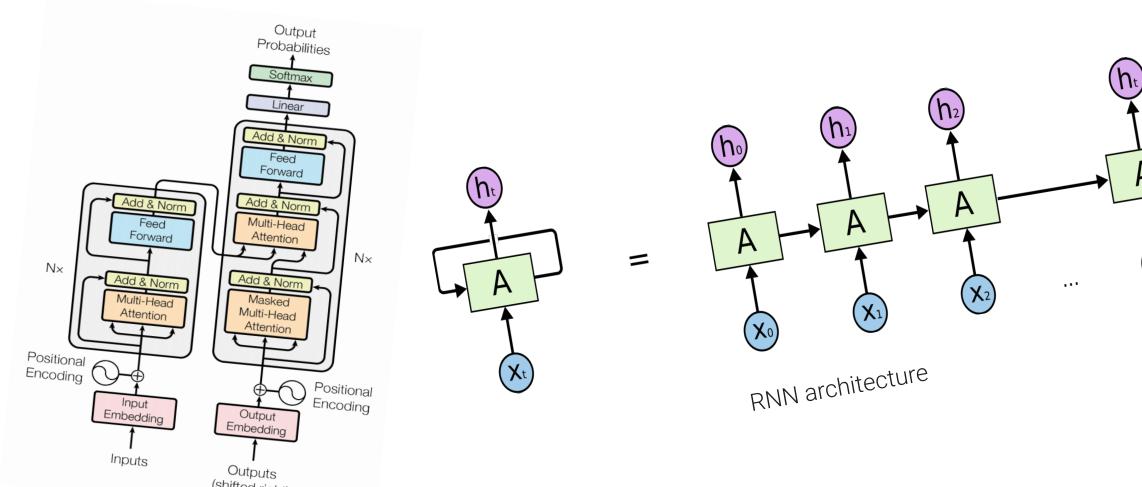
Neural Network Arena: Investigating Long-Term Dependencies in Deep Models

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GitHub repository

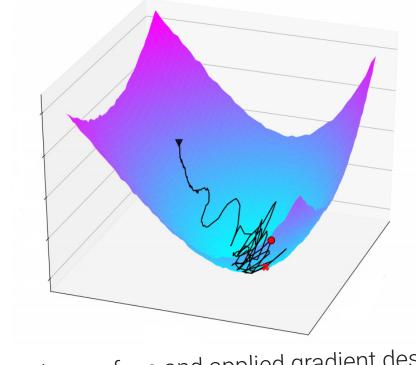


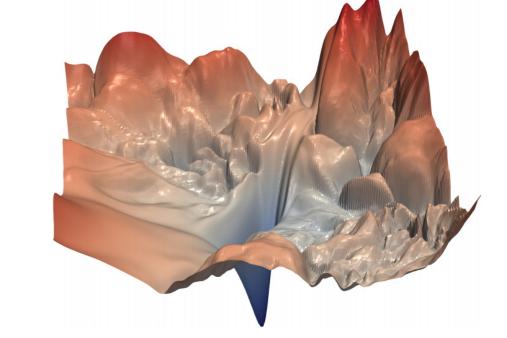


Transformer architecture

Implemented Models

- · LSTM (Long Short-Term Memory)
- GRU (Gated Recurrent Unit)
- CT-RNN (Continuous-Time RNN)
- CT-GRU (Continuous-Time GRU)
- ODE-LSTM (Ordinary Differential Equation LSTM) NCP (Neural Circuit Policies)
- Unitary RNN
- Matrix Exponential Unitary RNN new
- Unitary NCP new
- Transformer
- Recurrent Network Augmented Transformer **new**
- Recurrent Network Attention Transformer **new**
- Memory Augmented Transformer new
- Differentiable Neural Computer
- Memory Cell new





loss function surface and applied gradient descent procedure

rugged loss function surface

Problem Statement

• implementation of a reusable benchmark suite to compare machine learning mo-benchmark suite should test the models for their capabilities to capture long-

• selection of state-of-the-art models should be implemented as well as possible

• thoroughful comparison of all implemented models using the benchmark suite • all implemented models are Transformer or RNN (Recurrent Neural Network) ar-

proof-of-concept design and implementation of a continuous-time memory cell

architecture based on LTC Networks

Methodology

• extensive literature review in the domain of sequence modeling

• implementation of the benchmark suite and all the implemented models

• the benchmark suite was invoked three times on all models and the statistics of the invocation output were interpreted

models on various sequence modeling tasks

term dependencies when being learned by gradient descent • investigate which mechanism works best in RNN architectures to

• provide an objective comparison and overview of all implemented Motivation

• especially RNN architectures have difficulties of capturing long-

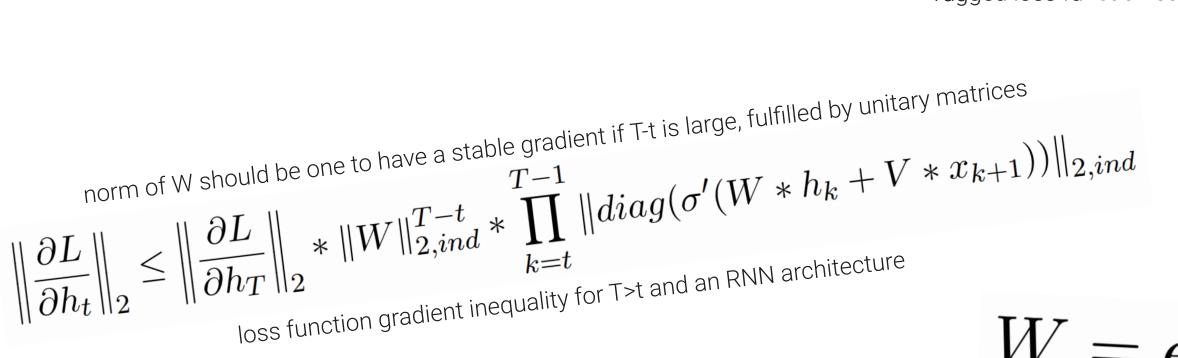
counteract this difficulty

Gradient Descent

- · each model is just a parameterized function whose parameters are optimized by derivating a loss function that compares model output with expected output
- parameters are updated according to the loss function gradient which should not vanish or explode ("walk down the hill")

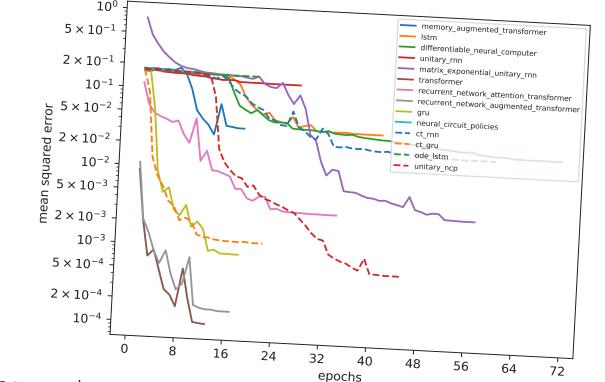
Results

- state-of-the-art in efficient unitary matrix parameterization was improved by using an approximated matrix exponential
- continous-time memory cell architecture was successfully trained to store sparse activations
- positional encoding used in Transformer architectures shows deficiencies in tasks where exact positional information is required
- the newly introduced Memory Augmented Transformer architecture shows promising results in some tasks



 $W = e^A$

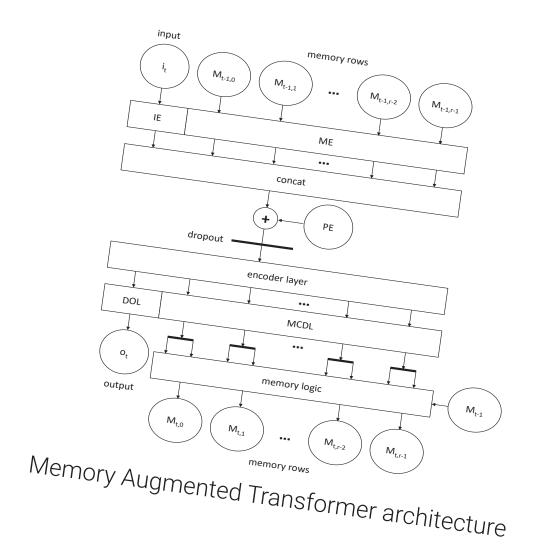
a unitary matrix W can be written as the matrix exponential of a skew-Hermitian matrix A

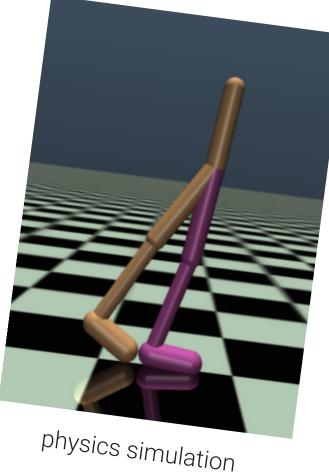




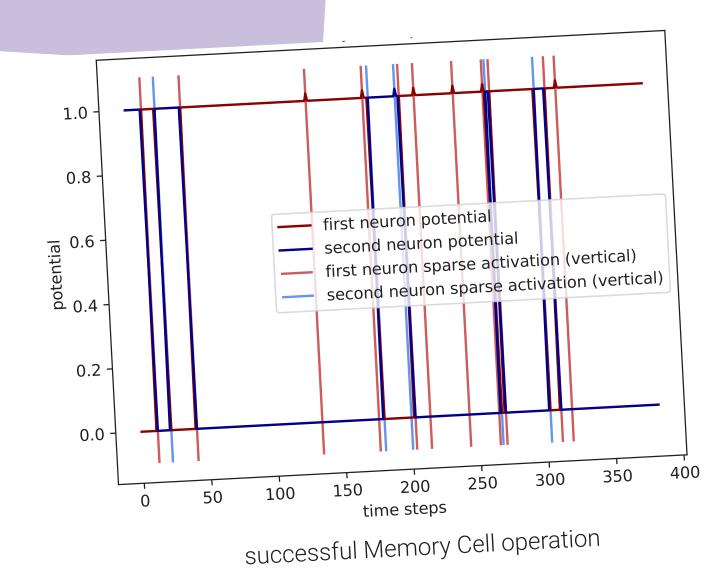


statistics of the Add Benchmark





leakage current leakage current inhibitory synapse recurrent synapse neuron neuron recurrent synapse inhibitory synapse input synapse input synapse Memory Cell architecture



Benchmark Suite Tasks

- Activity Benchmark human activity classification of inertial sensor measurement data sequences
- Add Benchmark adding up two marked numbers in a very
- Walker Benchmark predict the next state of a physics simulation given a sequence of previous simulation states
- Memory Benchmark store a seen category exactly and recall it after seeing a sequence of irrelevant filler symbols • MNIST Benchmark - digit classification using a sequence of
- MNIST handwritten digit image chunks Cell Benchmark - validates if sparse activations are correctly
- stored in the time-continuous memory cell architecture