

Digital Design Engineer

Vienna, Vienna, Austria

- Hybrid work
- Full-time position
- Entry level position

Have you ever wondered how it is to work in a company that shapes the future? Then we have a great opportunity for you to boost your career! Don't miss this chance and apply now. In your new role you will:

- Be responsible for Digital design (System Verilog / VHDL coding), verification and simulation
- Implement design-for-test concepts
- Closely cooperate with analog and digital designers and verification engineers
- Support hardware post-silicon validation in the lab

You are best equipped for this task if you have:

- A university / university of applied sciences degree in Electrical Engineering, Information Technology, Telematics or comparable with focus on electronics
- First experience in digital design
- Good knowledge of VHDL, Verilog, or SystemVerilog
- Experience with Unix programming languages such as shell, Perl, TCL etc.
- Fluent English skills with German skills is a plus

The employment is in accordance with the collective salary and wage agreement for employees of the electrical and electronics industry, employment group G. The monthly salary is paid 14 times p.a. We offer a higher compensation depending on your expertise and skills.

Driving decarbonization and digitalization. Together.

Our company designs, develops, manufactures, and markets a broad range of semiconductors and semiconductor-based solutions, focusing on key markets in the automotive, industrial, and consumer sectors. Its products range from standard components to special components for digital, analog, and mixed-signal applications to customer-specific solutions together with the appropriate software.

The central R&D organization provides the design environments (design-systems) to the different product development teams. With leading edge design methods, complex building blocks and a wide range of product development services DES enables advanced IC development. This means the complete value creation chain from abstract system models down to the fully verified product layout which ensures high quality manufacturing readiness.