

Kontakt

sherif_mohamed@chello.at

www.linkedin.com/in/sherif-m-7a6426206 (LinkedIn)

Top-Kenntnisse

Phyton

XML

Xilinx

Languages

Slovakian (Professional Working)

Italian (Professional Working)

German (Native or Bilingual)

Arabic (Native or Bilingual)

English (Full Professional)

Sherif M.

Digital Design Engineer @ TTTech Aerospace
Wien, Wien, Österreich

Zusammenfassung

- BSc degree in Computer Engineering
- MSc Student in Embedded Systems
- Digital Design Engineer in the Aerospace and Space sector

Berufserfahrung

TTTech

Digital Design Engineer

März 2023 - Present (11 Monate)

Vienna, Austria

- Chip Development (ASIC/FPGA) in Aerospace/Space
- Test and Integration
- Creation of verification cases and procedures
- Creation of verification environments for hardware testing and simulation
- Digital Design coding (VHDL and SystemVerilog)
- FPGA and ASIC implementation
- Review of concepts, requirements, source code, verification cases & procedures, tool outputs
- Documentation of design and verification
- Maintenance of product documentation

Bosch

Web Developer

Juli 2019 - Februar 2023 (3 Jahre 8 Monate)

Wien, Österreich

Full-Stack Web Application Development and maintenance according to ISO26262 for Functional Safety in Automotive Systems

Scripting for Hardware Failure Detection (FMEA, FMEDA, FTA)

Microsoft Excel Handling

Server Administration and Database Management

Technische Universität Wien

Study Assistant

September 2019 - Februar 2020 (6 Monate)

Vienna, Austria

Assisting Lecturer in Research and Student Support

Knorr-Bremse AG

Internship Trainee

Juli 2017 - Juli 2017 (1 Monat)

Austria

Scripting of Document based converters (*.csv, *.xml, *.txt, *.config, etc.)

Ausbildung

Technische Universität Wien

Master of Science - MS, Embedded Systems · (Januar 2021 - Juni 2023)

Technische Universität Wien

Bachelor of Science - BS, Computer Engineering · (Juni 2015 - Dezember 2020)

University of Vienna

Law · (Juni 2014 - Juni 2015)