

UNIVERSITY OF ZAGREB  
FACULTY OF ELECTRICAL ENGINEERING AND COMPUTING

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# **Software and Hardware Architecture for Redundant Embedded Systems**

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*Umjesto ove stranice umetnite izvornik Vašeg rada.*  
*Kako biste uklonili ovu stranicu, obrišite naredbu \izvornik.*

*Hvala.*

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TODO: remove!!Downes (2002)

# INTRODUCTION

Uvod u rad.

# 1. Conclusion

Zaključak rada.

# BIBLIOGRAPHY

Michael Downes. *Short Math Guide for L<sup>A</sup>T<sub>E</sub>X*. American Mathematical Society, 2002. URL <ftp://ftp.ams.org/pub/tex/doc/amsmath/short-math-guide.pdf>.



## **Software and Hardware Architecture for Redundant Embedded Systems**

### **Abstract**

Abstract.

**Keywords:** Keywords.

## **Programska i sklopovska arhitektura redundantnih ugradbenih računalnih sustava**

### **Sažetak**

Sažetak.

**Ključne riječi:** Ključne riječi, odvojene zarezima.