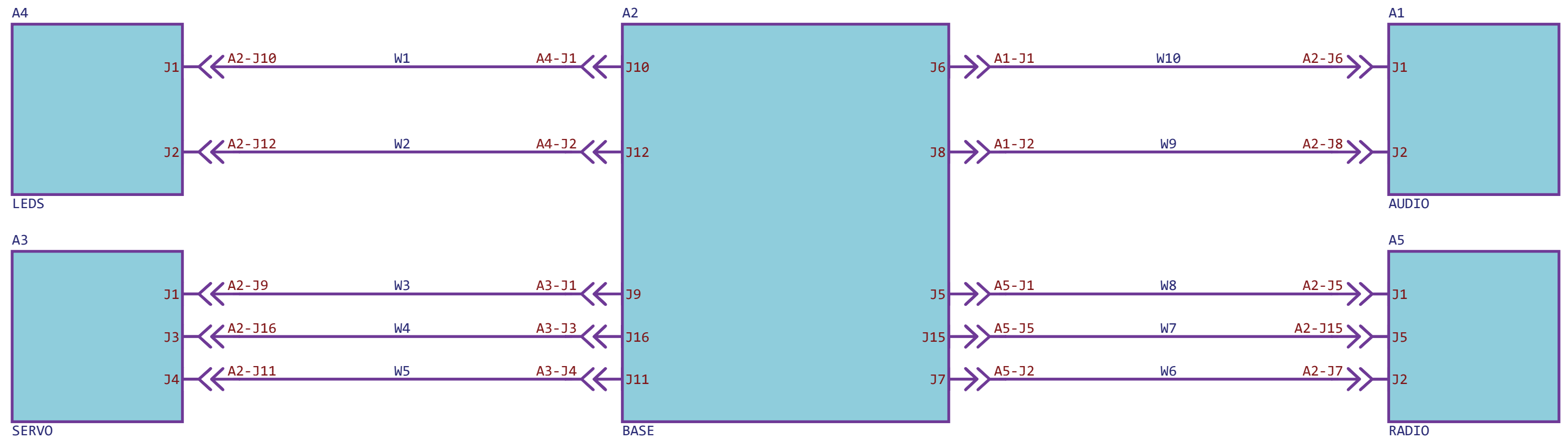


MULTIMOD REVISION 1

WIRING DIAGRAM



1		2		3		4		5		6		
A	INDEX	SCHEMATIC		DESCRIPTION								
	01	S01-LAUNCHPAD		EK-TM4C123GXL LAUNCHPAD HEADERS								
	02	S02-BEAGLEBONE		BEAGLEBONE BLACK HEADERS								
	03	S03-BEAGLEBONE		BEAGLEBONE BLACK INTERFACES								
	04	S04-INTERFACES		RADIO/AUDIO MODULE INTERFACES								
	05	S05-INTERFACES		SERVO/LED MODULE INTERFACES								
	06	S06-PERIPHERALS		JOYSTICK/CAMERA/TFT PERIPHERAL INTERFACES								
B												
C												
D												

SYSTEM DIAGRAM

RADIO
MODULE

AUDIO
MODULE

SERVO
MODULE

LED
MODULE

TIVA
LAUNCHPAD

BEAGLEBONE
BLACK

CAMERA

TFT/LCD
DISPLAY

NOTES

[1] CONNECTION TYPES AND IMPLEMENTATION DETAILS HAVE BEEN OMITTED FROM THE SYSTEM DIAGRAM

Engineering

UNIVERSITY OF FLORIDA

FLORIDA

UNIVERSITY OF

TITLE OVERVIEW AND TITLE PAGE

SIZE
B

PROJECT
MULTIMOD BASE

REVISION
1

ORGANIZATION
UNIVERSITY OF FLORIDA

DESIGNER
MEHRON TALEBI

IDENTIFIER
SUMMER 2023

REVIEWER
MIKE STAPLETON

1

2

3

4

5

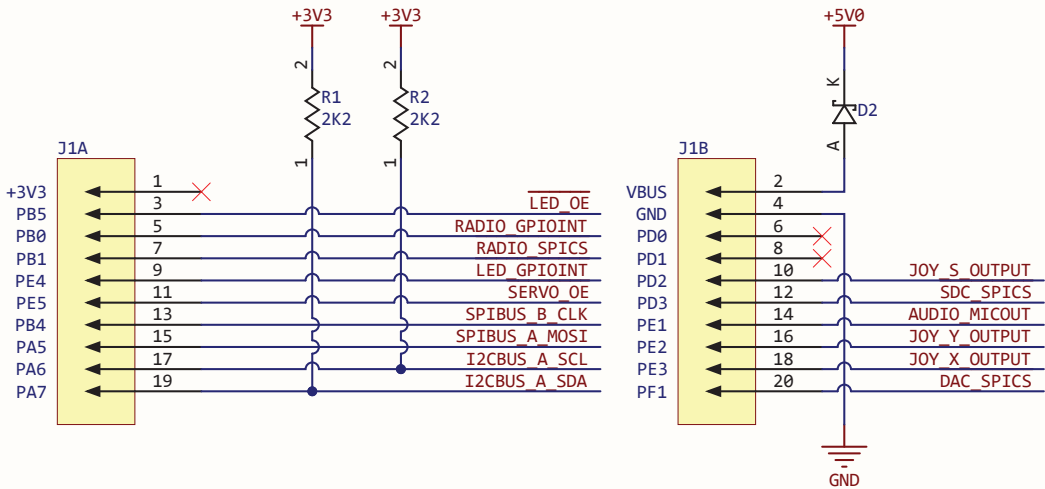
6



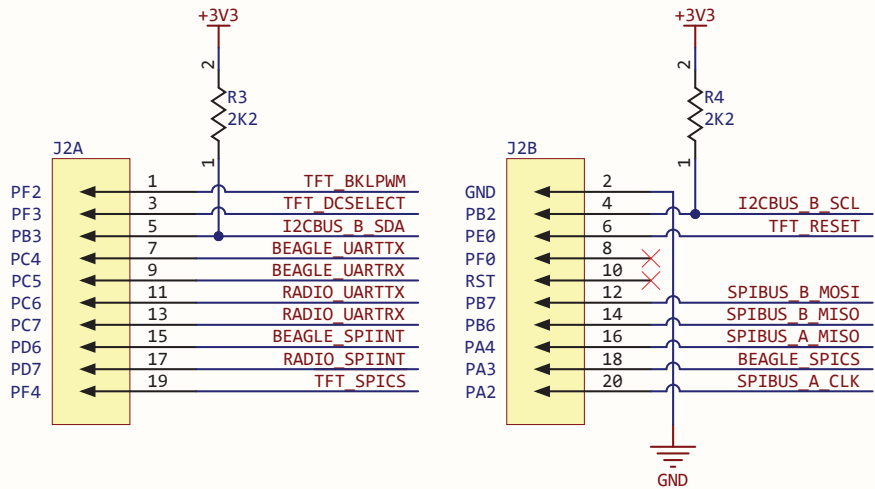
TITLE OVERVIEW AND TITLE PAGE

SIZE B	PROJECT MULTIMOD BASE	REVISION 1
ORGANIZATION IDENTIFIER	UNIVERSITY OF FLORIDA SUMMER 2023	DESIGNER MEHRON TALEBI REVIEWER MIKE STAPLETON

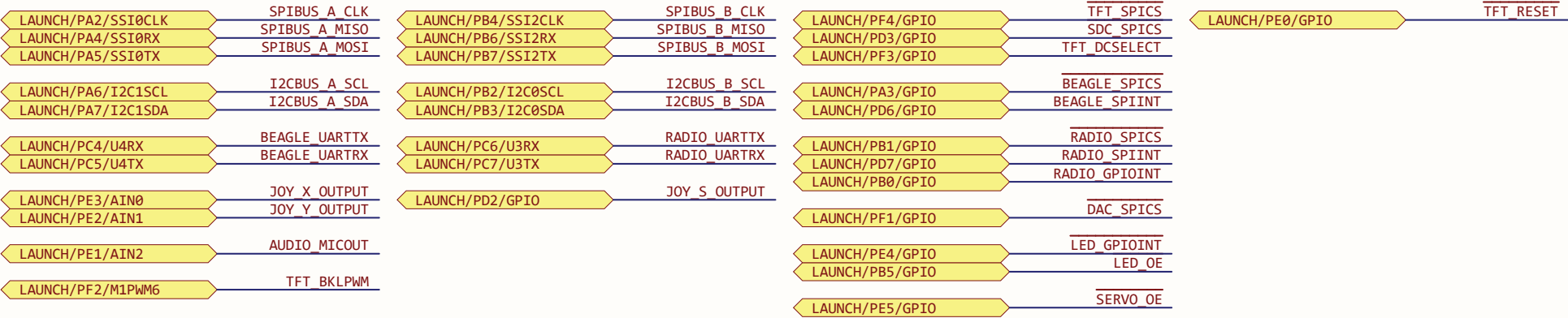
(1) LAUNCHPAD J1/J3



(1) LAUNCHPAD J2/J4



(1) GLOBAL NETS



NOTES

- [1] INTERRUPTS ON SENSORS BOOSTERPACK CANNOT BE USED DUE TO INTERFERENCE WITH PIN ASSIGNMENTS AT PE4, PA2, PA3, PA4, AND PA5!
[2] SENSORS BOOSTERPACK I2C ADDRESSES: OPT3001 = 0x47 / TMP007 = 0x40 / BMI160 = 0x69 / BMM150 = 0x13 / BME280 = 0x77



TITLE EK-TM4C123GXL LAUNCHPAD HEADERS

SIZE B

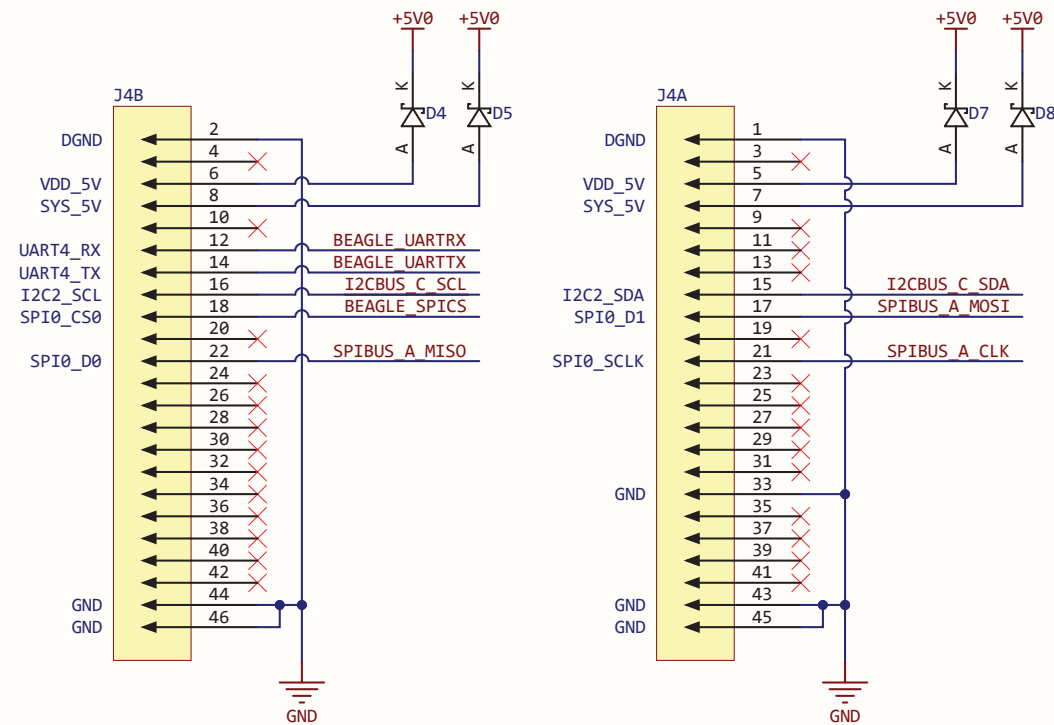
PROJECT MULTIMOD BASE

REVISION 1

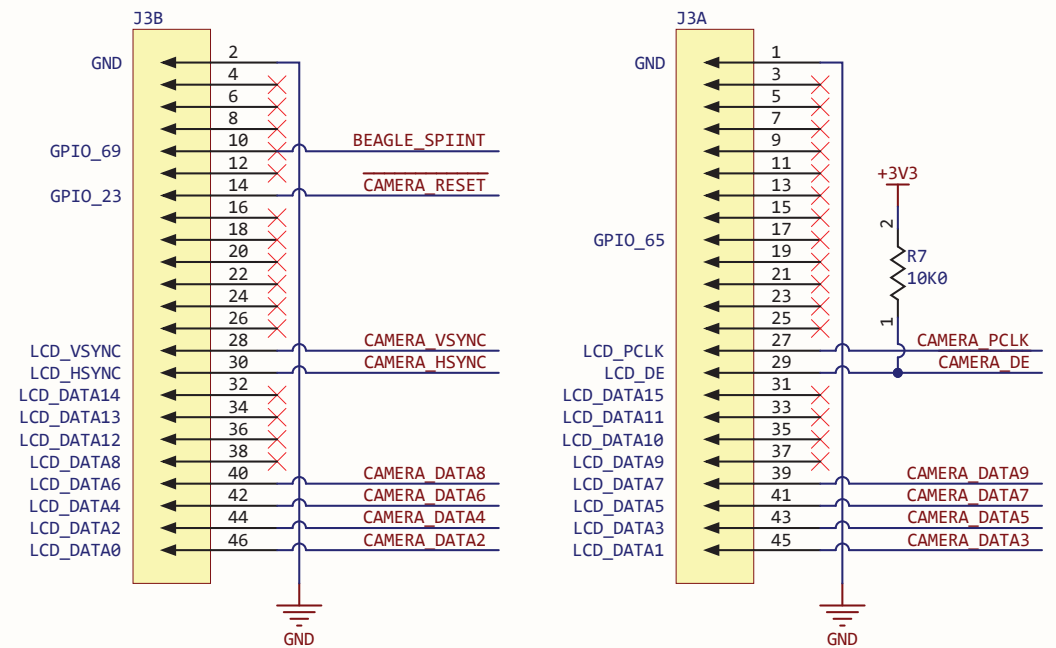
ORGANIZATION UNIVERSITY OF FLORIDA
IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI
REVIEWER MIKE STAPLETON

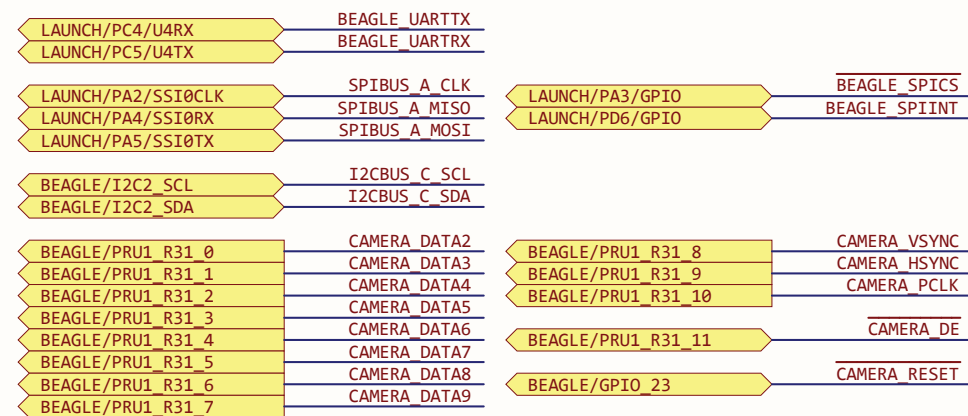
(1) BEAGLEBONE P9



(1) BEAGLEBONE P8



(1) GLOBAL NETS



NOTES

```
[01] LINK TO BEAGLEBONE BLACK DESIGN FILES: https://github.com/beagleboard/beaglebone-black
[02] VEXT PROVIDES +5V0 FROM THE BARREL JACK CONNECTOR ON THE BEAGLEBONE
[03] PIN ORDERING IS FLIPPED WRT OFFICIAL BEAGLEBONE PIN NUMBERS (I.E. EVEN PINS ARE ODD HERE AND ODD PINS ARE EVEN HERE)
```



TITLE	BEAGLEBONE BLACK HEADERS
--------------	--------------------------

SIZE
B

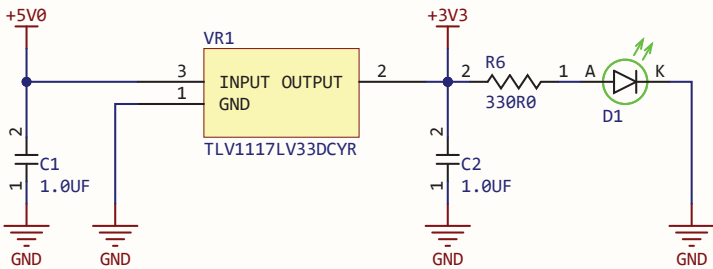
PROJECT	MULTIMOD BASE
---------	---------------

REVISION
1

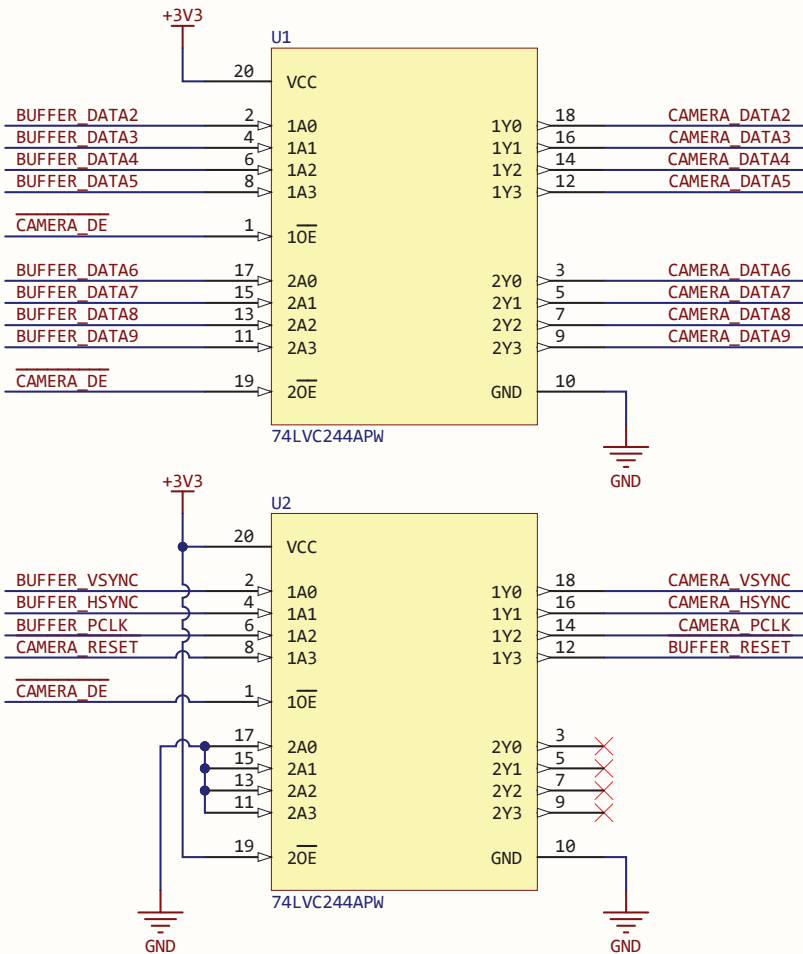
ORGANIZATION	UNIVERSITY OF FLORIDA
IDENTIFIER	SUMMER 2023

DESIGNER	MEHRON TALEBI
REVIEWER	MIKE STAPLETON

(1) +3V3 VOLTAGE REGULATOR

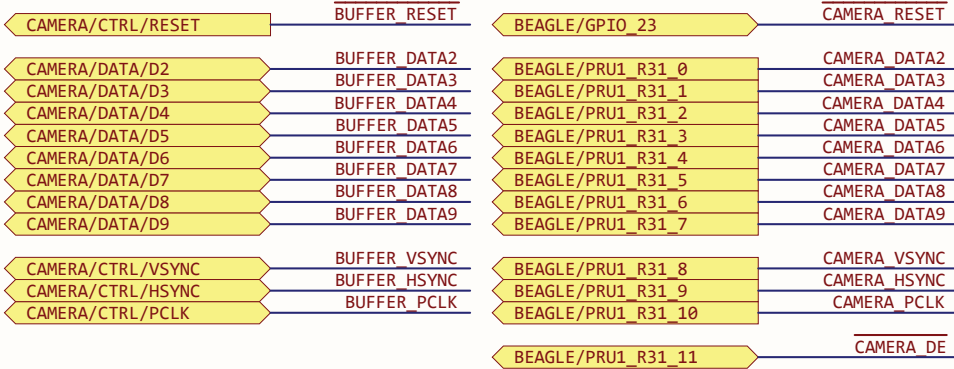


(2) CAMERA I/O BUFFER



(1) GLOBAL NETS

(2) GLOBAL NETS



NOTES
N/A



TITLE BEAGLEBONE BLACK INTERFACES

SIZE
B

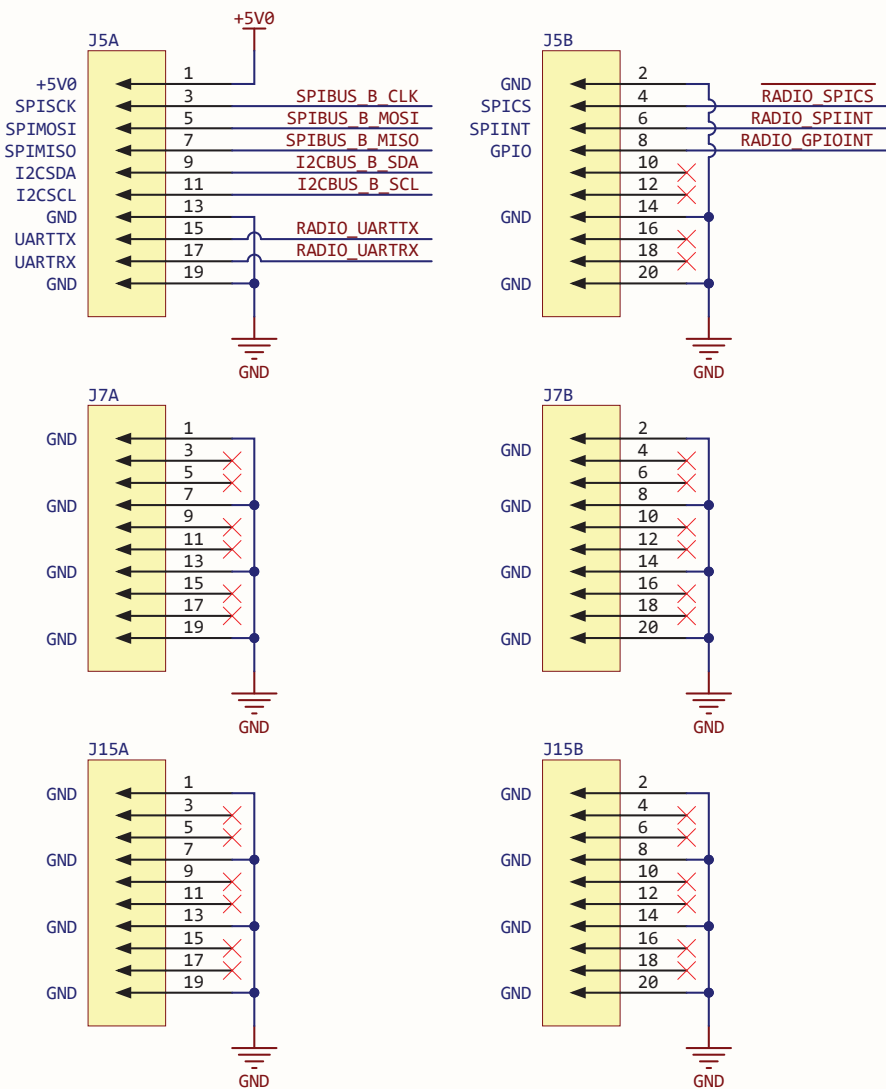
PROJECT
MULTIMOD BASE

REVISION
1

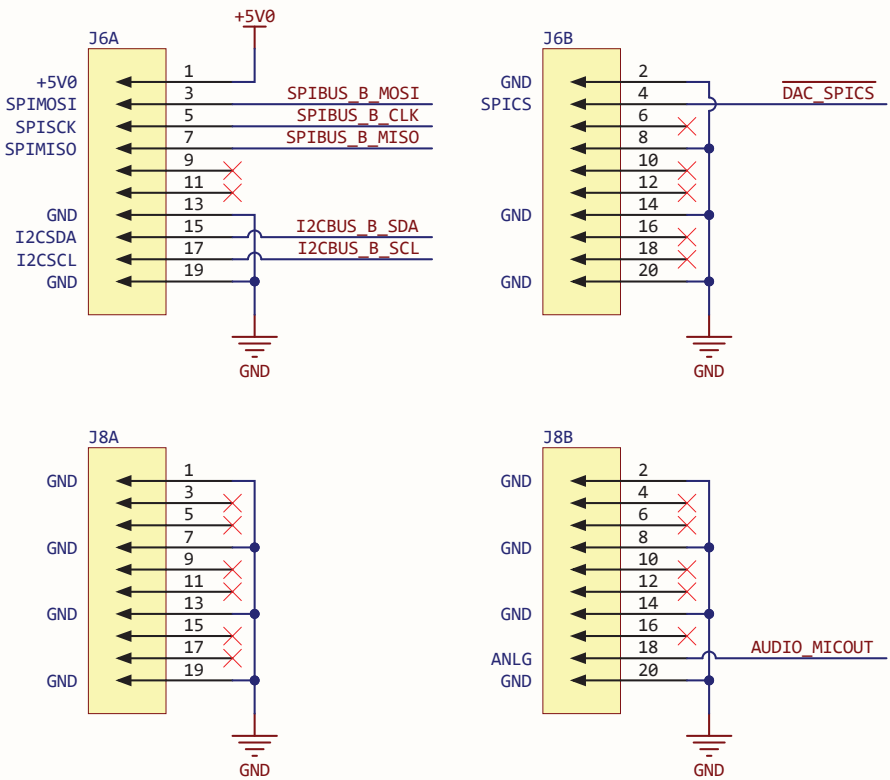
ORGANIZATION UNIVERSITY OF FLORIDA
IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI
REVIEWER MIKE STAPLETON

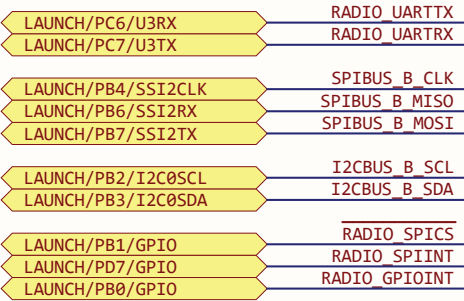
(1) RADIO MODULE



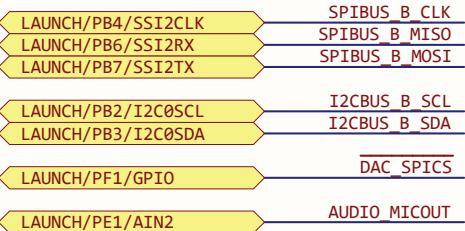
(2) AUDIO MODULE



(1) GLOBAL NETS



(2) GLOBAL NETS



NOTES
N/A



TITLE RADIO/AUDIO MODULE INTERFACES

SIZE
B

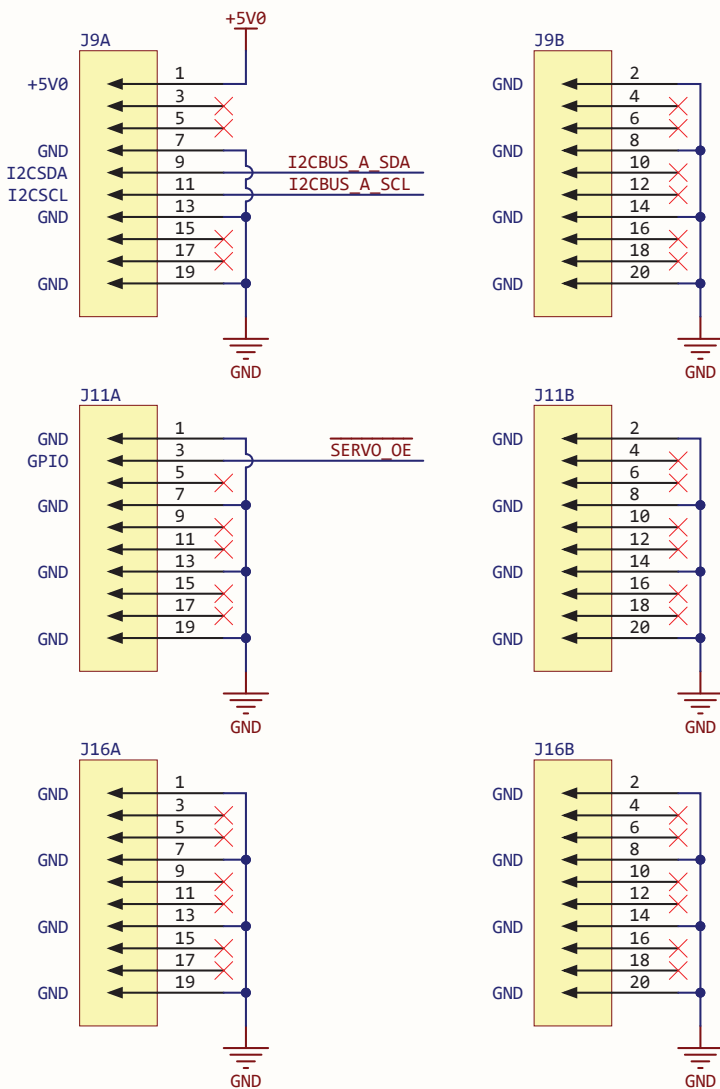
PROJECT
MULTIMOD BASE

REVISION
1

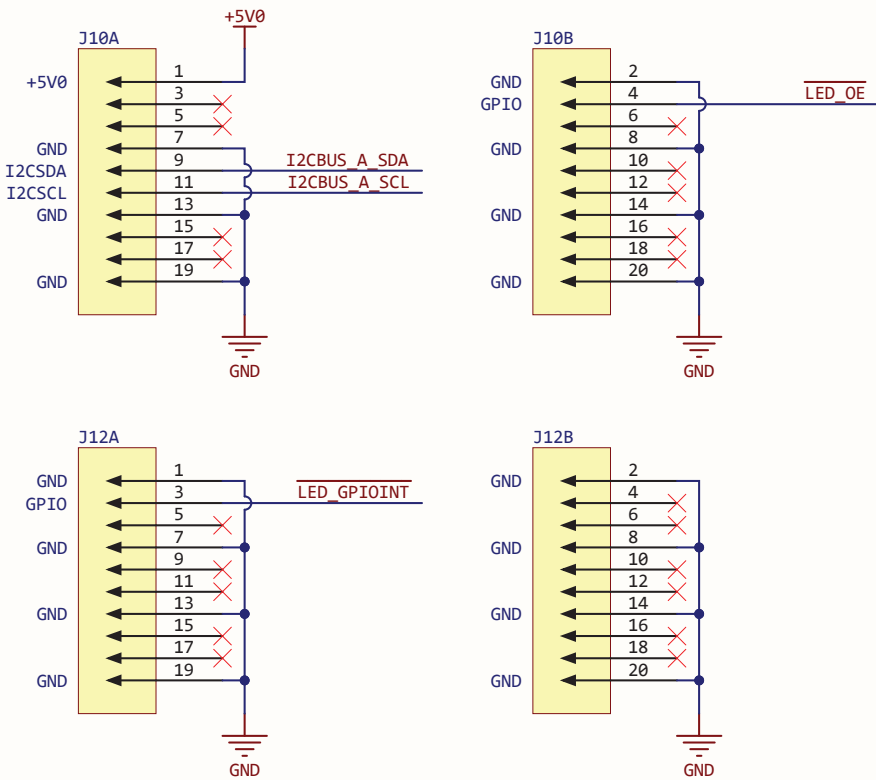
ORGANIZATION UNIVERSITY OF FLORIDA
IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI
REVIEWER MIKE STAPLETON

(1) SERVO MODULE



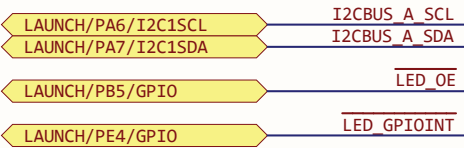
(2) LED MODULE



(1) GLOBAL NETS



(2) GLOBAL NETS



NOTES
N/A



TITLE SERVO/LED MODULE INTERFACES

SIZE
B

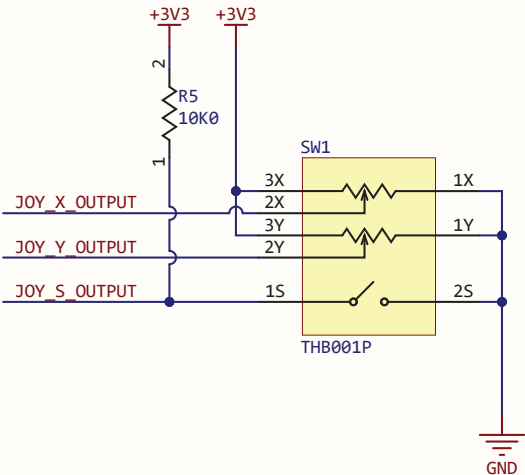
PROJECT
MULTIMOD BASE

REVISION
1

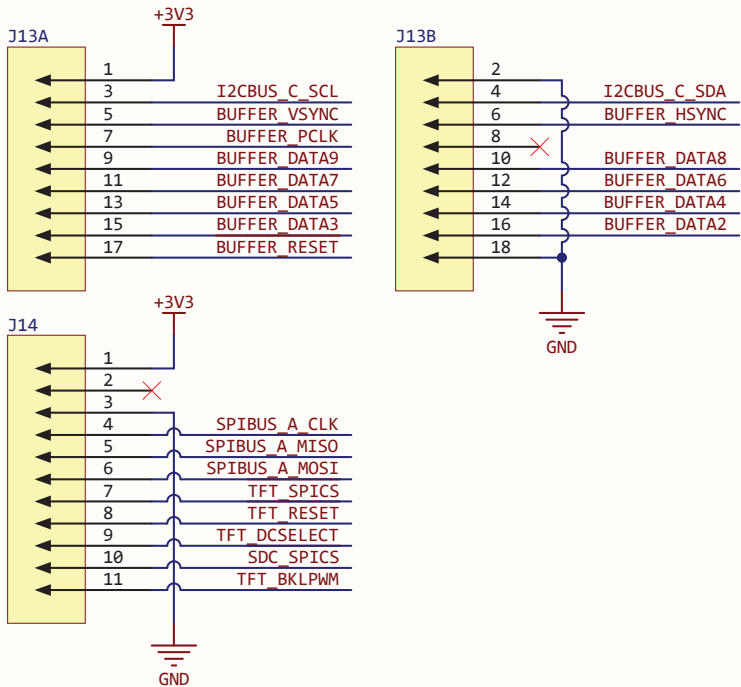
ORGANIZATION UNIVERSITY OF FLORIDA
IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI
REVIEWER MIKE STAPLETON

(1) JOYSTICKS



(2) CAMERA/TFT



(1) GLOBAL NETS

LAUNCH/PE3/AIN0	JOY_X_OUTPUT
LAUNCH/PE2/AIN1	JOY_Y_OUTPUT
LAUNCH/PD2/GPIO	JOY_S_OUTPUT

(2) GLOBAL NETS

CAMERA/CTRL/RESET	BUFFER_RESET	LAUNCH/PE0/GPIO	TFT_RESET
CAMERA/DATA/D2	BUFFER_DATA2	LAUNCH/PF3/GPIO	TFT_DCSELECT
CAMERA/DATA/D3	BUFFER_DATA3	LAUNCH/PF2/M1PWM6	TFT_BKLPWM
CAMERA/DATA/D4	BUFFER_DATA4	LAUNCH/PA2/SSI0CLK	SPIBUS_A_CLK
CAMERA/DATA/D5	BUFFER_DATA5	LAUNCH/PA4/SSI0RX	SPIBUS_A_MISO
CAMERA/DATA/D6	BUFFER_DATA6	LAUNCH/PA5/SSI0TX	SPIBUS_A_MOSI
CAMERA/DATA/D7	BUFFER_DATA7	LAUNCH/PF4/GPIO	TFT_SPICS
CAMERA/DATA/D8	BUFFER_DATA8	LAUNCH/PD3/GPIO	SDC_SPICS
CAMERA/DATA/D9	BUFFER_DATA9		
CAMERA/CTRL/VSYNC	BUFFER_VSYNC		
CAMERA/CTRL/HSYNC	BUFFER_HSYNC		
CAMERA/CTRL/PCLK	BUFFER_PCLK		
BEAGLE/I2C2_SCL	I2CBUS_C_SCL		
BEAGLE/I2C2_SDA	I2CBUS_C_SDA		

NOTES
N/A



TITLE JOYSTICK/CAMERA/TFT PERIPHERAL INTERFACES

SIZE B	PROJECT MULTIMOD BASE	REVISION 1
ORGANIZATION UNIVERSITY OF FLORIDA	DESIGNER MEHRON TALEBI	
IDENTIFIER SUMMER 2023	REVIEWER MIKE STAPLETON	

1		2		3		4		5		6	
A	INDEX	SCHEMATIC	DESCRIPTION								
	01	S01-INTERFACES	AUDIO MODULE INTERFACES AND GPIO EXPANSION								
	02	S02-POWER	POWER SUPPLY AND ANALOG VOLTAGE REFERENCE								
	03	S03-MICROPHONE	MICROPHONE AND ADJUSTABLE GAIN AMPLIFIER								
B	04	S04-SPEAKER	DAC AND SPEAKER OUTPUT DRIVER								
C											
D											

SYSTEM DIAGRAM

```
graph TD; MI[MODULE INTERFACE] --> PS[POWER SUPPLY]; MI --> DAC[DIGITAL-TO-ANALOG CONVERTER]; MI --> VR[VOLTAGE REFERENCE]; MI --> MA[MICROPHONE AMPLIFIER]; PS --> SA[SPEAKER AMPLIFIER]; DAC --> SA; VR --> SA; SA --> SI[SPEAKER INTERFACE]; EMI[ELECTRET MICROPHONE] --> MA;
```

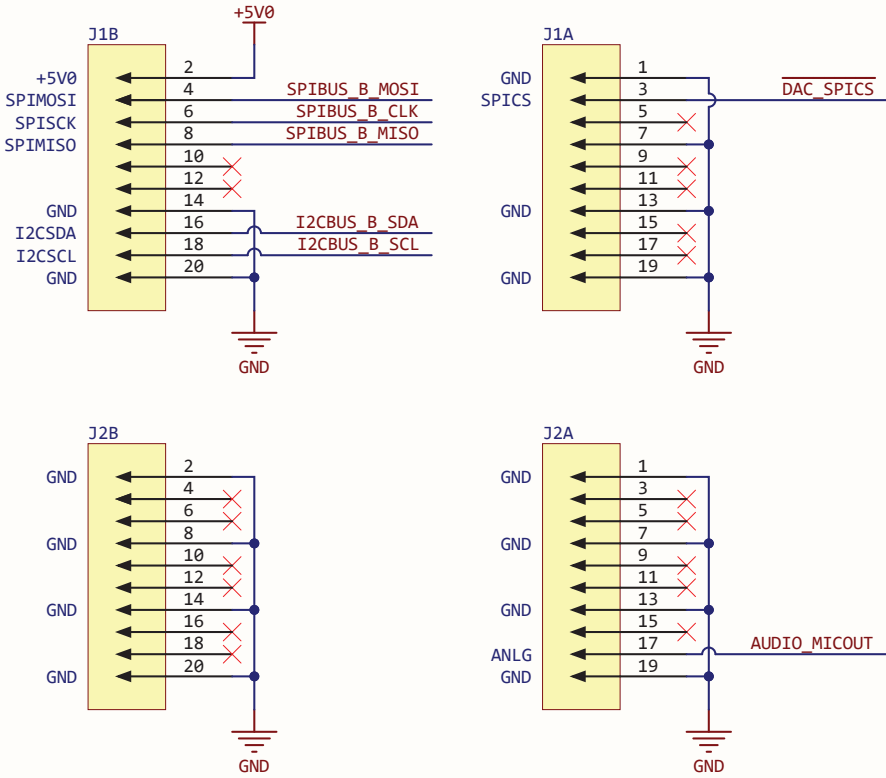
The diagram illustrates the system architecture of the Multimod Audio Module. At the base is the 'MODULE INTERFACE' (red box), which connects to four main functional blocks: 'POWER SUPPLY', 'DIGITAL-TO-ANALOG CONVERTER', 'VOLTAGE REFERENCE', and 'MICROPHONE AMPLIFIER' (all purple boxes). The 'POWER SUPPLY', 'DIGITAL-TO-ANALOG CONVERTER', and 'VOLTAGE REFERENCE' blocks all feed into the 'SPEAKER AMPLIFIER' (purple box). The output of the 'SPEAKER AMPLIFIER' is connected to the 'SPEAKER INTERFACE' (blue box). Additionally, an 'ELECTRET MICROPHONE' (purple box) is connected to the 'MICROPHONE AMPLIFIER'.

NOTES			
[1] CONNECTION TYPES AND IMPLEMENTATION DETAILS HAVE BEEN OMITTED FROM THE SYSTEM DIAGRAM			

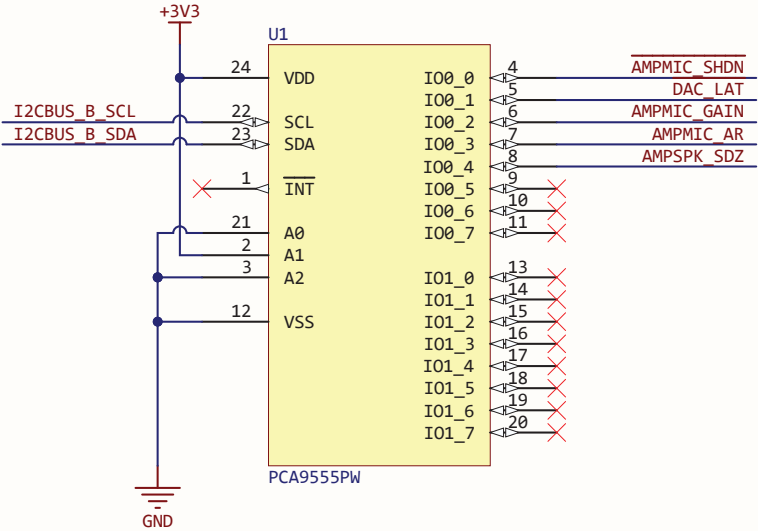
TITLE OVERVIEW AND TITLE PAGE			
SIZE B	PROJECT MULTIMOD AUDIO MODULE		REVISION 1
ORGANIZATION	UNIVERSITY OF FLORIDA	DESIGNER	MEHRON TALEBI
IDENTIFIER	SUMMER 2023	REVIEWER	MIKE STAPLETON

1		2		3		4		5		6	
---	--	---	--	---	--	---	--	---	--	---	--

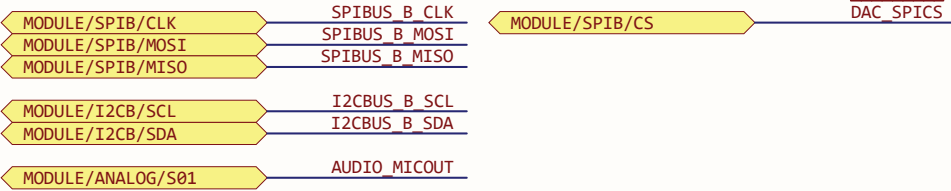
(1) MODULE INTERFACE



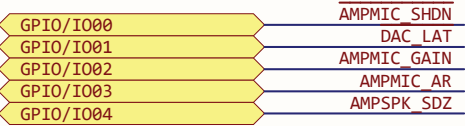
(2) GPIO/SPEAKER OUTPUT



(1) GLOBAL NETS



(2) GLOBAL NETS



NOTES

[1] 7-BIT GPIO EXPANDER I2C ADDRESS (P.6 OF PCA9555PW DATASHEET REV 08) = 0x22



TITLE AUDIO MODULE INTERFACES AND GPIO EXPANSION

SIZE B

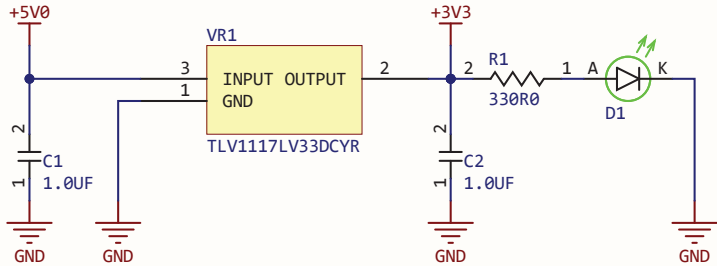
PROJECT MULTIMOD AUDIO MODULE

REVISION 1

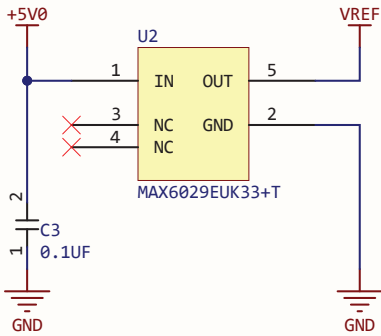
ORGANIZATION UNIVERSITY OF FLORIDA IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI REVIEWER MIKE STAPLETON

(1) +3V3 VOLTAGE REGULATOR



(2) +3V3 VOLTAGE REFERENCE



(1) GLOBAL NETS

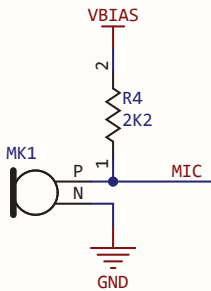
(2) GLOBAL NETS

NOTES
N/A

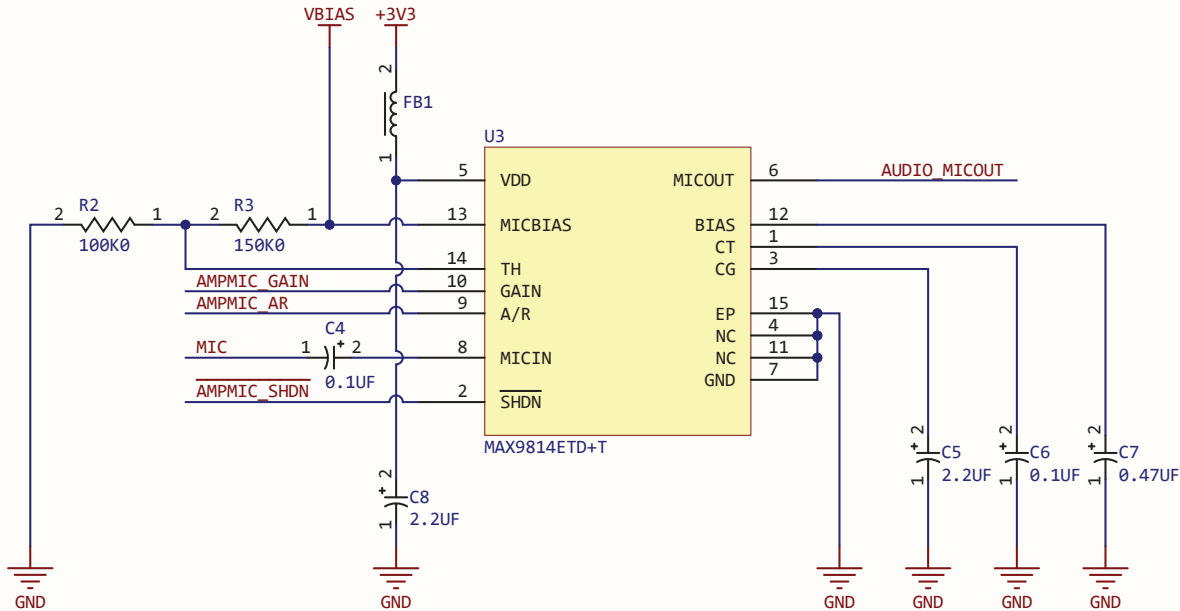


TITLE POWER SUPPLY AND ANALOG VOLTAGE REFERENCE			
SIZE B	PROJECT MULTIMOD AUDIO MODULE	REVISION 1	
ORGANIZATION UNIVERSITY OF FLORIDA	DESIGNER MEHRON TALEBI		
IDENTIFIER SUMMER 2023	REVIEWER MIKE STAPLETON		

(1) ELECTRET MICROPHONE



(1) MICROPHONE AMPLIFIER



(1) GLOBAL NETS

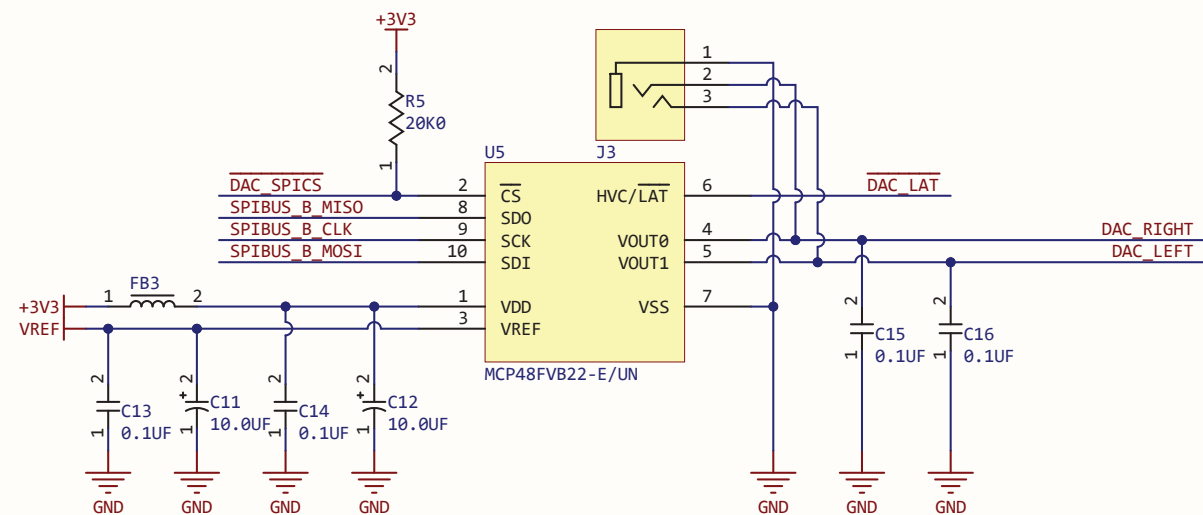
GPIO/I000	AMPMIC_SHDN
GPIO/I002	AMPMIC_GAIN
GPIO/I003	AMPMIC_AR
MODULE/ANALOG/S01	AUDIO_MICOUT

NOTES
N/A

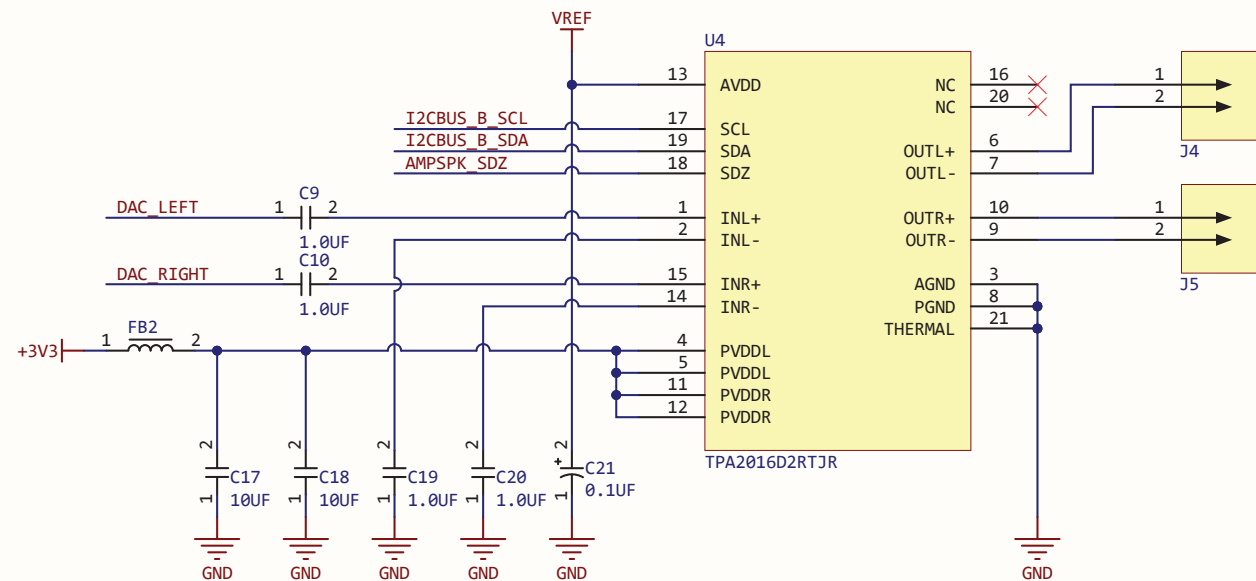


TITLE MICROPHONE AND ADJUSTABLE GAIN AMPLIFIER		
SIZE B	PROJECT MULTIMOD AUDIO MODULE	REVISION 1
ORGANIZATION UNIVERSITY OF FLORIDA	DESIGNER MEHRON TALEBI	
IDENTIFIER SUMMER 2023	REVIEWER MIKE STAPLETON	

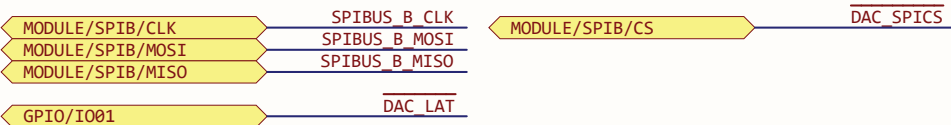
(1) DIGITAL-TO-ANALOG CONVERTER



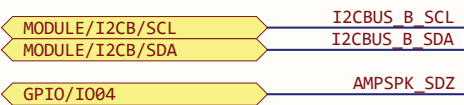
(2) CLASS-D AMPLIFIER



(1) GLOBAL NETS



(2) GLOBAL NETS



NOTES

- [1] BARREL JACK ON BEAGLEBONE SHOULD BE CONNECTED TO EFFECTIVELY DRIVE SPEAKER AMPLIFIER
[2] CLASS-D AMPLIFIER I2C ADDRESS (P.24 OF TPA2016D2RTJR DATASHEET) = 0x58



TITLE DAC AND SPEAKER OUTPUT DRIVER

SIZE
B

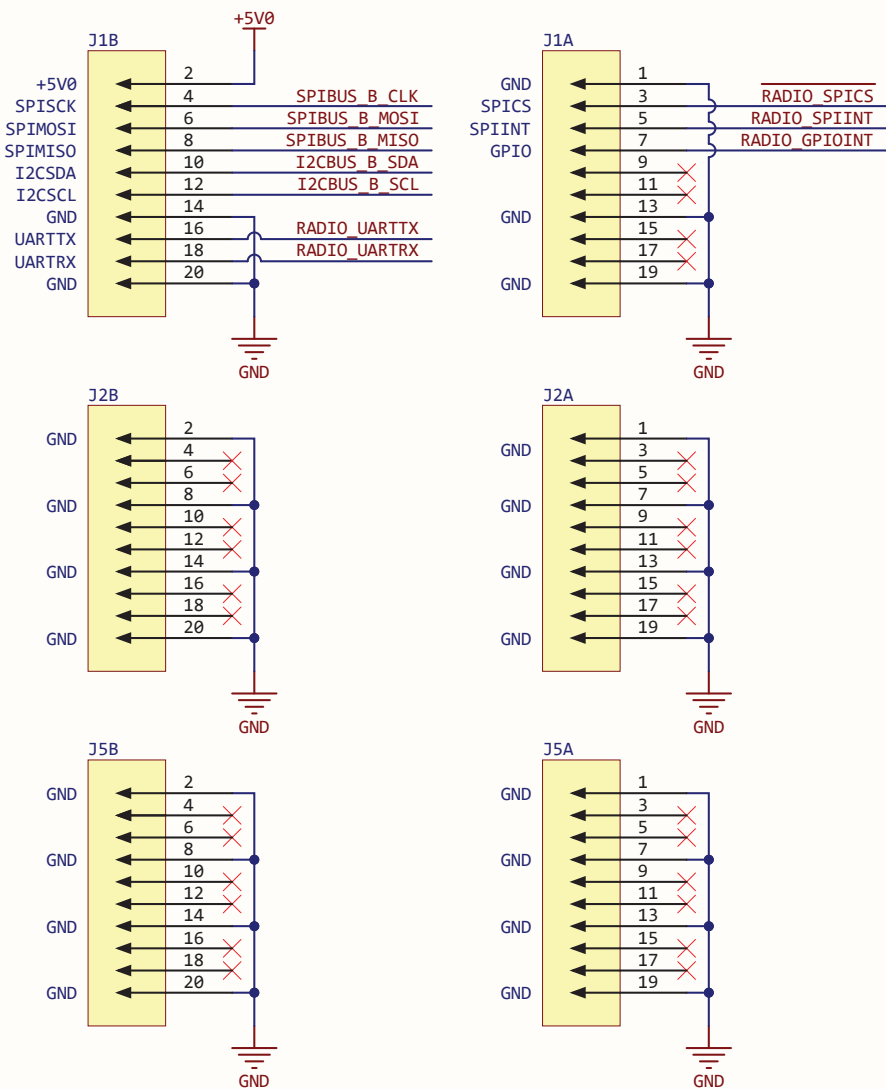
PROJECT
MULTIMOD AUDIO MODULE

REVISION
1

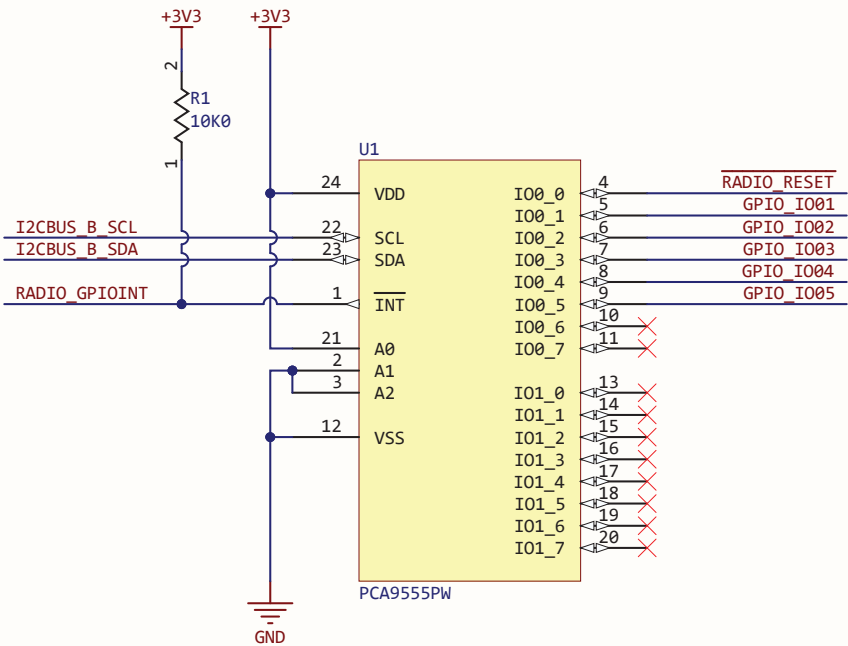
ORGANIZATION UNIVERSITY OF FLORIDA
IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI
REVIEWER MIKE STAPLETON

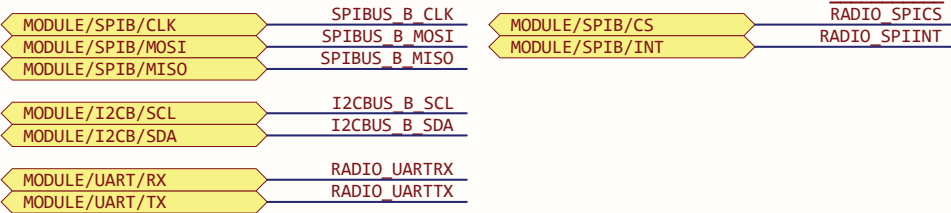
(1) MODULE INTERFACE



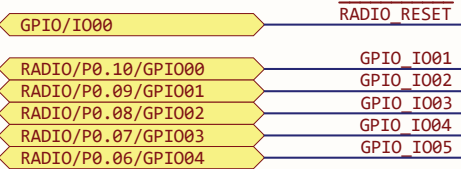
(1) GPIO INTERFACE



(1) GLOBAL NETS



(1) GLOBAL NETS



NOTES

[1] 7-BIT GPIO EXPANDER I2C ADDRESS (P.6 OF PCA9555PW DATASHEET REV 08) = 0x21



TITLE RADIO MODULE INTERFACES AND GPIO EXPANSION

SIZE
B

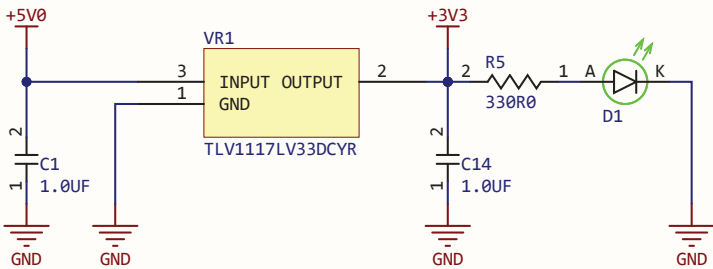
PROJECT
MULTIMOD RADIO MODULE

REVISION
1

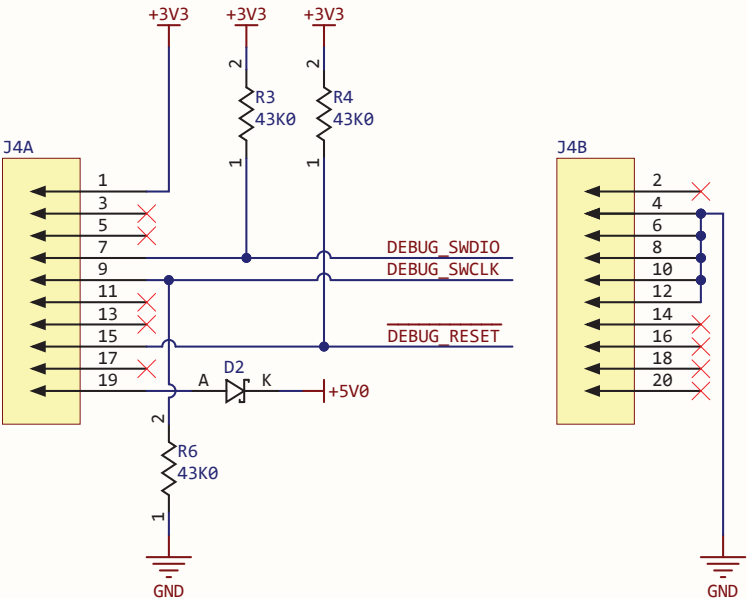
ORGANIZATION UNIVERSITY OF FLORIDA
IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI
REVIEWER MIKE STAPLETON

(1) +3V3 VOLTAGE REGULATOR

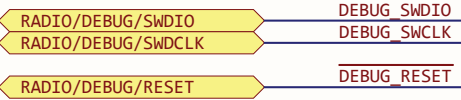


(2) DEBUG INTERFACE



(1) GLOBAL NETS

(2) DEBUG INTERFACE



NOTES
N/A



TITLE TRANSCEIVER SERIAL WIRE DEBUG PORT

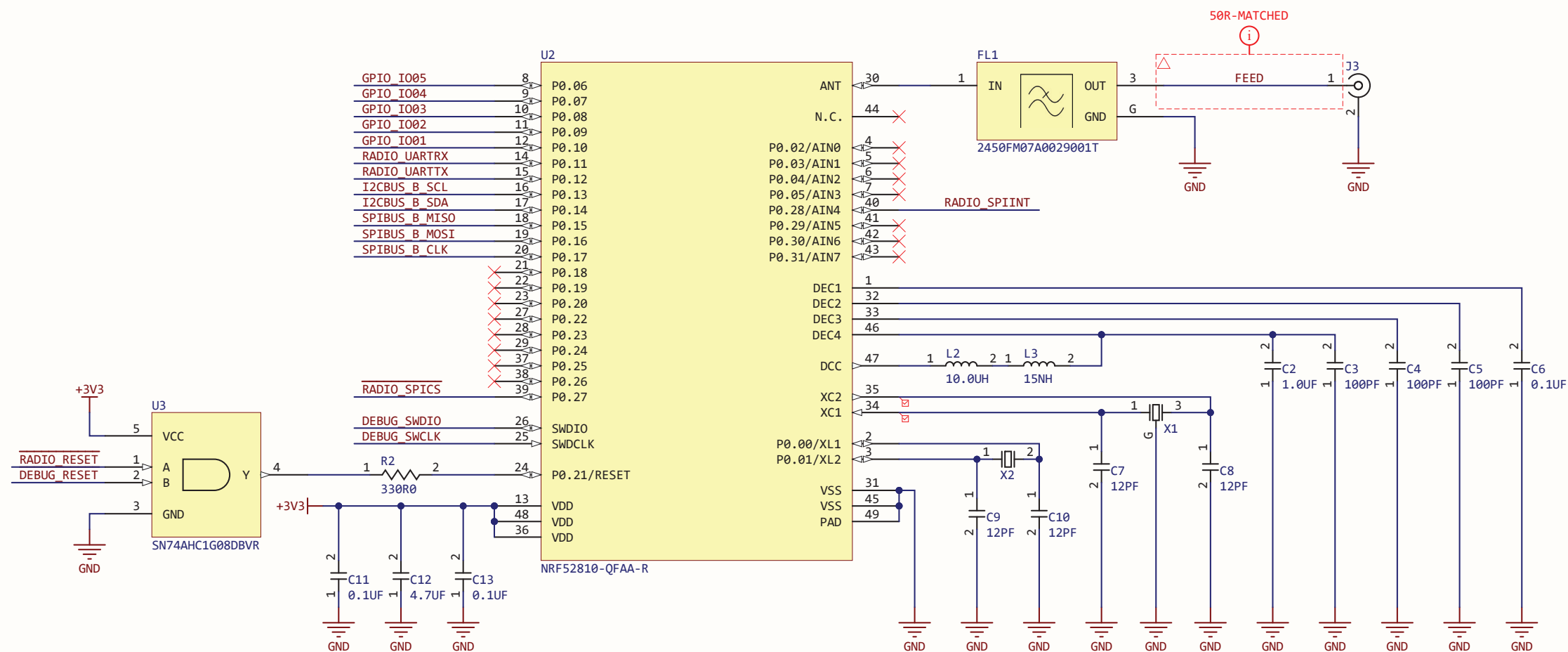
SIZE B PROJECT MULTIMOD RADIO MODULE

REVISION 1

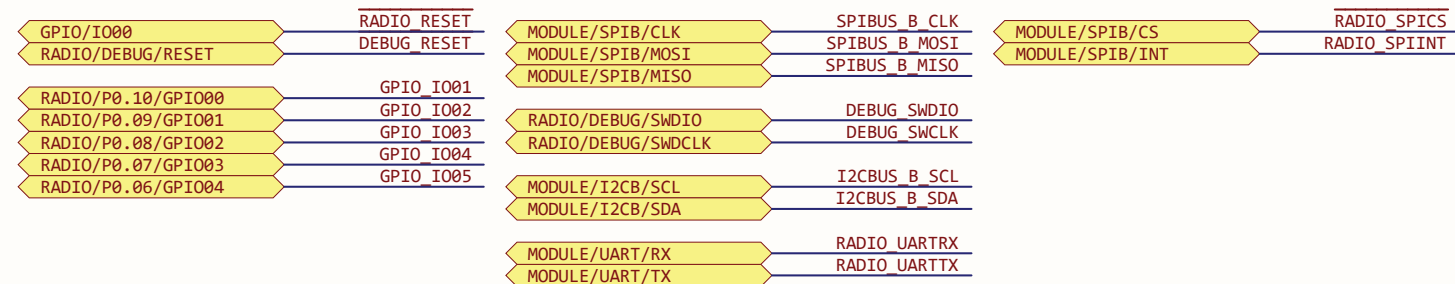
ORGANIZATION UNIVERSITY OF FLORIDA IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI REVIEWER MIKE STAPLETON

(1) BLUETOOTH TRANSCEIVER



(1) GLOBAL NETS



NOTES

[01] LINK TO NRF CONNECT SDK: <https://www.nordicsemi.com/Products/Development-software/nRF-Connect-SDK>
[02] LINK TO NRF5 SDK: <https://www.nordicsemi.com/Products/Development-software/nrf5-sdk>
[03] LINK TO NRF COMMAND LINE TOOLS: <https://www.nordicsemi.com/Products/Development-tools/nRF-Command-Line-Tools>
[04] LINK TO DATASHEET: https://infocenter.nordicsemi.com/pdf/nRF52810_PS_v1.4.pdf



TITLE BLUETOOTH RADIO TRANSCEIVER

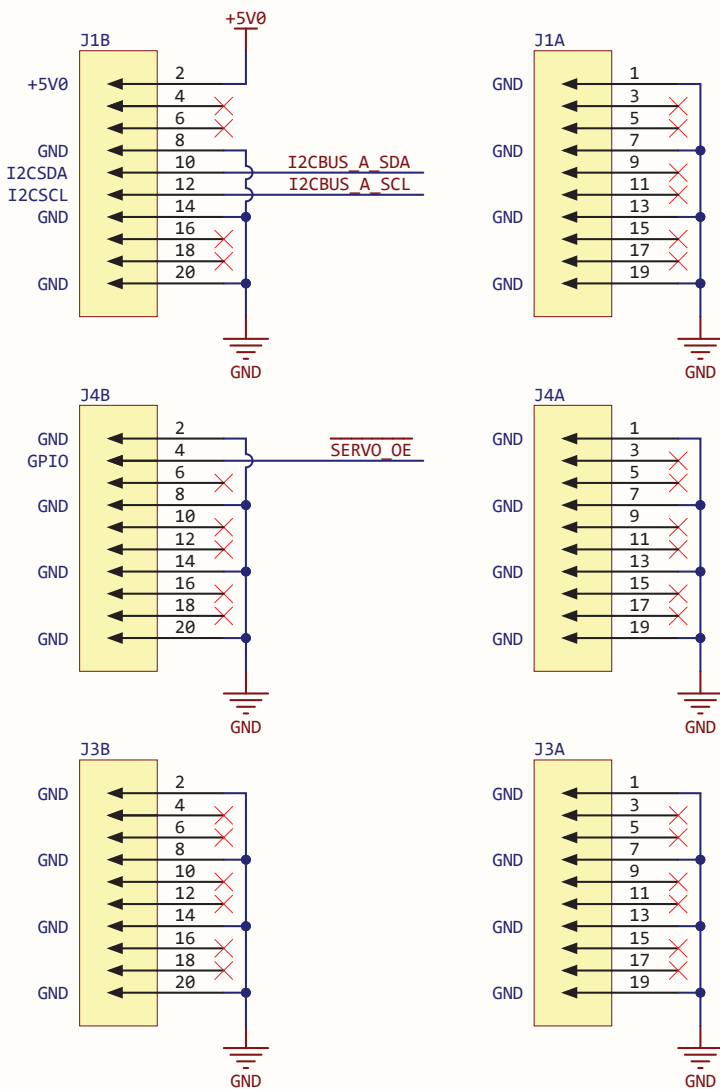
SIZE B	PROJECT MULTIMOD RADIO MODULE
-----------	----------------------------------

REVISION
1

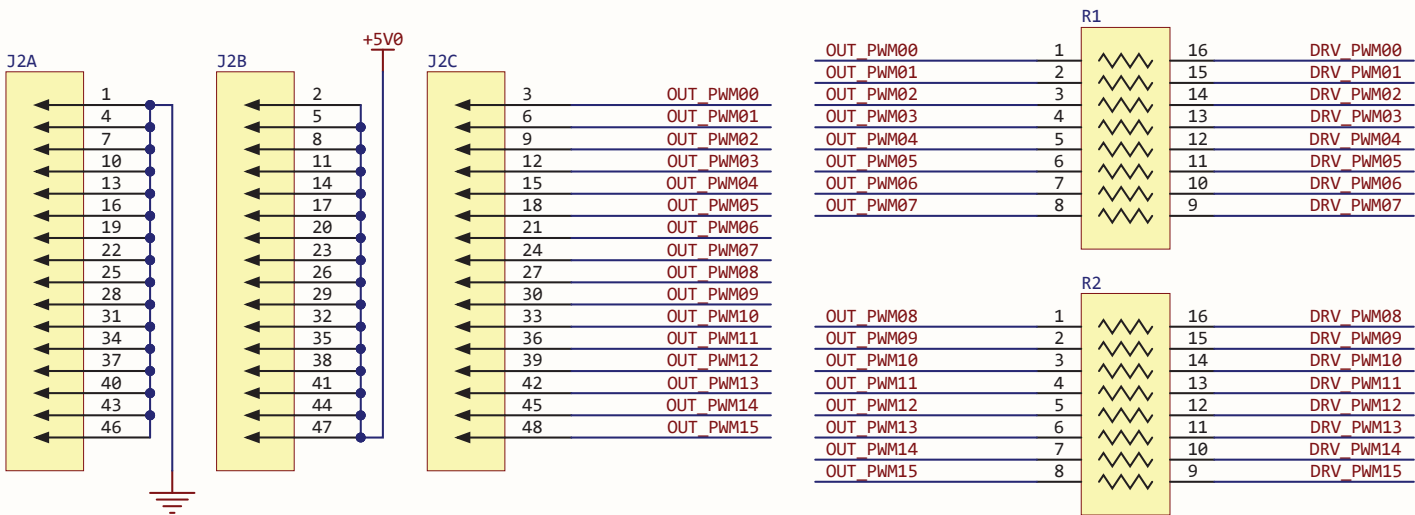
ORGANIZATION	UNIVERSITY OF FLORIDA
IDENTIFIER	SUMMER 2023

DESIGNER	MEHRON TALEBI
REVIEWER	MIKE STAPLETON

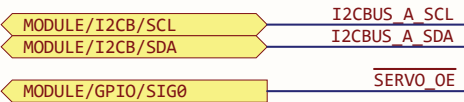
(1) MODULE INTERFACE



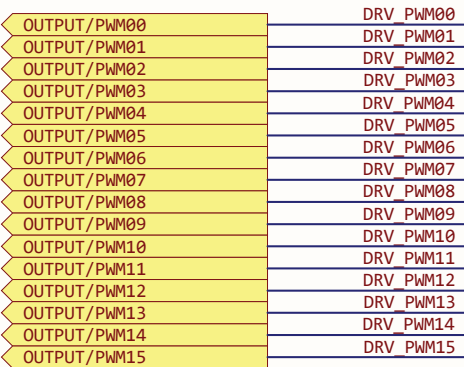
(2) SERVO INTERFACE



(1) GLOBAL NETS



(2) GLOBAL NETS



NOTES

[1] VEXT (BARREL JACK ON BEAGLEBONE) MUST BE CONNECTED TO DRIVE SERVO POWER OUTPUT



TITLE SERVO MODULE INTERFACES

SIZE B

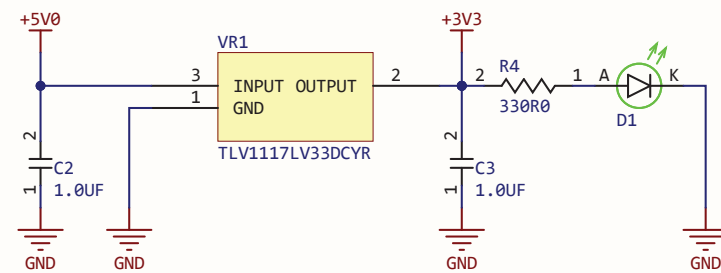
PROJECT MULTIMOD SERVO MODULE

REVISION 1

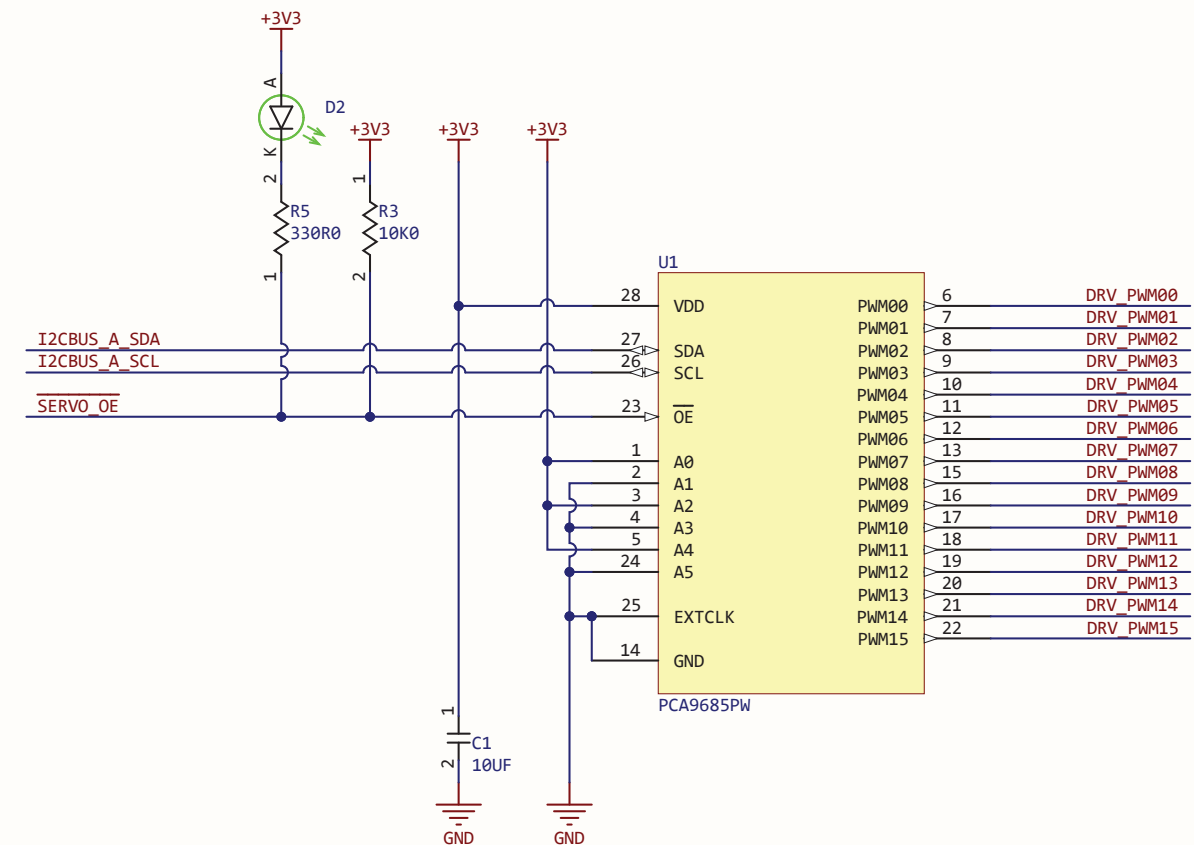
ORGANIZATION UNIVERSITY OF FLORIDA IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI REVIEWER MIKE STAPLETON

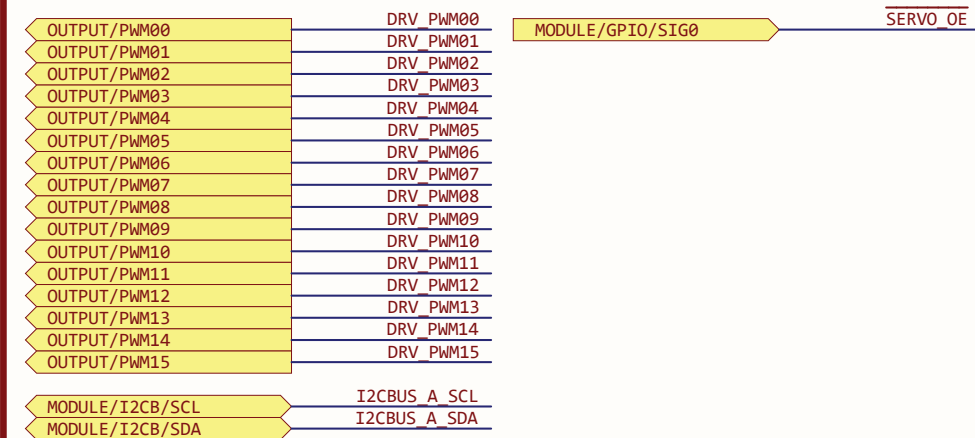
(1) +3V3 VOLTAGE REGULATOR



(2) PWM DRIVER



(1) GLOBAL NETS



NOTES

[1] PWM DRIVER I2C ADDRESS (P.8 OF PCA9685PW DATASHEET REV 4) = 0x55



TITLE PWM SIGNAL DRIVER

SIZE
B

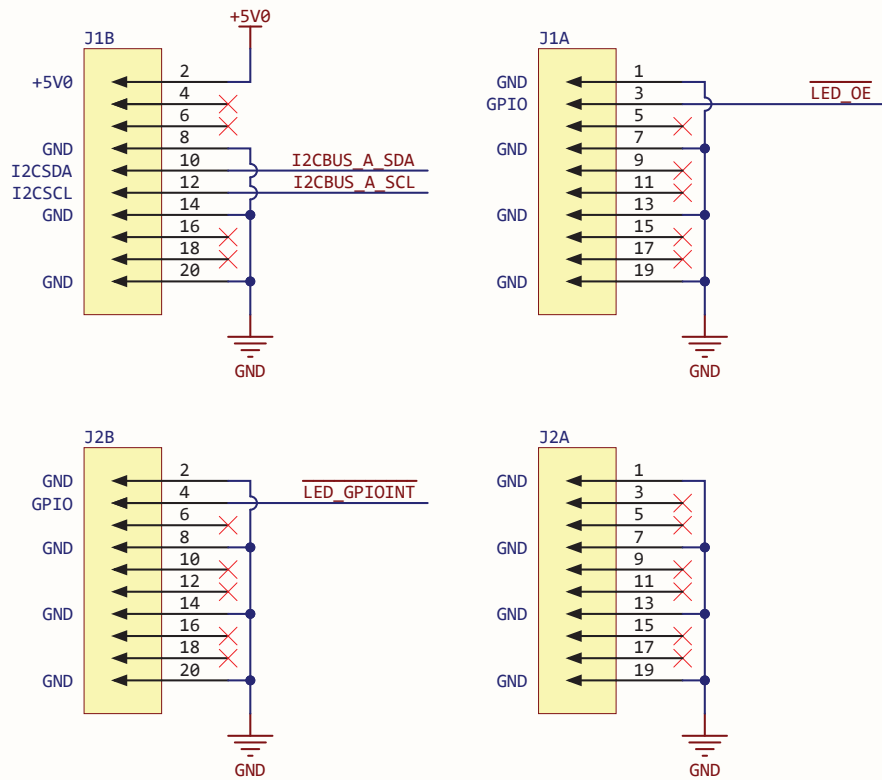
PROJECT
MULTIMOD SERVO MODULE

REVISION
1

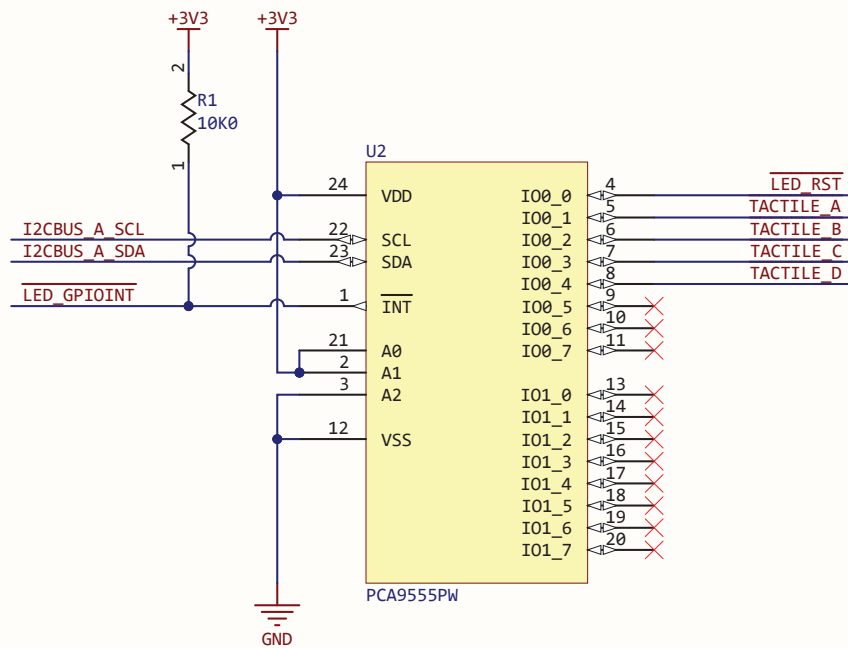
ORGANIZATION	UNIVERSITY OF FLORIDA
IDENTIFIER	SUMMER 2023

DESIGNER	MEHRON TALEBI
REVIEWER	MIKE STAPLETON

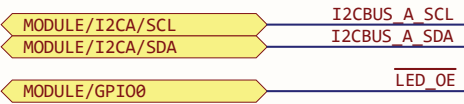
(1) MODULE INTERFACE



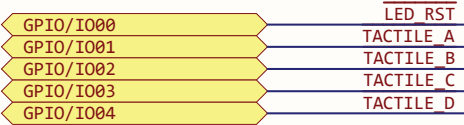
(2) GPIO INTERFACE



(1) GLOBAL NETS

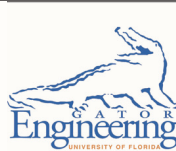


(2) GLOBAL NETS



NOTES

[1] 7-BIT GPIO EXPANDER I2C ADDRESS (P.6 OF PCA9555PW DATASHEET REV 08) = 0x23



TITLE LED MODULE INTERFACES AND TACTILE BUTTONS

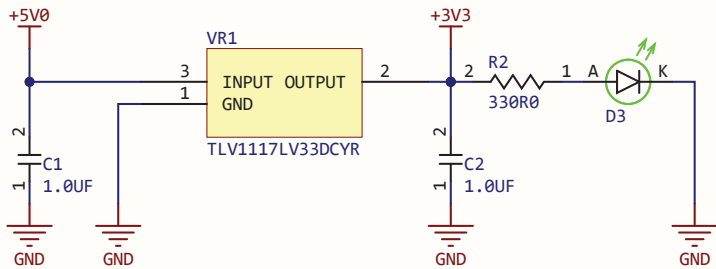
SIZE B PROJECT MULTIMOD LED MODULE

REVISION 1

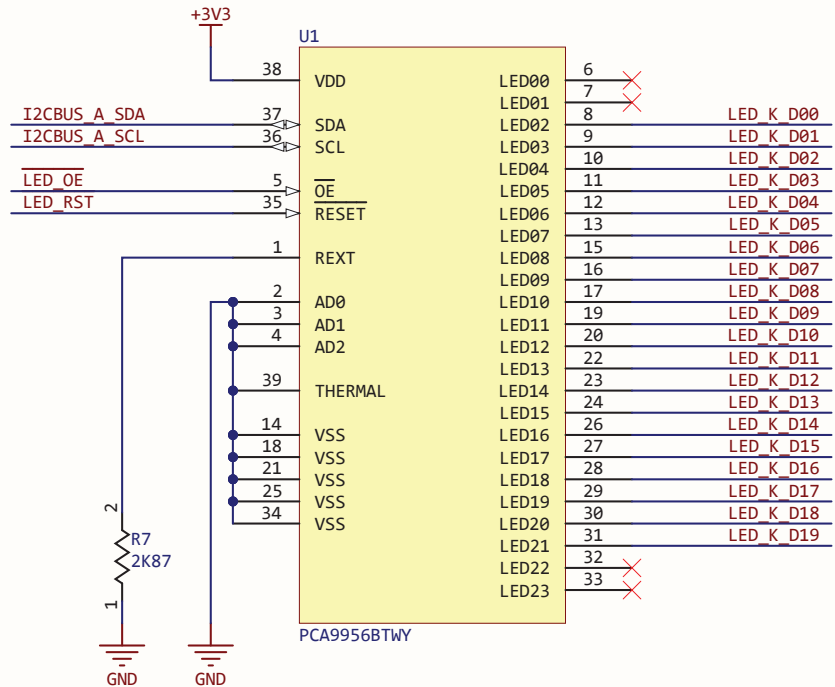
ORGANIZATION UNIVERSITY OF FLORIDA IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI REVIEWER MIKE STAPLETON

(1) +3V3 VOLTAGE REGULATOR

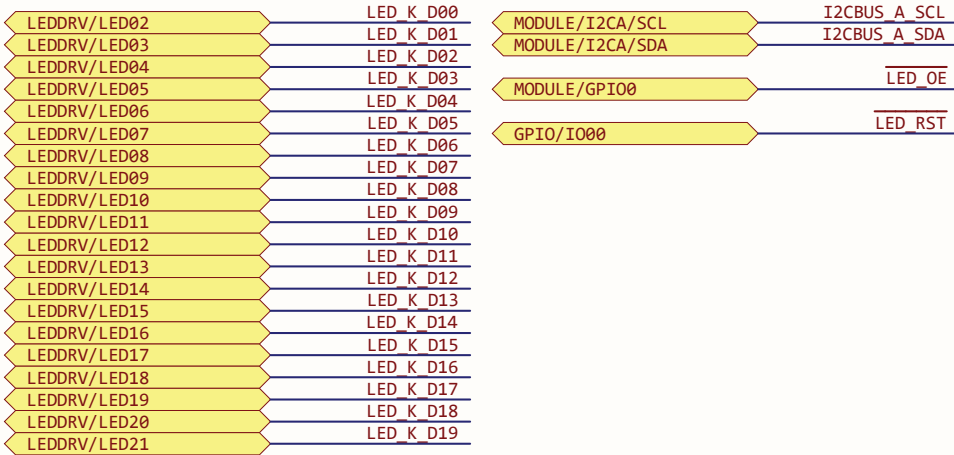


(2) CONSTANT CURRENT LED DRIVER



(1) GLOBAL NETS

(2) GLOBAL NETS



NOTES

- [1] REXT CALCULATION (P.24 OF PCA9956BTWY DATASHEET REV 1.1): $IOLED = 255 * (0.9 / 4) / REXT \Rightarrow IOLED \approx 20 \text{ MA} \Rightarrow REXT \approx 2K87$
- [2] LED DRIVER I2C ADDRESS = 0x01



TITLE LED OUTPUT DRIVER

SIZE B

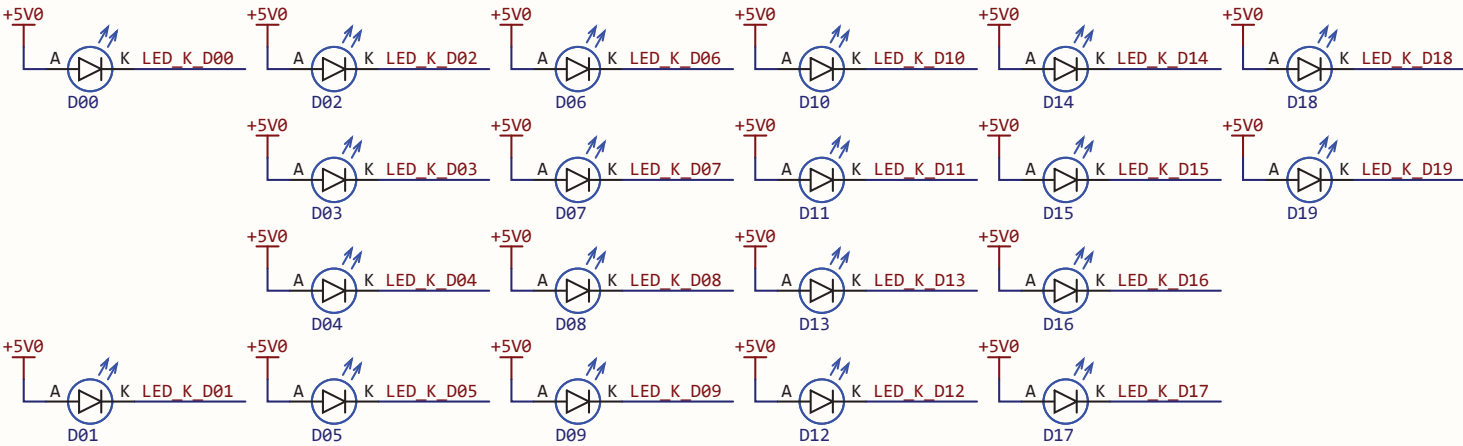
PROJECT MULTIMOD LED MODULE

REVISION 1

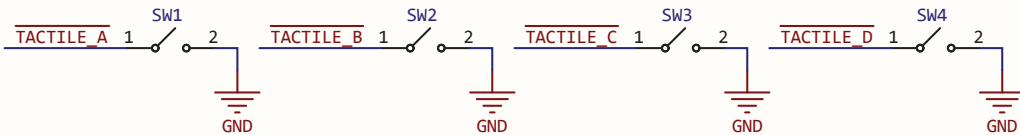
ORGANIZATION UNIVERSITY OF FLORIDA
IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI
REVIEWER MIKE STAPLETON

(1) LED ARRAY



(1) TACTILE SWITCHES



(1) GLOBAL NETS

LEDDRV/LED02	LED_K_D00
LEDDRV/LED03	LED_K_D01
LEDDRV/LED04	LED_K_D02
LEDDRV/LED05	LED_K_D03
LEDDRV/LED06	LED_K_D04
LEDDRV/LED07	LED_K_D05
LEDDRV/LED08	LED_K_D06
LEDDRV/LED09	LED_K_D07
LEDDRV/LED10	LED_K_D08
LEDDRV/LED11	LED_K_D09
LEDDRV/LED12	LED_K_D10
LEDDRV/LED13	LED_K_D11
LEDDRV/LED14	LED_K_D12
LEDDRV/LED15	LED_K_D13
LEDDRV/LED16	LED_K_D14
LEDDRV/LED17	LED_K_D15
LEDDRV/LED18	LED_K_D16
LEDDRV/LED19	LED_K_D17
LEDDRV/LED20	LED_K_D18
LEDDRV/LED21	LED_K_D19

(1) GLOBAL NETS

GPIO/I001	TACTILE_A
GPIO/I002	TACTILE_B
GPIO/I003	TACTILE_C
GPIO/I004	TACTILE_D

NOTES
N/A



TITLE LED ARRAY AND TACTILE SWITCHES

SIZE
B

PROJECT
MULTIMOD LED MODULE

REVISION
1

ORGANIZATION UNIVERSITY OF FLORIDA
IDENTIFIER SUMMER 2023

DESIGNER MEHRON TALEBI
REVIEWER MIKE STAPLETON