

Project: **Inside-DGX-XC95**

Title: **Inside-DGX-XC95 Top Level.SchDoc**

Variant:

Inside-DGX-XC95

Engineer: Sergej Bakhmach

Revision: 1

Size: A4

Sheet: 1 of 8

Company: **Those Boards**



A

A

B

B

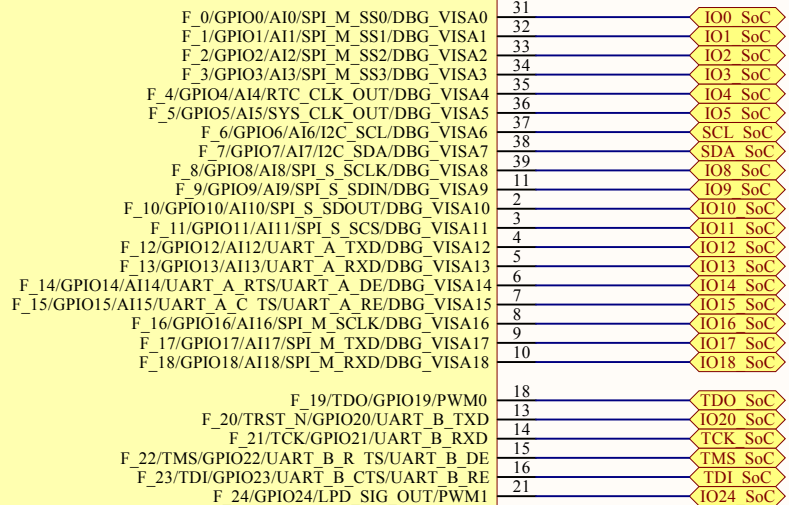
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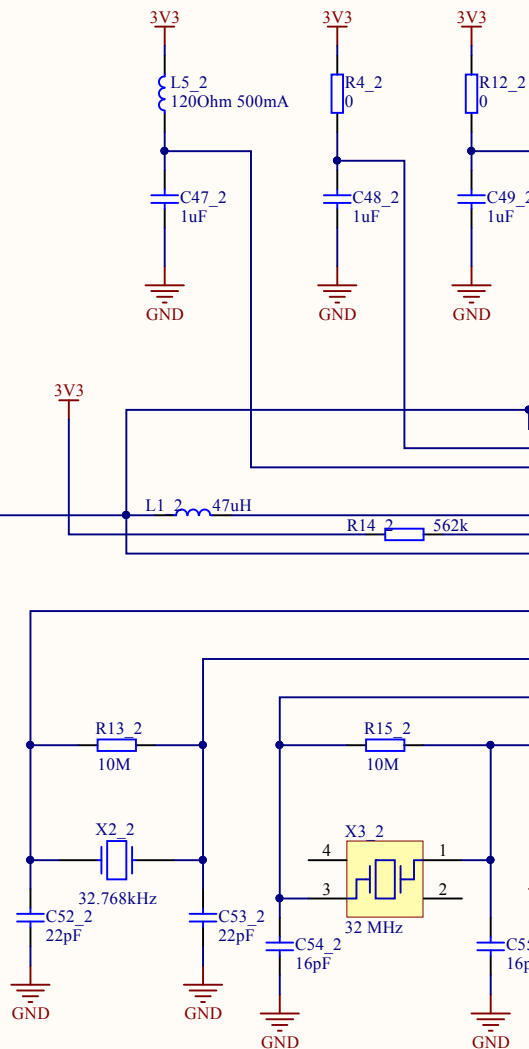
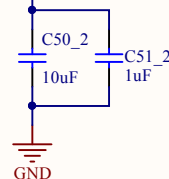
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D

U1 2A



FND2000 SR2KF

AR
RST_N

U1 2B

Pinout for U1 2B (FND2000 SR2KF):

U1 2B Pin	U1 2B Label
27	PVDD
17	DVDD
24	DVDD_2
12	IOVDD
40	AVDD
26	LX
29	VREN
28	VSENSE
25	GSENSE
22	RTC_XTALI
23	RTC_XTALO
19	HYB_XTALI
20	HYB_XTALO
41	PAD

FND2000 SR2KF

Project: **Inside-DGX-XC95**Title: **D2000.SchDoc**Variant:
Inside-DGX-XC95Engineer: **Sergej Bakhmach**Revision: **1**Size: **A4**Sheet: **2 of 8**Company: **Those Boards**

A

A

B

B

C

C

D

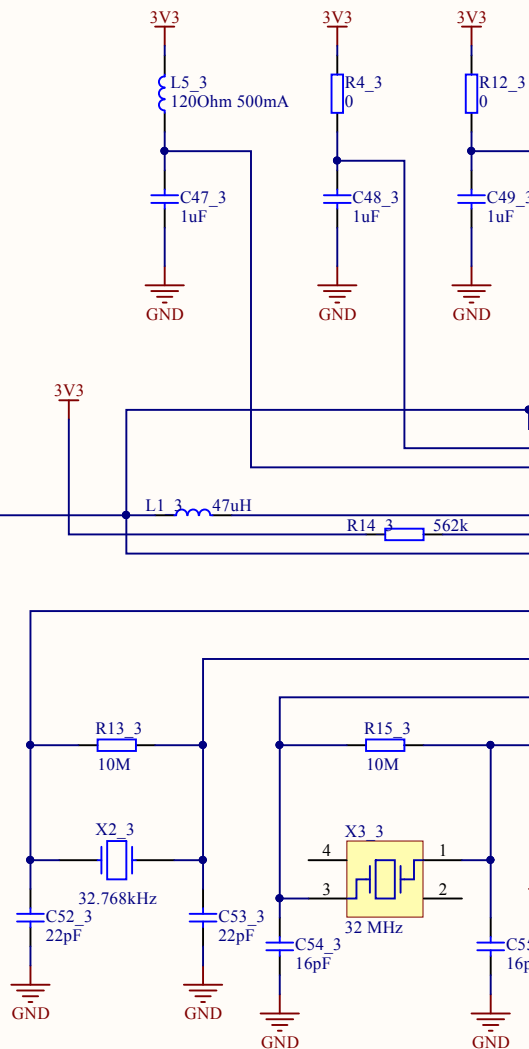
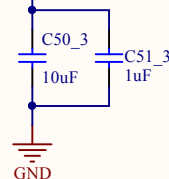
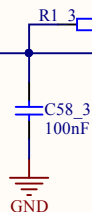
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U1 3A

F_0/GPIO0/AI0/SPI_M_SS0/DBG_VISA0	31	IO0 SoC
F_1/GPIO1/AI1/SPI_M_SS1/DBG_VISA1	32	IO1 SoC
F_2/GPIO2/AI2/SPI_M_SS2/DBG_VISA2	33	IO2 SoC
F_3/GPIO3/AI3/SPI_M_SS3/DBG_VISA3	34	IO3 SoC
F_4/GPIO4/AI4/RTC_CLK_OUT/DBG_VISA4	35	IO4 SoC
F_5/GPIO5/AI5/SYS_CLK_OUT/DBG_VISA5	36	IO5 SoC
F_6/GPIO6/AI6/I2C_SCL/DBG_VISA6	37	SCL SoC
F_7/GPIO7/AI7/I2C_SDA/DBG_VISA7	38	SDA SoC
F_8/GPIO8/AI8/SPI_S_SCLK/DBG_VISA8	39	IO8 SoC
F_9/GPIO9/AI9/SPI_S_SDIN/DBG_VISA9	11	IO9 SoC
F_10/GPIO10/AI10/SPI_S_SDOUT/DBG_VISA10	2	IO10 SoC
F_11/GPIO11/AI11/SPI_S_SCS/DBG_VISA11	3	IO11 SoC
F_12/GPIO12/AI12/UART_A_TXD/DBG_VISA12	4	IO12 SoC
F_13/GPIO13/AI13/UART_A_RXD/DBG_VISA13	5	IO13 SoC
F_14/GPIO14/AI14/UART_A_RTS/UART_A_DE/DBG_VISA14	6	IO14 SoC
F_15/GPIO15/AI15/UART_A_CTS/UART_A_RE/DBG_VISA15	7	IO15 SoC
F_16/GPIO16/AI16/SPI_M_SCLK/DBG_VISA16	8	IO16 SoC
F_17/GPIO17/AI17/SPI_M_TXD/DBG_VISA17	9	IO17 SoC
F_18/GPIO18/AI18/SPI_M_RXD/DBG_VISA18	10	IO18 SoC
F_19/TDO/GPIO19/PWM0	18	TDO SoC
F_20/TRST_N/GPIO20/UART_B_TXD	13	IO20 SoC
F_21/TCK/GPIO21/UART_B_RXD	14	TCK SoC
F_22/TMS/GPIO22/UART_B_RTS/UART_B_DE	15	TMS SoC
F_23/TDI/GPIO23/UART_B_CTS/UART_B_RE	16	TDI SoC
F_24/GPIO24/LPD_SIG_OUT/PWM1	21	IO24 SoC

AR
RST_N

FND2000 SR2KF



U1 3B

27	PVDD
17	DVDD
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FND2000 SR2KF

Project: **Inside-DGX-XC95**Title: **D2000.SchDoc**Variant:
Inside-DGX-XC95

Engineer: Sergej Bakhmach

Revision: 1

Size: A4

Sheet: 2 of 8

Company: **Those Boards**

A

A

B

B

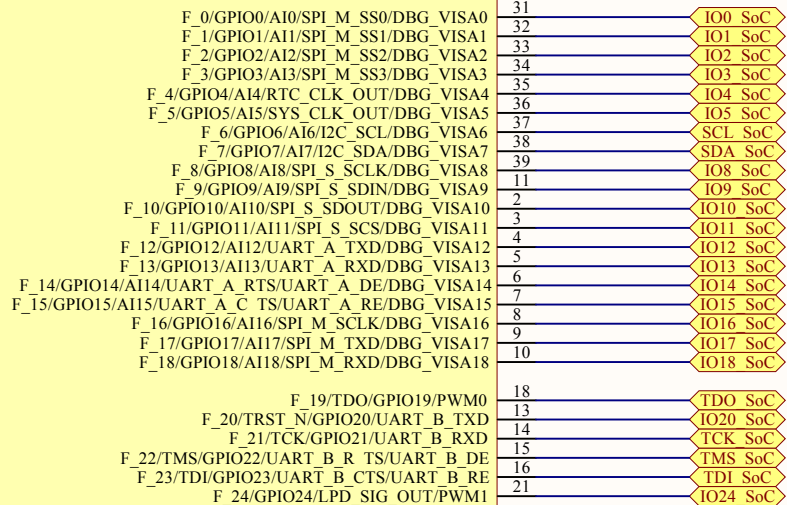
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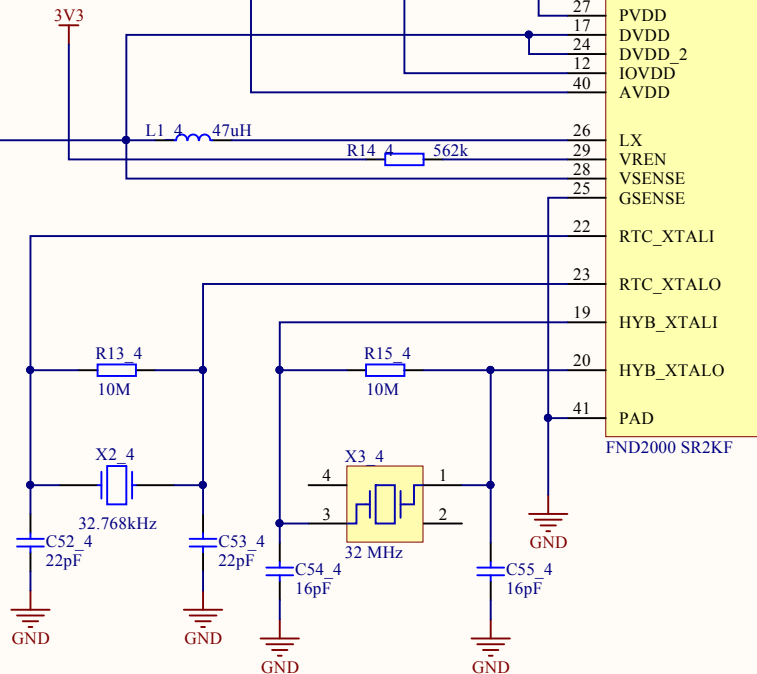
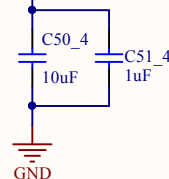
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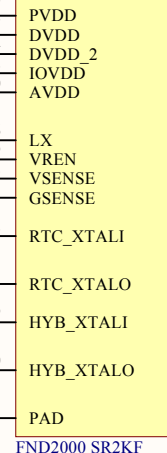
U1 4A



FND2000 SR2KF

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RST_N

U1 4B



FND2000 SR2KF

Project: **Inside-DGX-XC95**Title: **D2000.SchDoc**Variant:
Inside-DGX-XC95Engineer: **Sergej Bakhmach**Revision: **1**Size: **A4**Sheet: **2 of 8**Company: **Those Boards**

A

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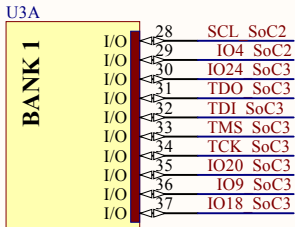
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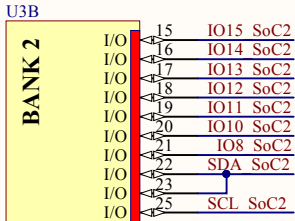
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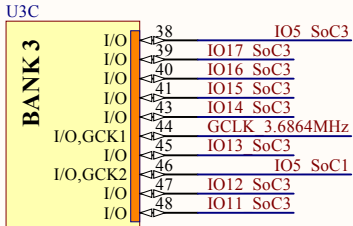
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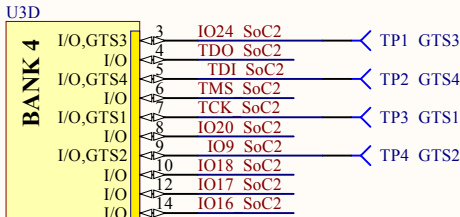
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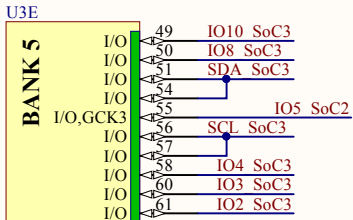
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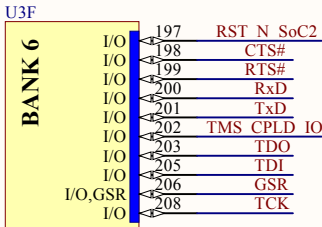
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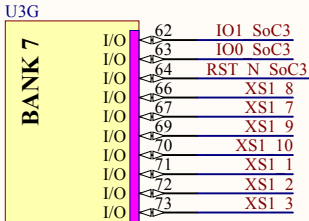
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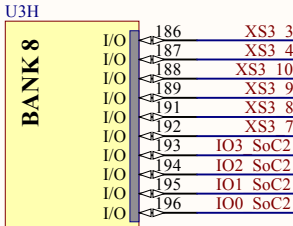
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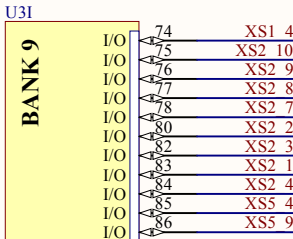
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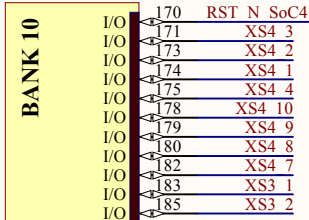
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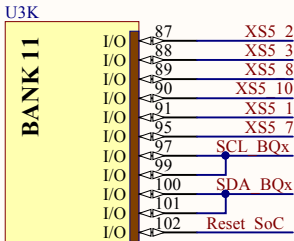
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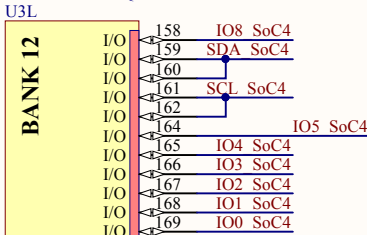
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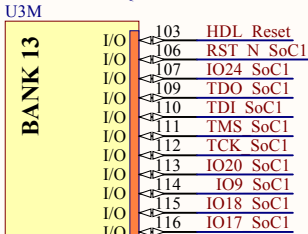
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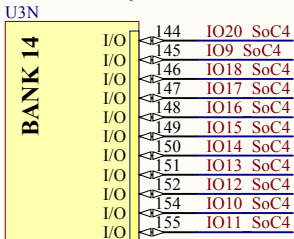
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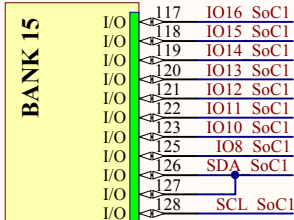
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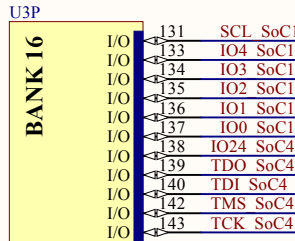
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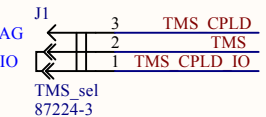
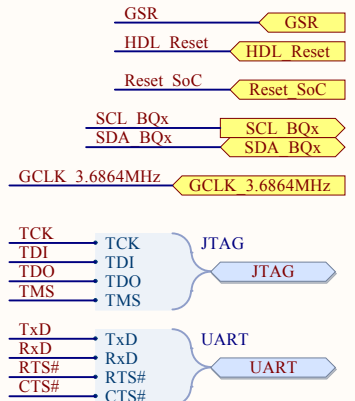
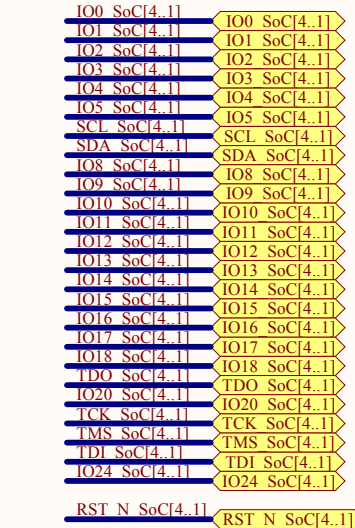
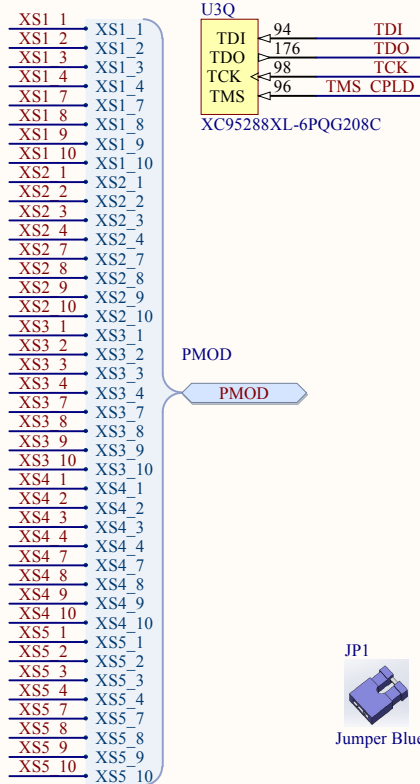
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XC95288XL-6PQG208C



Project: Inside-DGX-XC95			
Title: CPLD.SchDoc		Variant: Inside-DGX-XC95	
Engineer: Sergej Bakhmach	Revision: 1		
Size: A4	Sheet: 3 of 8		
Company: Those Boards			



Project: Inside-DGX-XC95		
Title: CPLD-Power.SchDoc		
Engineer: Sergej Bakhmach	Revision: 1	
Size: A4	Sheet: 5 of 8	
Company: Those Boards		

A

B

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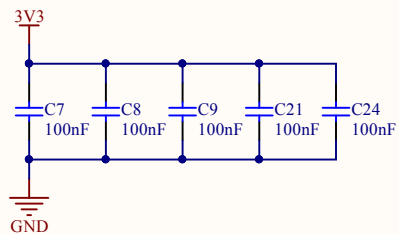
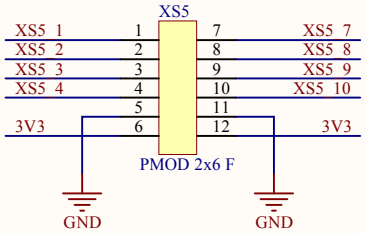
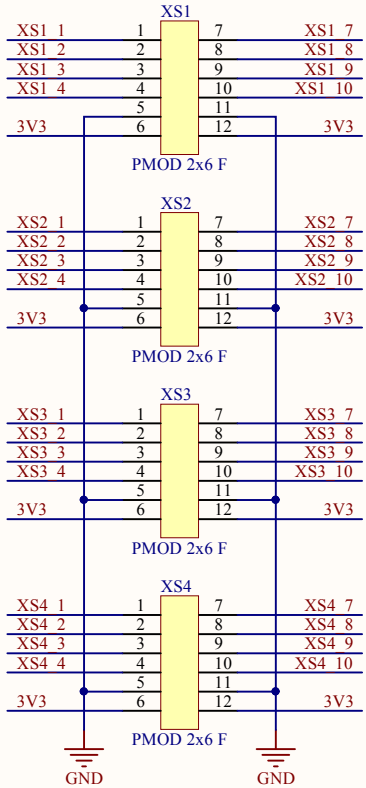
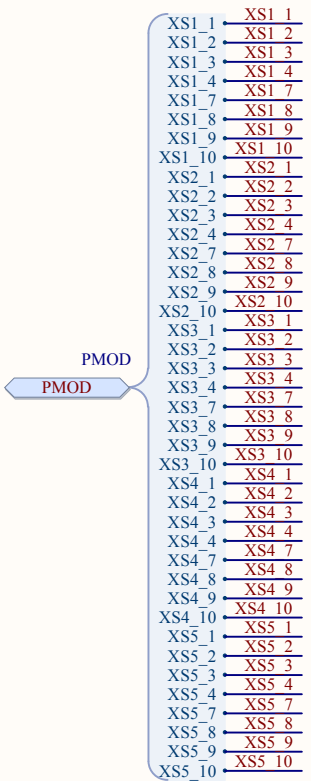
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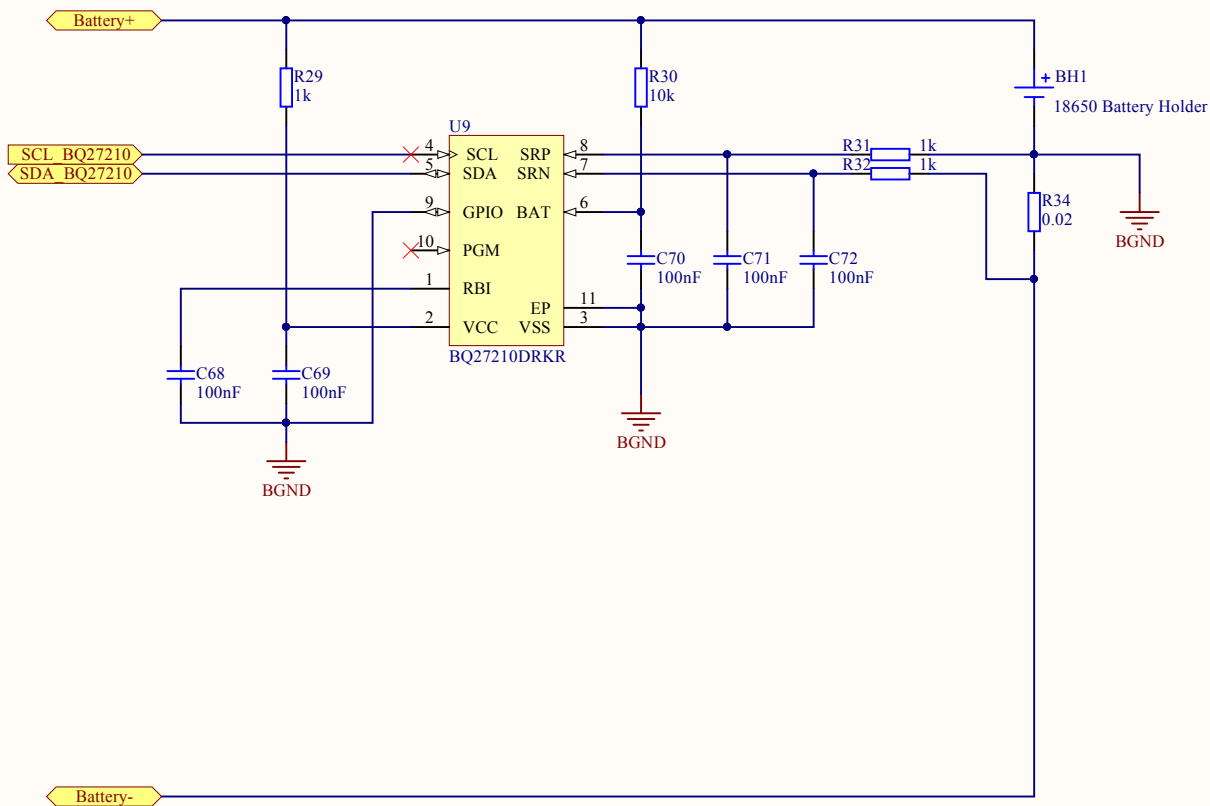
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Project: Inside-DGX-XC95		
Title: PMOD Connectors.SchDoc		Variant: Inside-DGX-XC95
Engineer: Sergej Bakhmach	Revision: 1	
Size: A4	Sheet: 6 of 8	
Company: Those Boards		



Project: Inside-DGX-XC95		
Title: Li-Ion Protect-Control.SchDoc		Variant: Inside-DGX-XC95
Engineer: Sergej Bakhmach	Revision: 1	
Size: A4	Sheet: 8 of 8	
Company: Those Boards		