6502 INSTRUCTIONS

Modes	
Accumulator	
Label	LABEL
Immediate	#$12
Zero Page	$12
Zero Page,X	$12,X
Zero Page,Y	$12,Y
Absolute	$1234
Absolute,X	$1234,X
Absolute,Y	$1234,Y
Indirect	($1234)
Indirect,X	($12,X)
Indirect,Y	($12),Y

ADC NVBDIZC
ADD with Carry
A Lb Im Z Zx Ab Ax Ay In Ix Iy

AND NVBDIZC
bitwise AND with accumulator
A Lb Im Z Zx Ab Ax Ay In Ix Iy

ASL NVBDIZC
Arithmetic Shift Left
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BIT NVBDIZC
test BITs
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BPL NVBDIZC
Branch on PLus
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BMI NVBDIZC
Branch on MInus
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BVC NVBDIZC
Branch on oVerflow Clear
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BVS NVBDIZC
Branch on oVerflow Set
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BCC NVBDIZC
Branch on Carry Clear
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BCS NVBDIZC
Branch on Carry Set
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BNE NVBDIZC
Branch on Not Equal
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BEQ NVBDIZC
Branch on EQual
A Lb Im Z Zx Ab Ax Ay In Ix Iy

BRK NVBDIZC
BReaK
A Lb Im Z Zx Ab Ax Ay In Ix Iy

CMP NVBDIZC
CoMPare accumulator
A Lb Im Z Zx Ab Ax Ay In Ix Iy

CPX NVBDIZC
ComPare X register
A Lb Im Z Zx Ab Ax Ay In Ix Iy

CPY NVBDIZC
ComPare Y register
A Lb Im Z Zx Ab Ax Ay In Ix Iy

DEC NVBDIZC
DECrement memory
A Lb Im Z Zx Ab Ax Ay In Ix Iy

EOR NVBDIZC
bitwise Exclusive OR
A Lb Im Z Zx Ab Ax Ay In Ix Iy

CLC NVBDIZC
CLear Carry
A Lb Im Z Zx Ab Ax Ay In Ix Iy

SEC NVBDIZC
SEt Carry
A Lb Im Z Zx Ab Ax Ay In Ix Iy

CLI NVBDIZC
CLear Interrupt
A Lb Im Z Zx Ab Ax Ay In Ix Iy

SEI NVBDIZC
SEt Interrupt
A Lb Im Z Zx Ab Ax Ay In Ix Iy

CLV NVBDIZC
CLear oVerflow
A Lb Im Z Zx Ab Ax Ay In Ix Iy

CLD NVBDIZC
CLear Decimal
A Lb Im Z Zx Ab Ax Ay In Ix Iy

SED NVBDIZC
SEt Decimal
A Lb Im Z Zx Ab Ax Ay In Ix Iy

INC NVBDIZC
INCrement memory
A Lb Im Z Zx Ab Ax Ay In Ix Iy

JMP NVBDIZC
JuMP
A Lb Im Z Zx Ab Ax Ay In Ix Iy

JSR NVBDIZC
Jump to SubRoutine
A Lb Im Z Zx Ab Ax Ay In Ix Iy

LDA NVBDIZC
LoaD Accumulator
A Lb Im Z Zx Ab Ax Ay In Ix Iy

LDX NVBDIZC
LoaD X register
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

LDY NVBDIZC
LoaD Y register
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

LSR NVBDIZC
Logical Shift Right
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

NOP NVBDIZC
No OPeration
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

ORA NVBDIZC
bitwize OR with Accumulator
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

TAX NVBDIZC
Transfer A to X
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

TXA NVBDIZC
Transfer X to A
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

DEX NVBDIZC
DEcrement X
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

INX NVBDIZC
INcrement X
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

TAY NVBDIZC
Transfer A to Y
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

TYA NVBDIZC
Transfer Y to A
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

DEY NVBDIZC
DEcrement Y
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

INY NVBDIZC
INcrement Y
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

ROL NVBDIZC
ROtate Left
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

ROR NVBDIZC
ROtate Right
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

RTI NVBDIZC
ReTurn from Interrupt
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

RTS NVBDIZC
ReTurn from Subroutine
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

SBC NVBDIZC
SuBtract with Carry
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

STA NVBDIZC
STore Accumulator
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

TXS NVBDIZC
Transfer X to Stack
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

TSX NVBDIZC
Transfer Stack to X
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

PHA NVBDIZC
PusH Accumulator
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

PLA NVBDIZC
PuLl Accumulator
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

PHP NVBDIZC
PusH Processor status
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

PLP NVBDIZC
PuLl Processor status
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

STX NVBDIZC
STore X register
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy

STY NVBDIZC
STore Y register
A Lb Im Z Zx Zy Ab Ax Ay In Ix Iy