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Baseband PCB
Application Notes
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I. Objective

The objective of this paper is familiarize students with KiCad and to act as a reference for future projects in the EEC 134 series. Sometimes written tutorials and guides are hard to follow. I recommend watching videos first, and using written sources as secondary resources. Overall, the goal of this paper is to be a supplement to the videos. The goal is to also go into more detail into the meaning of some particular parts of PCB design which may not be clear or is missing in most other sources.

II. Paper Outline

- a. Introduction to KiCad
- b. Schematic Symbols
- c. CvPCB List
- d. PCB New
- e. Gerber Files

III. Introduction to KiCad

KiCad can be downloaded from <http://kicad-pcb.org/> (if the link expired, Google KiCad). This software works on most operating systems including Windows and OSX for Macs. Upon installing the software and opening the program, you should see a box like this:

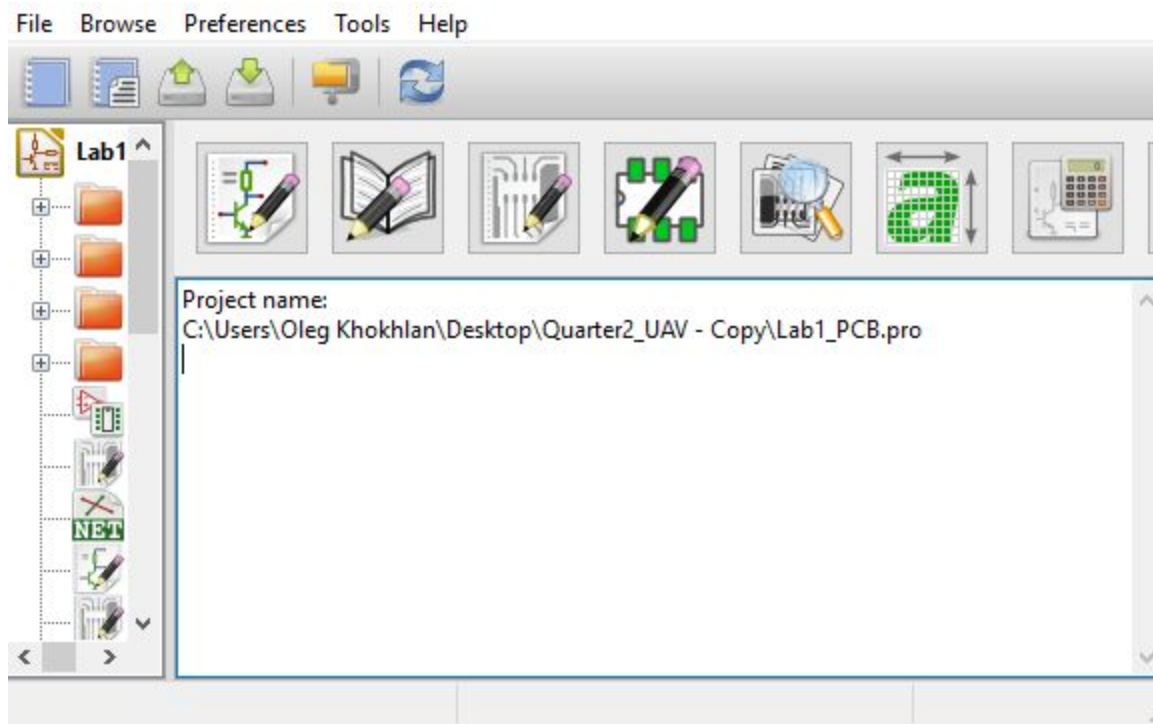


Figure 1

Before anything, you must start a new project. Click on File > New Project. Avoid naming the project with a generic name such as project1 or PCB1. Give it a relevant and a specific name to avoid confusion in the future. After it will ask if you would like to create a new directory, click yes. Note: All files will be saved and should be saved to the project folder.



Figure 2

Introduction to KiCad layout:

1st box - “Eeschema” is an electronic schematic editor. This is the first step of the design process. You essentially build a schematic. Similar to 180A.

2nd box - Schematic library editor. KiCad has default and popular schematic components, however, it does not have everything especially specific components such as unusual ICs that average users will not use. This allows you to manually design schematic components.

3rd box - “Pcbnew” is a printed circuit board editor. After designing the schematic, and assigning footprints, you will finally be able to create the layout for your PCB.

4th box - PCB footprint editor. Same as the schematic library editor. It allows you to build custom footprints for your components which KiCad may not have.

Note: Footprints are physical layouts for components. In your schematics, you will have some component such as a resistor labeled “R8” with a value of say $1k\ \Omega$ which is just a symbol. This does not provide any details on the physical representation of the actual resistor. Just a value and a name. Footprints will consider the physical dimensions of the resistor and the type (thru-hole or SMD). Notice the difference below.

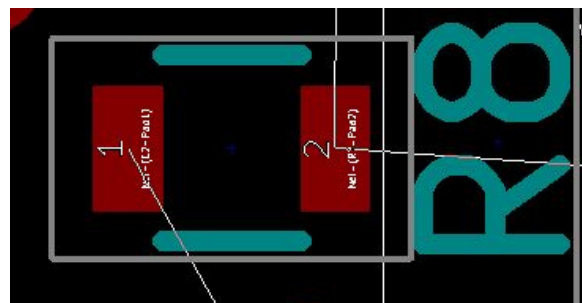


Figure 3 - Schematic R

Figure 4 - Footprint of R (SMD)

Part A - Schematic Tutorial and Tips

As mentioned, the first step is to build a schematic of your design. This schematic should look exactly as the schematic you may have on paper. This schematic acts as a map for the PCB editor.

Clicking on the 1st box will open up a blank page. At the rightmost side of the screen, you will see a lot of tools. We will quickly go over them.

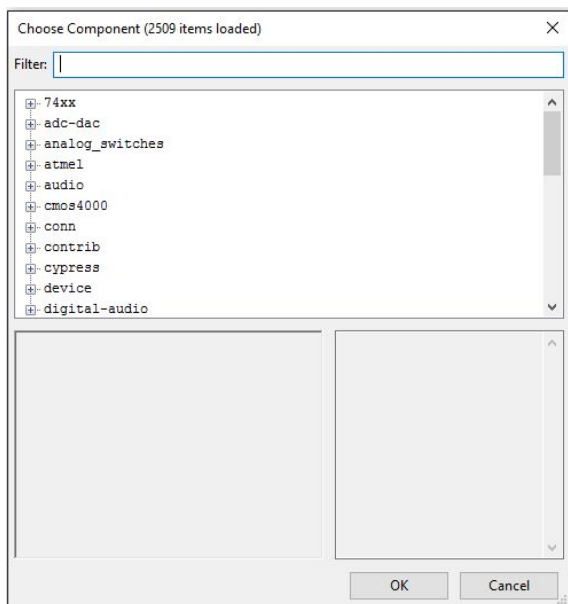
Note: For all of the boxes below follow the same process. After clicking on a box, click on the schematic page for dialogue boxes or wires to show.



Click here to go back to a mouse pointer, and escape from some other tool.



“Place Component” - Click on this box, then once on the schematic page to open a component search box. See below.



This component search box lists all of the schematic symbols within KiCad. If you know the name of the symbol, you can filter search. If not, you can look through the different files.

Use deduction to narrow the search.

Ex. The file adc-dac will have ADC symbols and the file name 74xx will have symbols for ICs which are in the 74xx series.

Tips: Type R for a resistor, and C for a capacitor.



This box is the component search box equivalent for voltage sources (VCC, VDD, 5V, etc...) and ground.



The green wire (top) is for connecting pins as in any schematic.

Tips: Shortcut for the wire is “w”. Double click to end the wire. Clicking once will not end it. It will freeze the wire and maintain its shape from that point allowing you to continue without fear of the wire shifting its position.

The blue wire on the bottom is for a bus, which I have not used, and will not be covered.



This is a “no connect”. It is attached to unconnected pins (pins which are floating).



This is a junction. Basically a giant circular wire connection. Junctions also form by default whenever two wires join.

Note: Two wires crossing (without a junction) is not a connection. It is the software equivalent of jumping over the other wire. Junctions, on the other hand, symbolize connections.

Be careful connecting TO a junction

I had an issue where a wire going through the junction was not actually connected. This resulted in a floating pin which I had to manually jump on the PCB. Always double check your junctions.

The issue is not when two lines meet and when this happens by default. It is when you are trying to connect a wire to an already existing junction.



Sometimes a schematic may contain too many wires. This allows you to label wires to reduce the amount of them you can see.

Ex. Name a node “5V” which is connected to a 5 V source
Then name all other wires which would be connected to the 5V node as “5V”.

Now you know enough to build a schematic.

CvPCB List

After finishing your schematic, it is time to do the CvPCB list which maps footprints to your components. First, click on



(tool bar above schematic) to annotate (number) all components. Adjust the settings if you like. Next, click on



To create a netlist. You must update the netlist anytime you make an update to your schematic. To update, simply click on it again. Now, click on



To open a new window for the CvPCB page. You should get something like this:

The screenshot shows the CvPCB page with a list of components and their values. The list is organized into three columns: Component Name, Value, and Footprint. The components are listed in a table format.

Component Name	Value	Footprint
1 C1 -	1uF	Capacitors_SMD:C_06
2 C2 -	1nF	Capacitors_SMD:C_06
3 C3 -	1nF	Capacitors_SMD:C_06
4 C4 -	1nF	Capacitors_SMD:C_06
5 C5 -	0.1uF	Capacitors_SMD:C_06
6 C6 -	0.33uF	Capacitors_SMD:C_06
7 C7 -	1uF	Capacitors_SMD:C_06
8 C8 -	10uF	Capacitors_SMD:C_06
9 C9 -	1nF	Capacitors_SMD:C_06
10 C10 -	100uF	Capacitors_ThroughH
11 C11 -	1uF	Capacitors_SMD:C_06
12 C12 -	0.1uF	Capacitors_SMD:C_06
13 C13 -	1uF	Capacitors_SMD:C_06
14 C14 -	0.1uF	Capacitors_SMD:C_06
15 C15 -	C	Capacitors_ThroughH
16 C16 -	10uF	Capacitors_SMD:C_06
17 C17 -	1uF	Capacitors_SMD:C_06
18 R1 -	10K	Resistors_SMD:R_060
19 R2 -	220	Resistors_SMD:R_060
20 R3 -	POT 20K	reg:SMD_POT50k
21 R4 -	8.45K	Resistors_SMD:R_060
22 R5 -	102K	Resistors_SMD:R_060
23 R6 -	7.15K	Resistors_SMD:R_060

Description: Neosid, Air-Coil, SML, 1turn, HDM0131A, Key words: Neosid Air-Coil SML 1turn HDM0131A, Filtered by library: 12

On the left, you have a selection of different components. In the middle are all of your components. On the right are the different varieties of the component you selected on the right.

For example, first click on one of your components (middle column). Say C1. Now find the Capacitor selection of the left column. Click on that. On the right column you should see different sizes for capacitors. Select the one you need. You can view footprints by clicking on



When the desired component is selected.

Remember to save. Now, it is time to go back and update the netlist.

PCB New

To open, select the 3rd box on the main KiCad window. You should get a blank window. Click on



Select “Read Current Netlist”. Afterwards you get a pile of components stacked onto each other. At this point, you have to manually separate the individual components manually. As you are separating them, spread them out first. Choose the most important components, and place them where you need them. Then begin placing smaller components around them.

To connect the components, use



To lay a trace for the PCB. Keep the trace width in mind. That can be accessed in Design Rules > Design Rules. Then change the Track Width.

The trace/track can be either on top or the bottom of the board. Red means F. Cu which is front copper. Green is B.Cu which is back copper. They may connect through a via which is selected by clicking on “v” on the keyboard while laying out the trace.

Tips: Avoid 90 degree angles for fabrication related reason.

To do edges, select the Edge.Cuts at the right of the screen. Then click on



To draw the line of the screen.

Instead of using trace for GND, fill the PCB with copper which is grounded. Use



To draw the trace (should be just inside the PCB outline). Do it for front and back. Then pour copper in. In settings, make sure that the copper selected is GND.

Once you place all components, think about the PCB size. You can measure the distance between edges by taking note of the x y coordinates at the bottom of the screen.

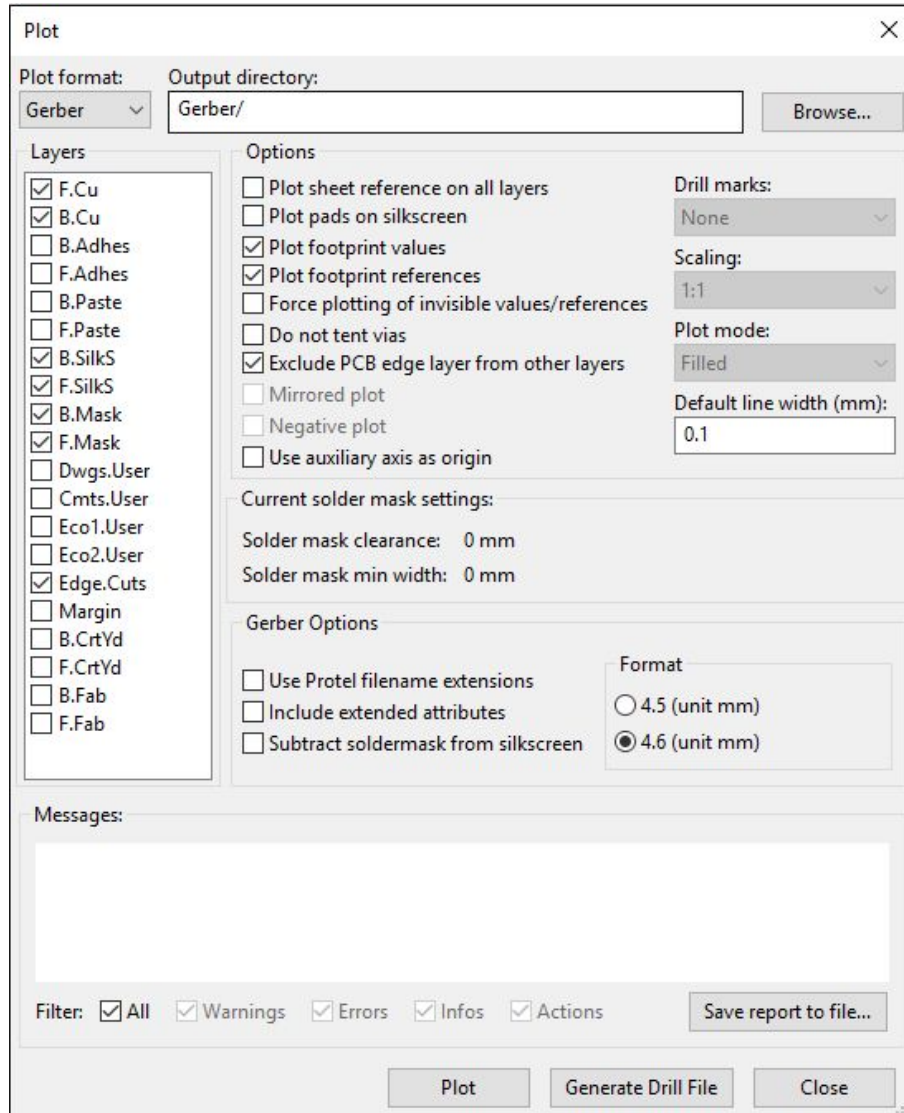
Gerber Files

These files contain vital information concerning PCB manufacturing. To plot these files, select

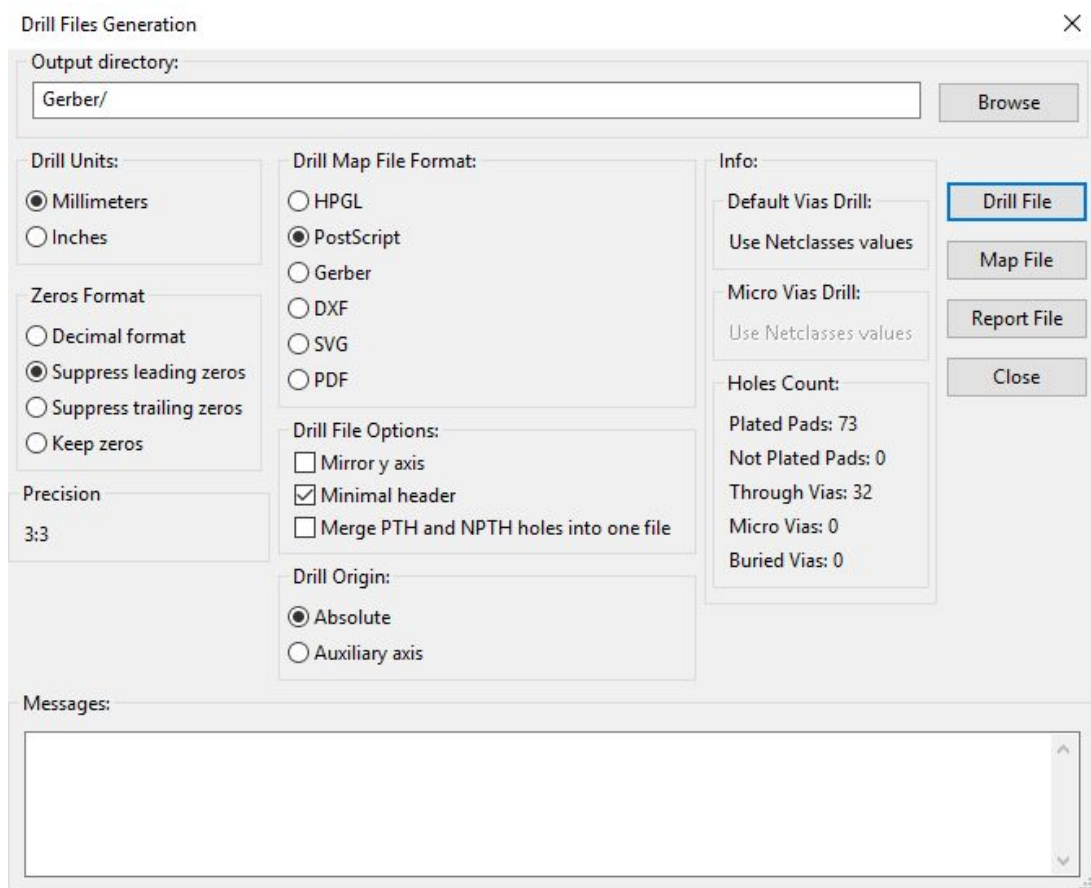


Before anything else, make sure to set the directory to a new folder named Gerber inside of your workspace.

At a minimum, make sure the boxes on the left are selected as following:



You must also click on Generate Drill File.



Click on Drill File to complete this step, then finally click on Plot to finish the Gerber files.

Now you have a complete set of Gerber files.

Conclusion

By now you should have a good understanding of KiCad, and should be able to complete the task at hand. Some more tips on getting a successful design: have others review your design. They will be able to spot mistakes that you always skip. Always try Googling an issue if it is not covered in this tutorial or some other video. If there are mistakes in this tutorial, or you would like to contribute, feel free to take this document and add to it.