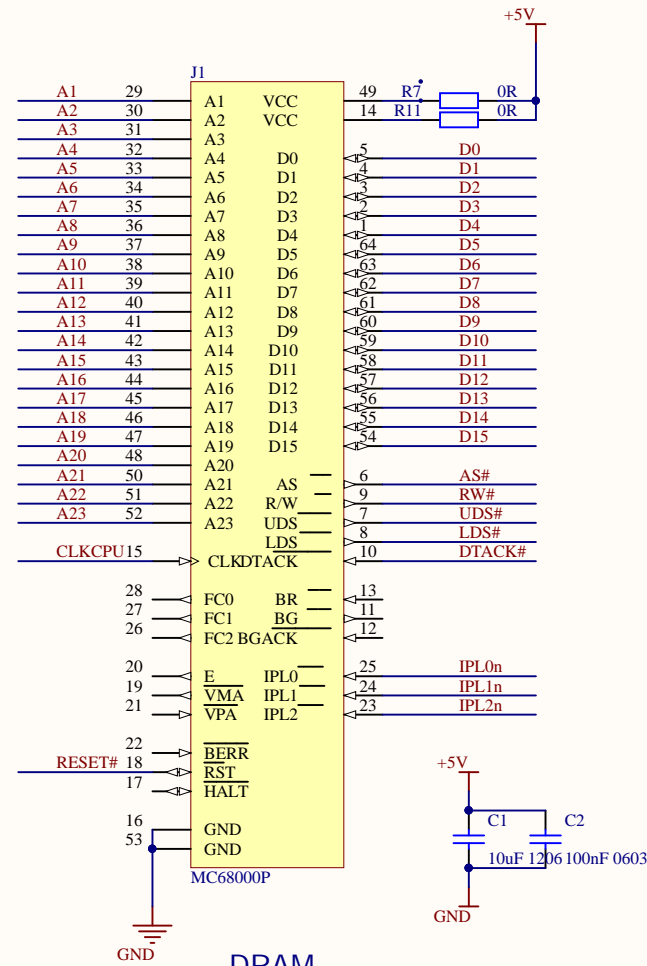
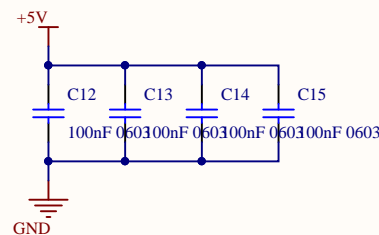
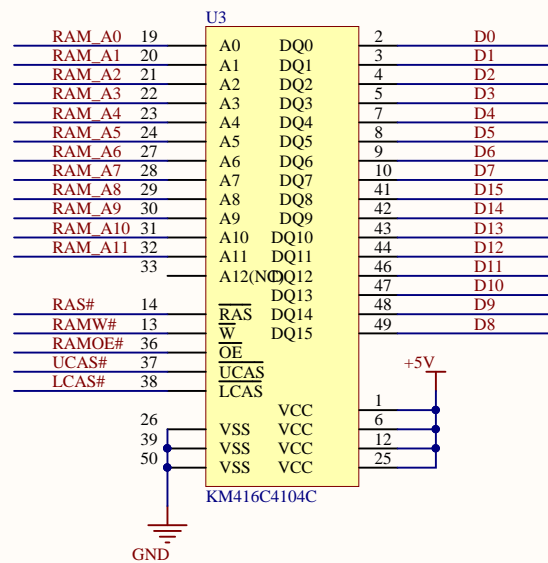


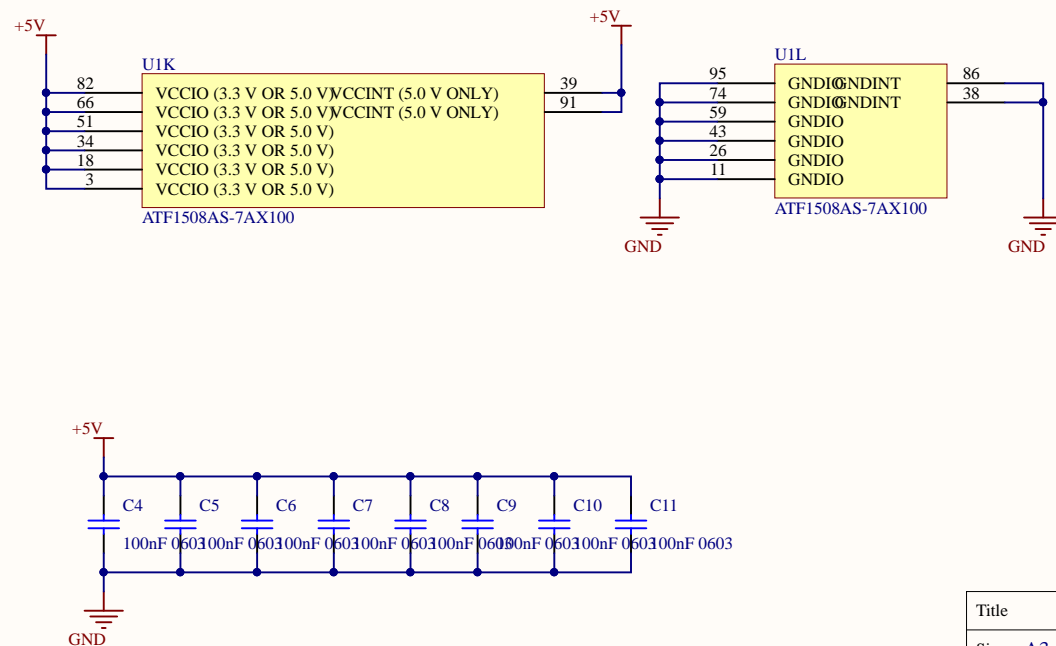
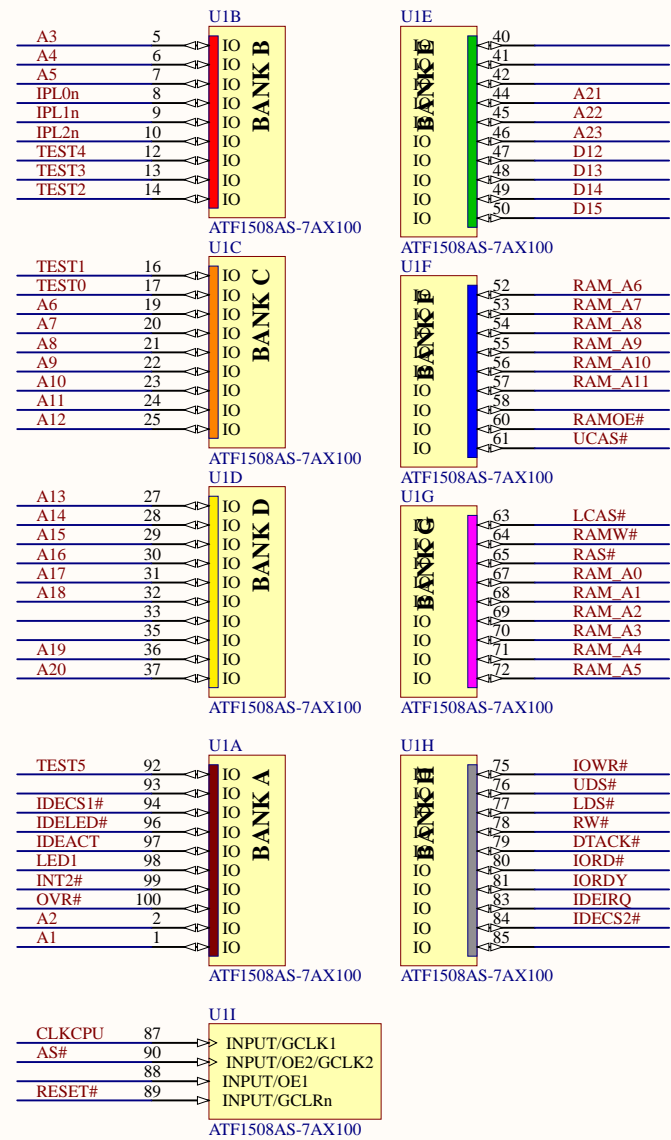
CPU socket



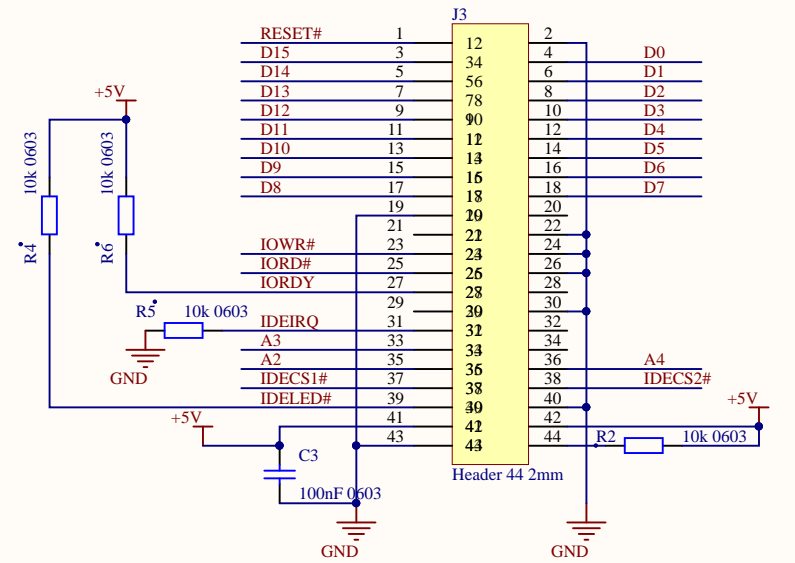
DRAM



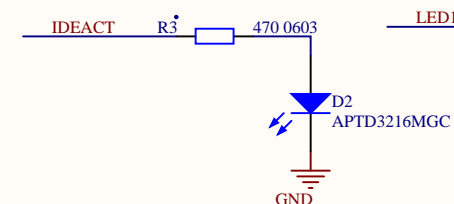
CPLD



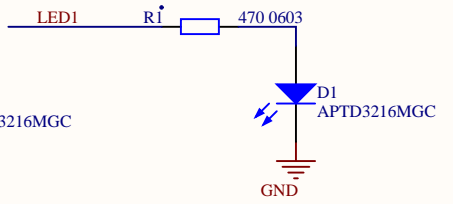
IDE connector



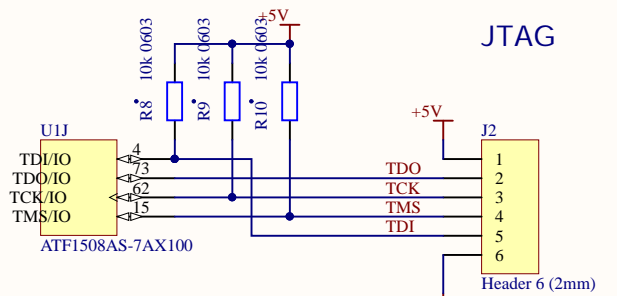
IDE Act.



Configured



JTAG



Test

