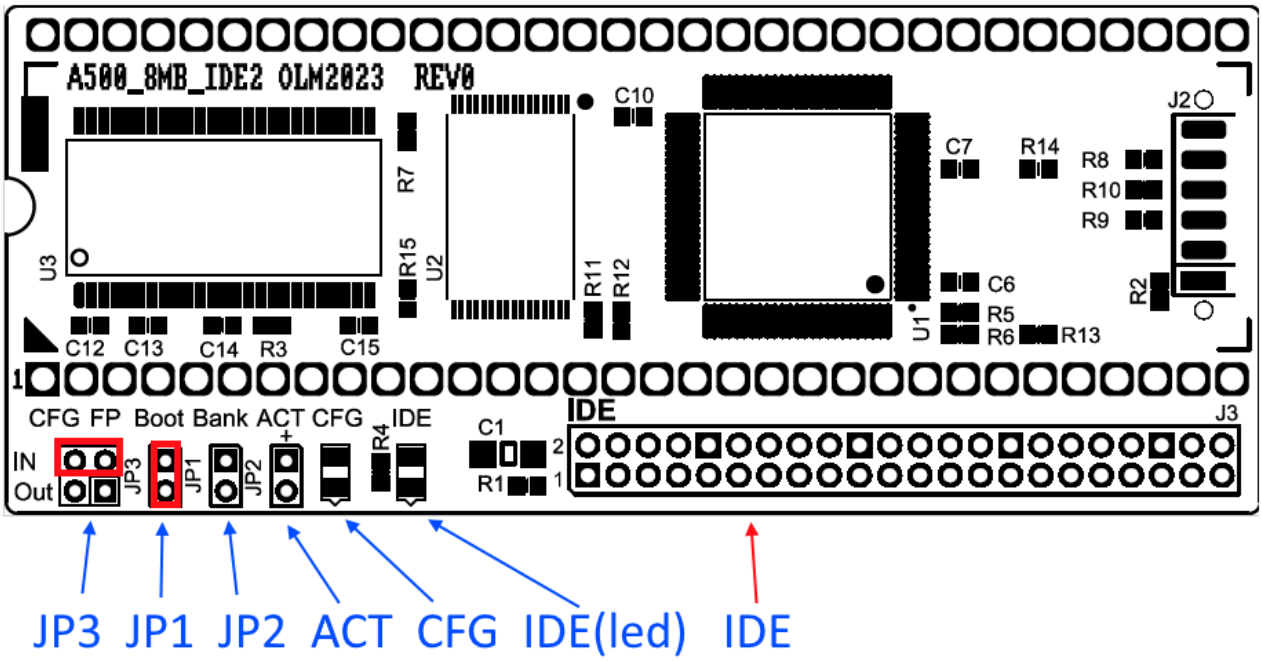
A500_8MB_ide2 – 8MB FastRAM and IDE controller expansion board for Amiga 500.

Quick start guide.

1. Connectors and indicators.

Connectors and indicators are shown below. Default jumper configuration is marked by RED color.



JP1 – Boot. Boot enable from IDE when jumper installed.

JP2 – Flash bank select. Not used in current revision.

JP3 – CONFIG IN and OUT, Flash Program. Set jumper between pins 3 and 4 to enable the board. Can be connected to CONFIG OUT of other AUTOCONFIG board.

Function	Pin		Function
CONFIG IN	4	3	GND
CONFIG OUT	2	1	Flash Program

ACT – IDE activity external LED

IDE – 44 pin IDE connector

2. Installation procedure

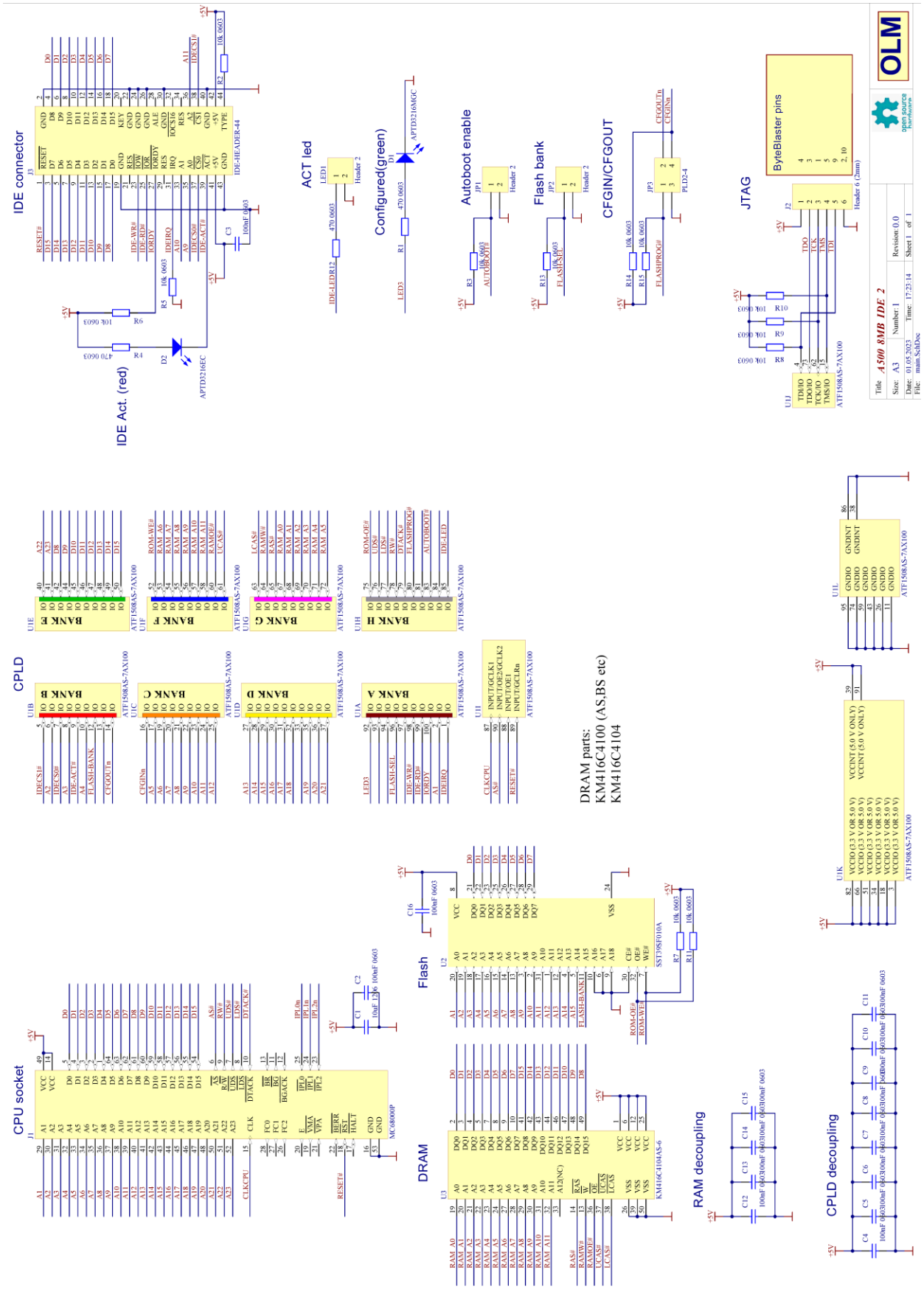
2.1. Remove original CPU from Amiga board

2.2. Install removed CPU to the A500_8MB_ide2 board

2.3. Install the board into Amiga's CPU socket.

2.4. Connect IDE hard drive and if needed an external LED to ACT connector.

3. **Circuit diagram.**



4. Bill of materials

N	Quantity	Ref. designator	Part	Comment
1	1	J3	PLD2-44	IDE-HEADER-44
2	1	C1	10uF 1206	Ceramic capacitor SMD
3	15	C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15, C16	100nF 0603	Ceramic capacitor SMD
4	1	D1	APTD3216MGC	LED Green 0805
5	1	D2	APTD3216EC	LED Red 0805
6	1	J1	Socket 1-pin x 64	LED socket-round pin
7	3	JP1, JP2, LED1	PLS-2	Header, 2-Pin, 2.54mm pitch
8	1	JP3	PLD2-4	Header, 2-Pin, Dual row
9	3	R1, R4, R12	470 0603	Resistor SMD
10	12	R2, R3, R5, R6, R7, R8, R9, R10, R11, R13, R14, R15	10k 0603	Resistor SMD
11	1	U1	ATF1508AS-7AX100 EPM7128SCT100-10N	128 macrocells CPLD
12	1	U2	SST39SF010A	4Mb FLASH MEMORY
13	1	U3	KM416C4104AS-6	4Mx16 EDO DRAM TSOP-II-50