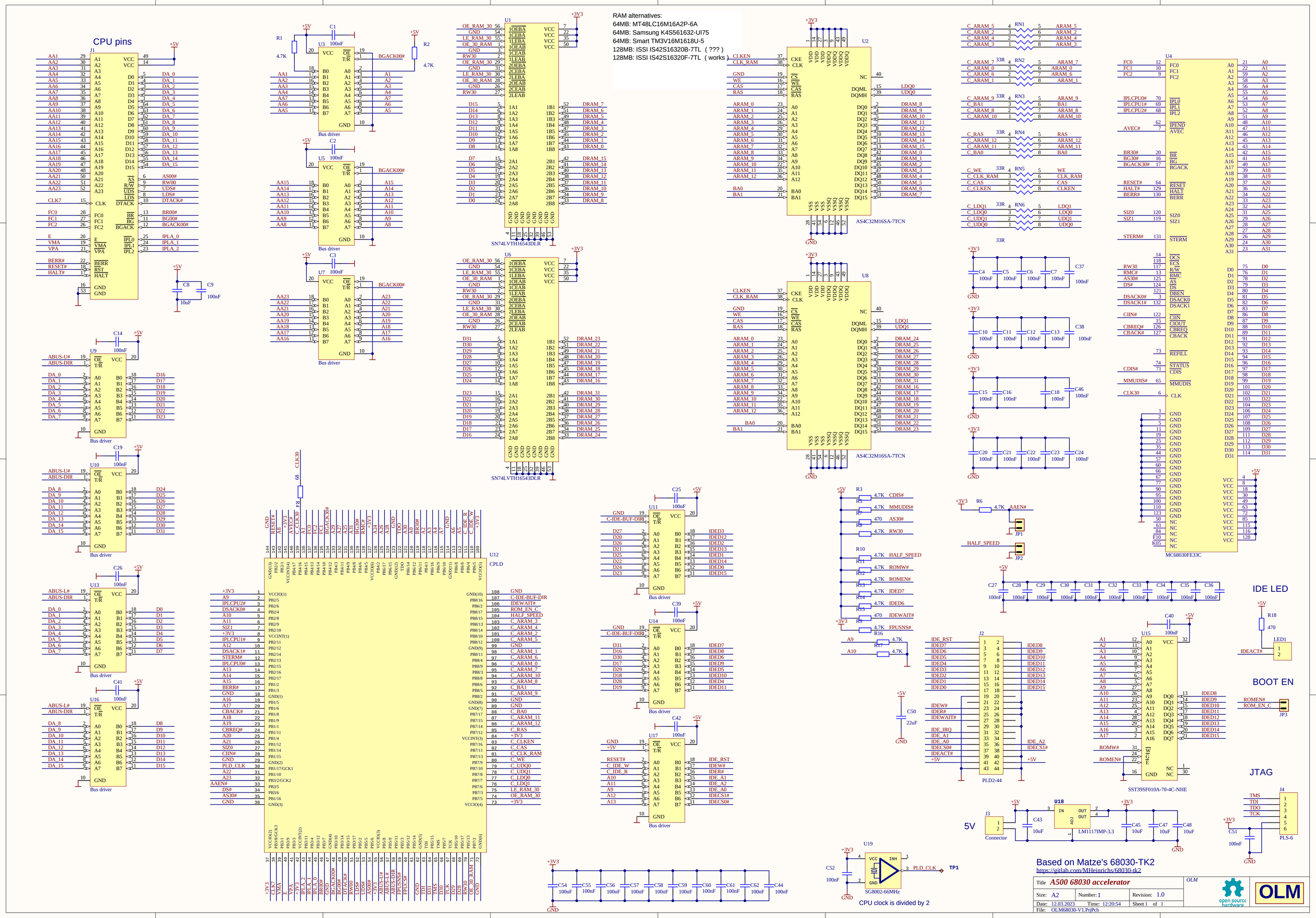
RAM alternatives:
64MB: MT48LC16M16A2P-6A
64MB: Samsung K4S561632-UI75
64MB: Smart TM3V16M1618U-5
128MB: ISSI IS42S16320B-7TL (???)
128MB: ISSI IS42S16320F-7TL (works)
CPU pins
J1
U1
U2
U3
U4
U5
U6
U7
U8
U9
U10
U11
U12
U13
U14
U15
U16
U17
U18
U19
J2
J3
J4
R1
R2
R3
R6
R16
R18
C1
C2
C3
C8
C9
C14
C19
C25
C26
C39
C41
C42
C50
C52
C54
C55
C56
C57
C58
C59
C60
C61
C62
C44
C27
C28
C29
C30
C31
C32
C33
C34
C35
C36
C37
C38
C46
JP1
JP2
JP3
LED1
IDE LED
BOOT EN
JTAG
5V
Connector
LM1117IMP-3.3
SG8002-66MHz
CPU clock is divided by 2
Bus driver
SN74LVTH16543DLR
AS4C32M16SA-7TCN
MC68030FE33C
CPLD
PLD2-44
SST39SF010A-70-4C-NHE
PLS-6
Based on Matze's 68030-TK2
https://gitlab.com/MHeinrichs/68030-tk2
Title A500 68030 accelerator
Size: A2
Number: 1
Revision: 1.0
Date: 12.03.2023
Time: 12:20:54
Sheet 1 of 1
File: OLM68030-V1.PrjPcb
OLM
open source hardware