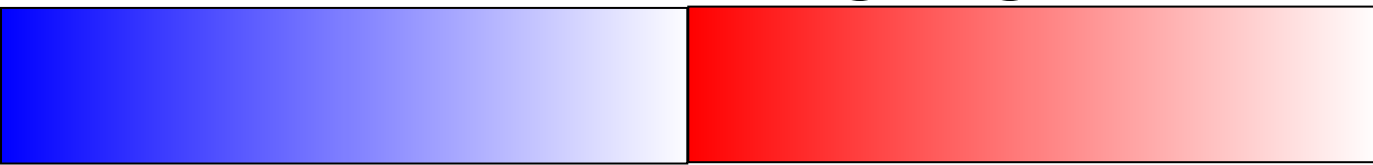
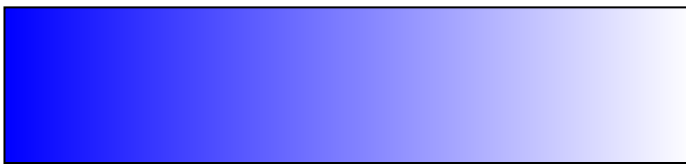


Eingang

Ausgang

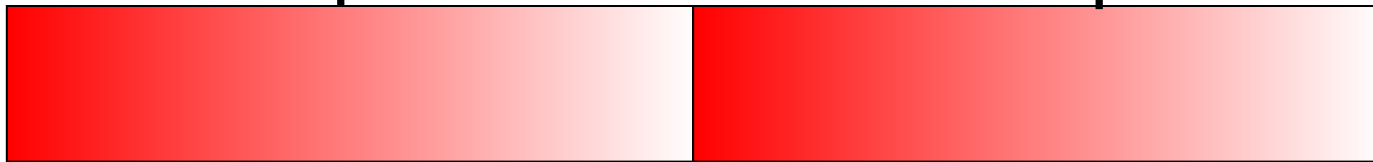


Adresse



Linker Operand

Rechter Operand



Schreiben



Lesen



Operation



16 Speicherblöcke

[illegible]