Module No.	Unit No.	Topics	Hrs.
1.0		**Fundamentals of MOS Amplifiers**	**10**
	1.1	MOS Single-stage Amplifiers: Basic concepts of common source stage, source follower, common gate stage, Differential Amplifiers:	
	1.2	Current mirrors: Basic current mirror, cascode current mirror, active current mirror, Wilson and Widlar current mirrors, voltage and current references.	
2.0		**Design of MOS operational amplifier**	**08**
	2.1	General considerations, One-Stage Op amps, Two-Stage Op amps, Gain Boosting, Input Range Limitation.	
	2.2	Frequency Response and Compensation, Slew Rate.	
3.0		**Oscillators and Phase Locked Loops**	**08**
	3.1	General Considerations, Ring Oscillators, LC Oscillators, Voltage Controlled Oscillators (VCO), tuning range, tuning linearity Mathematical Model of VCO.	
	3.2	Simple PLL-phase detector, Charge-pump PLL's, Non ideal effects in PLL, Delay locked Loops, applications of PLL.	
4.0		**Switched Capacitor circuits**	**06**
	4.1	Theory of sampled data systems, Basic sampling circuits for analog signal sampling, performance metrics of sampling circuits, design and analysis of switched capacitor circuits.	
	4.2	Switched capacitor amplifiers (SC), switched capacitor integrators, first and second order switched capacitor circuits.	
5.0		**Data converters**	**06**
	5.1	Analog versus digital discrete time signals, converting analog signals to data signals, sample and hold characteristics. DAC specifications, ADC specifications.	
	5.2	Mixed signal Layout issues, Floor planning, power supply and Ground issues, other interconnect Considerations.	
6.0		**Data Converter Architectures**	**10**
	6.1	DAC architectures: R-2R ladder networks, current steering, charge scaling DACs, Cyclic DAC, pipeline DAC, Switched capacitor based DAC design.	
	6.2	ADC architectures: flash, 2-step flash ADC, pipeline ADC, integrating ADC, and successive approximation ADC, Switched capacitor based ADC design	
		Total	**48**